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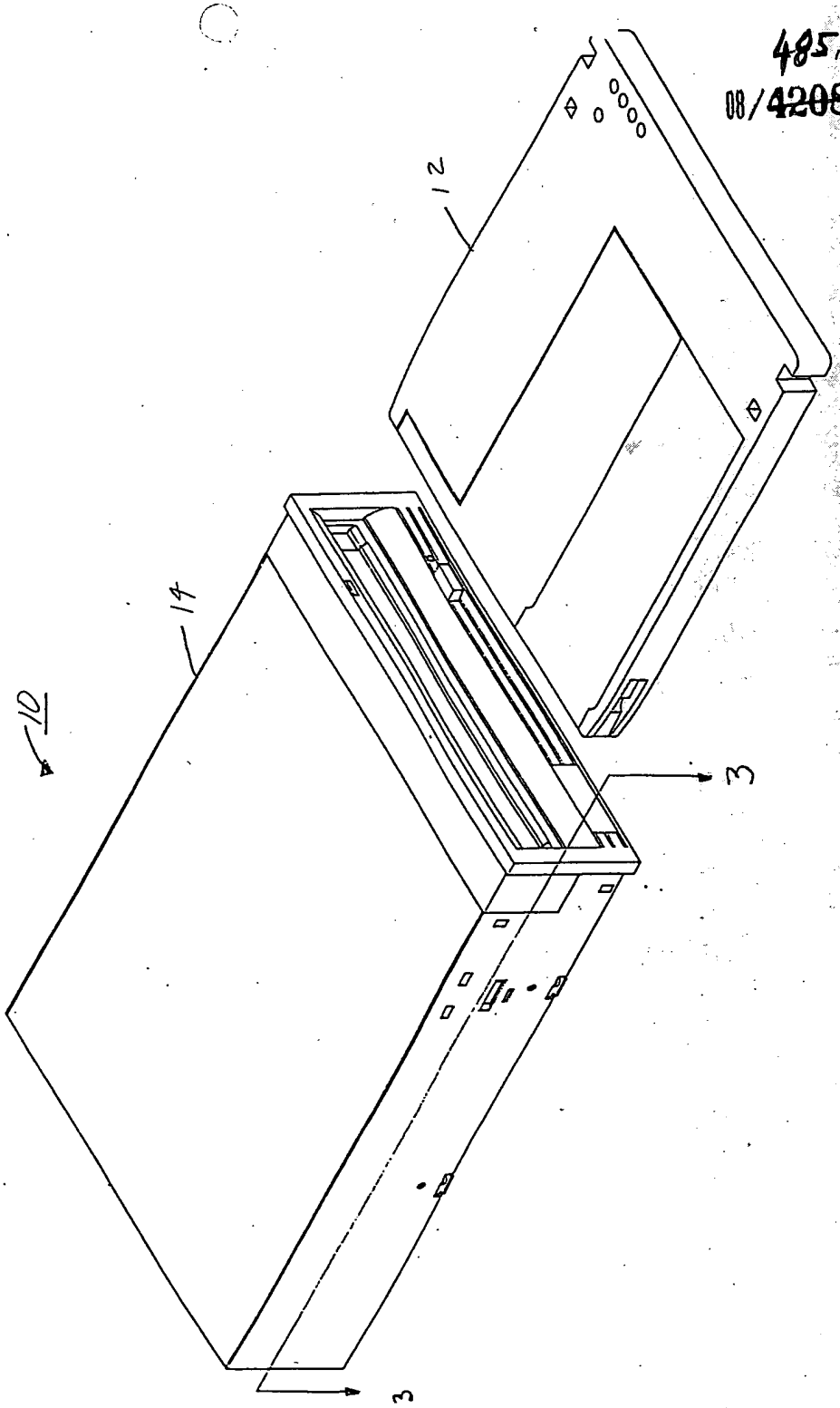

FIG. 1

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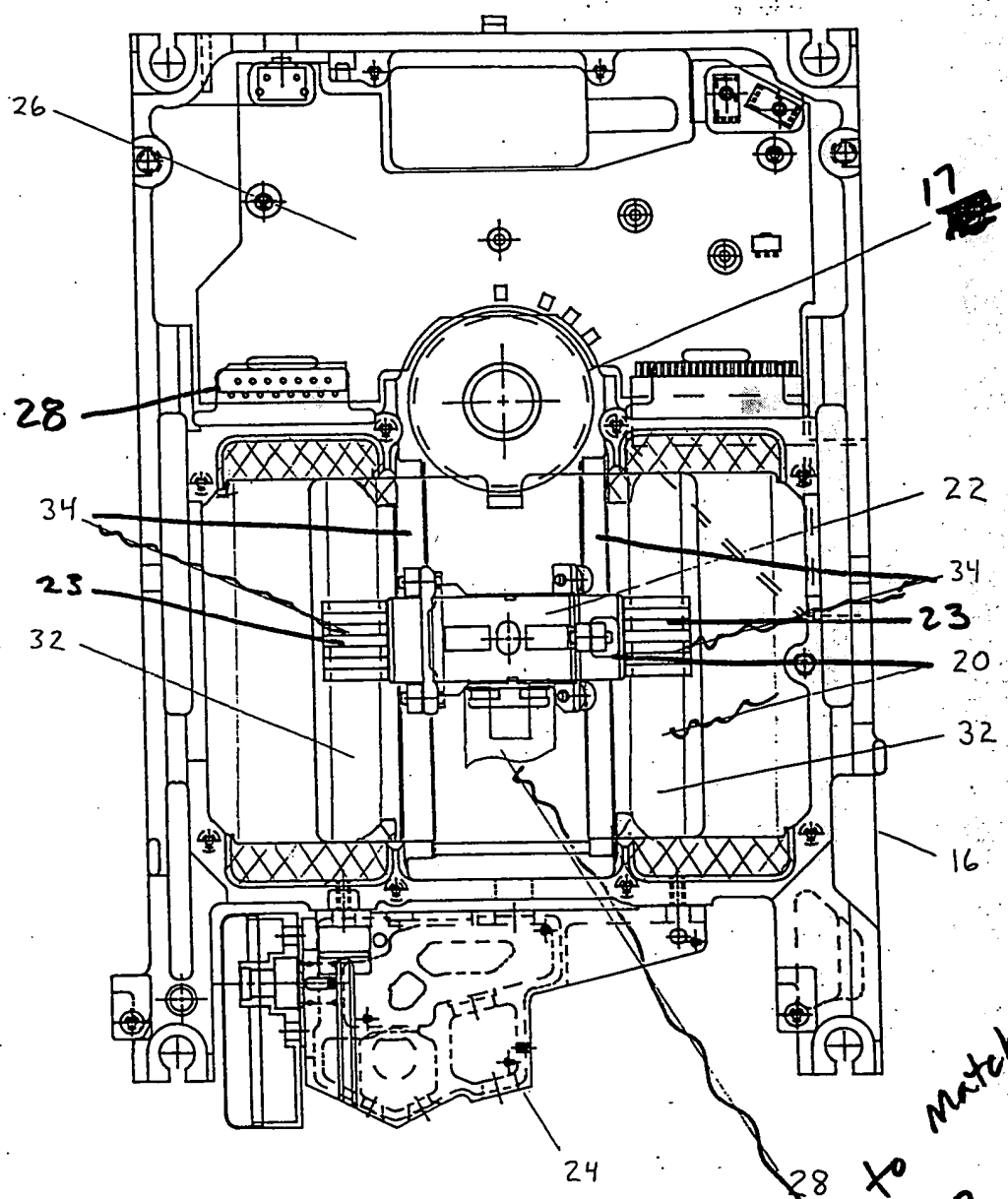

FIG. 2

Re-do to match Page 43

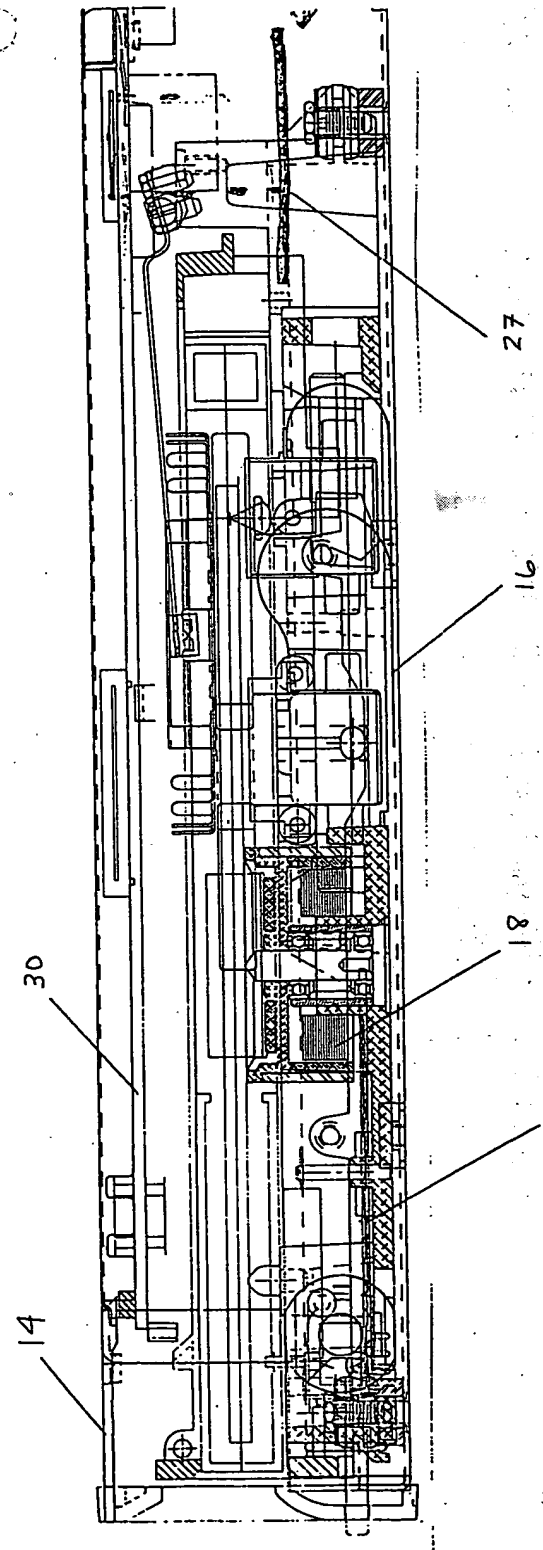

FIGURE 3

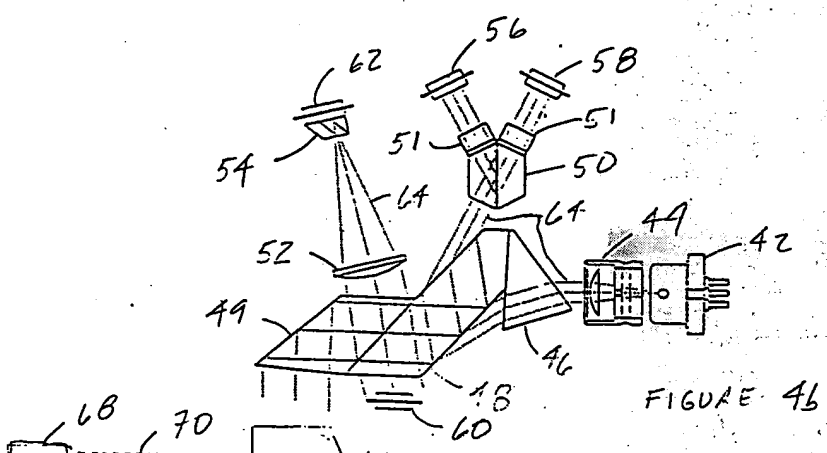

FIGURE 4b

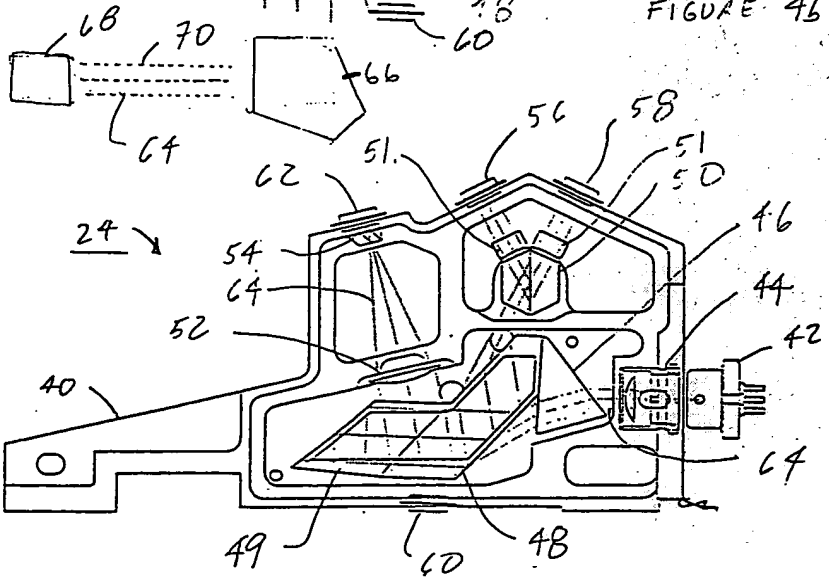

FIGURE 4a

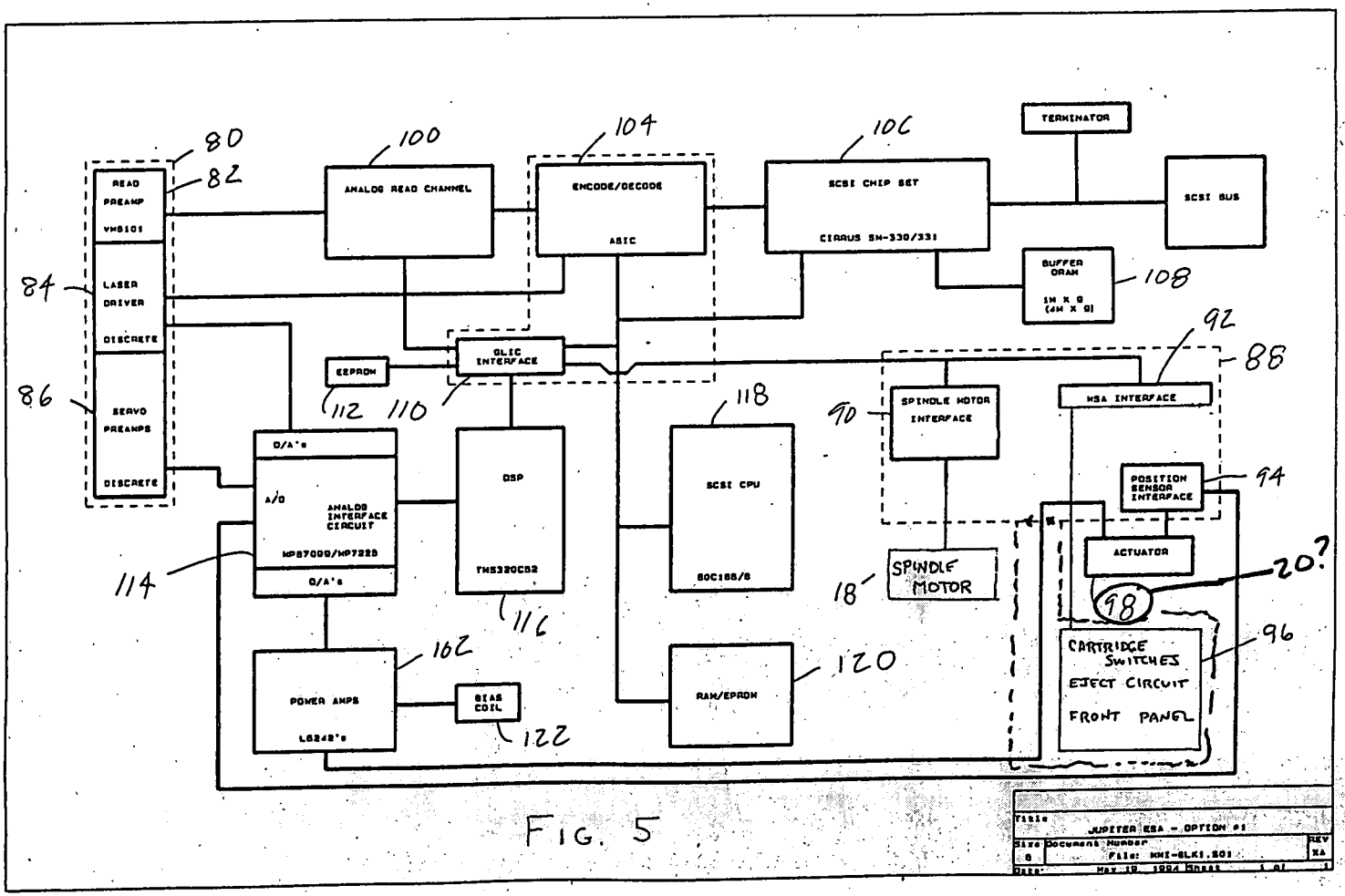

FIG. 5

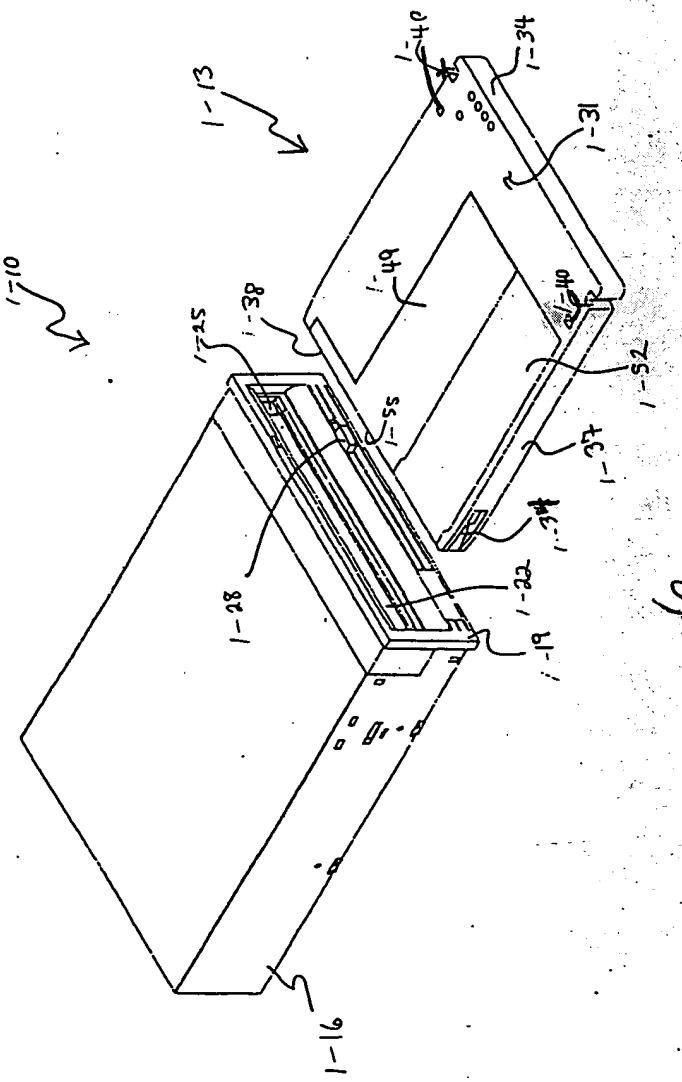
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FIG. 6

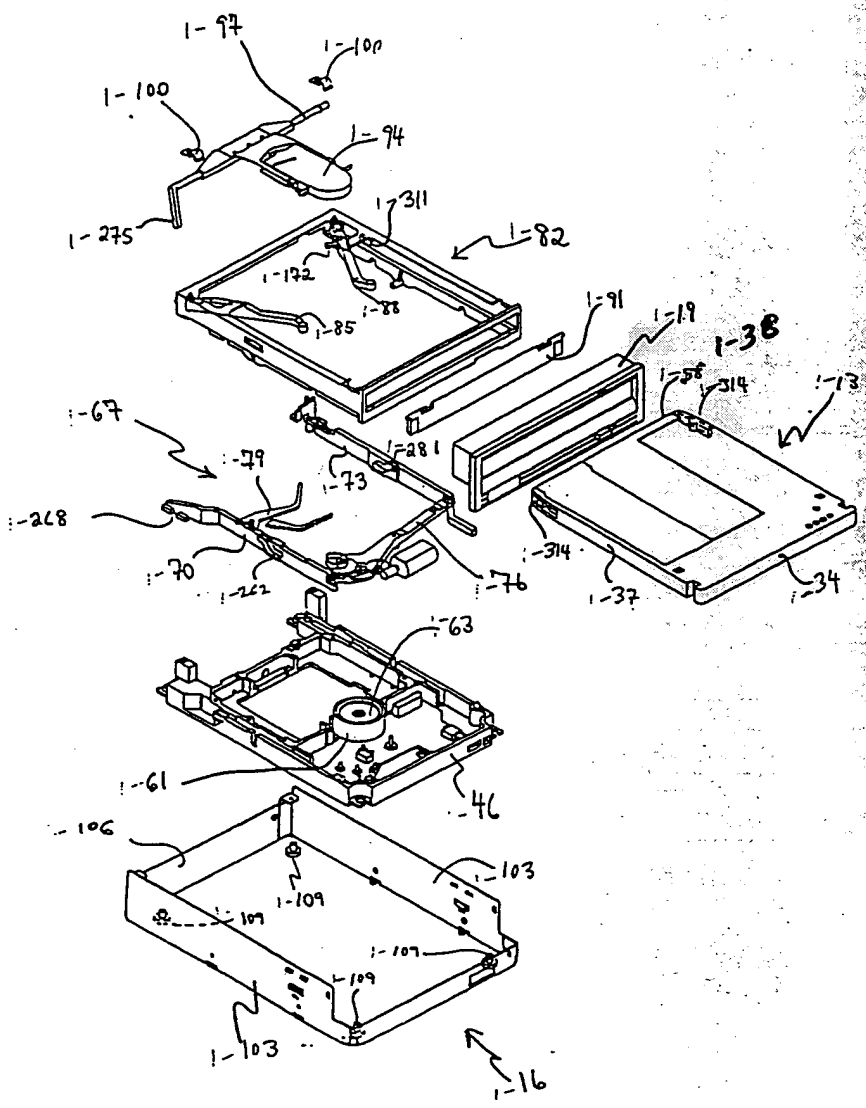
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FIG. 7

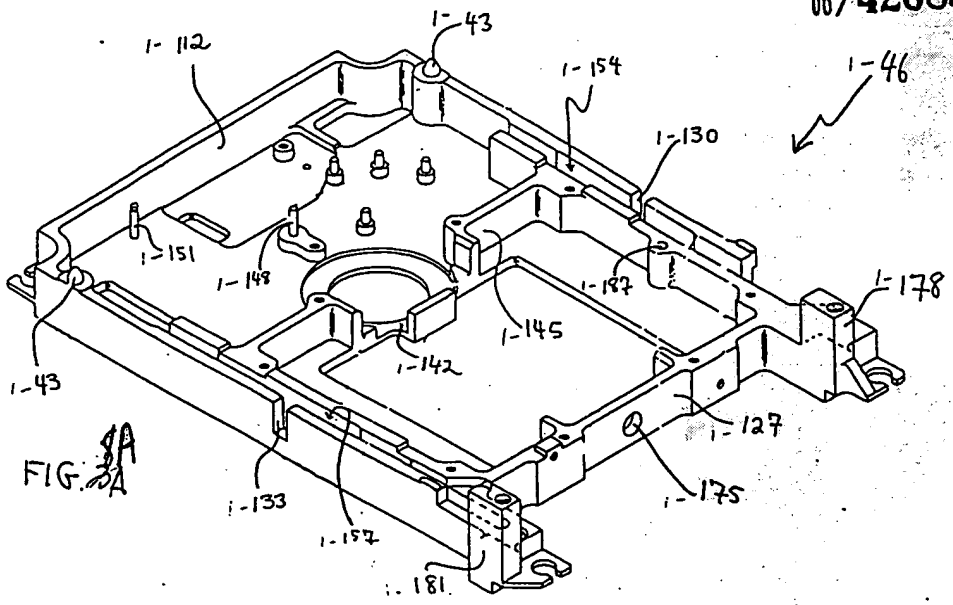
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FIG. 8A

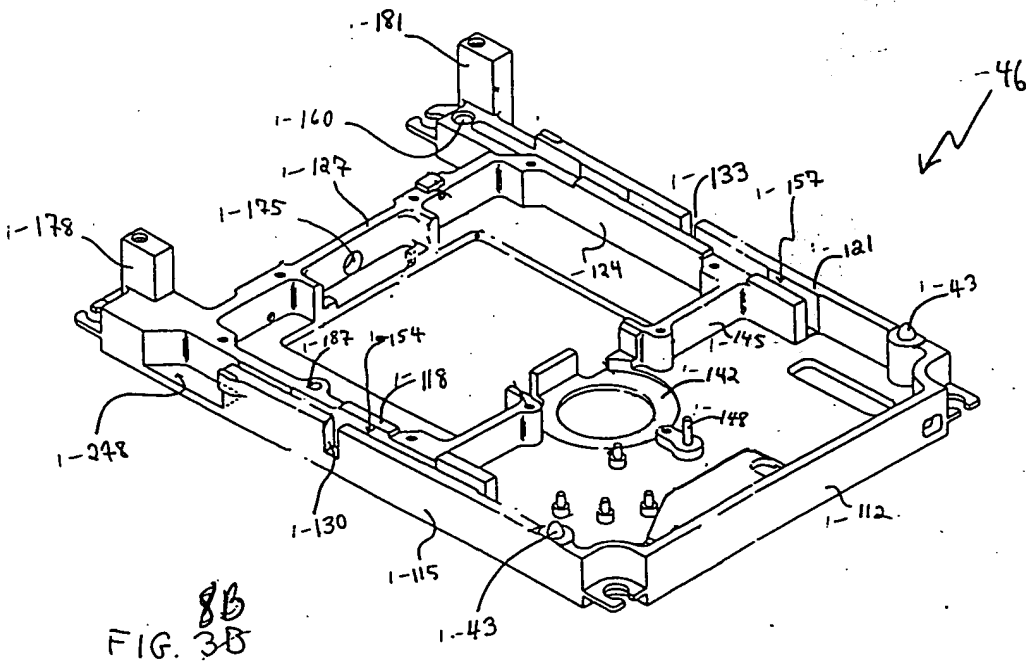
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FIG. 8B

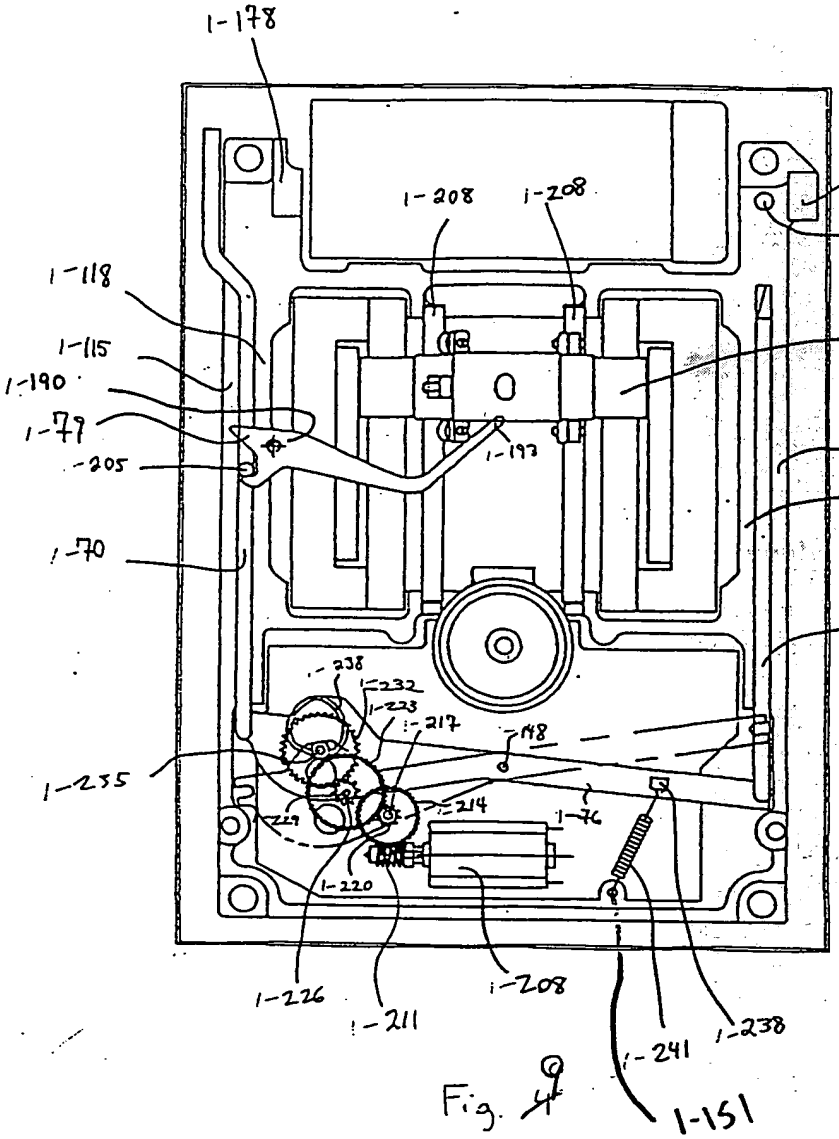
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Fig. 4

FIG. 10A (5A)

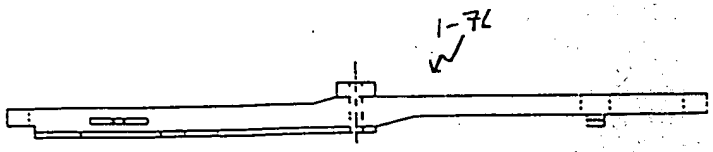

FIG. 10B (5B)

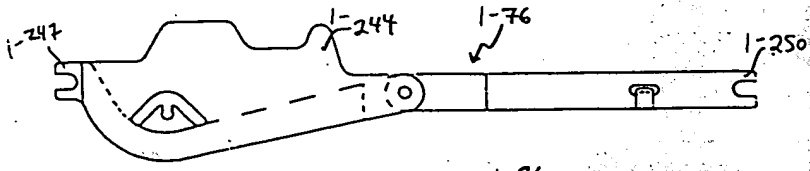

FIG. 10C (5C)

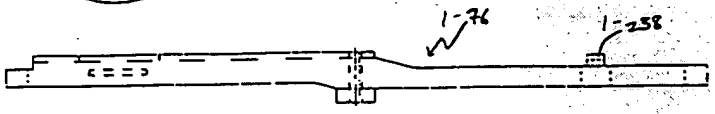

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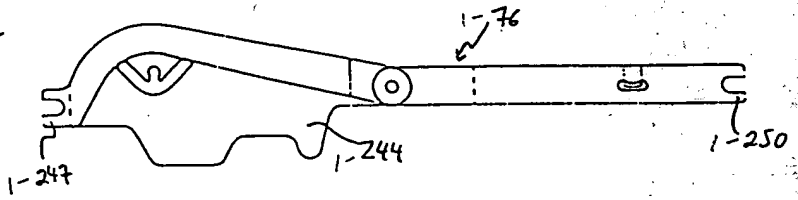
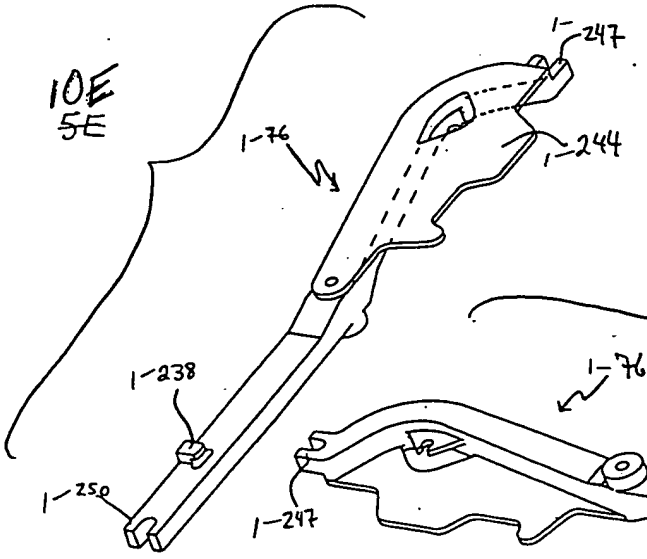
FIG. 10D (5D)



10E (5E)



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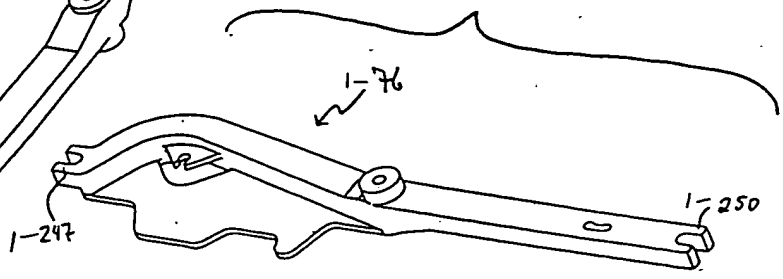


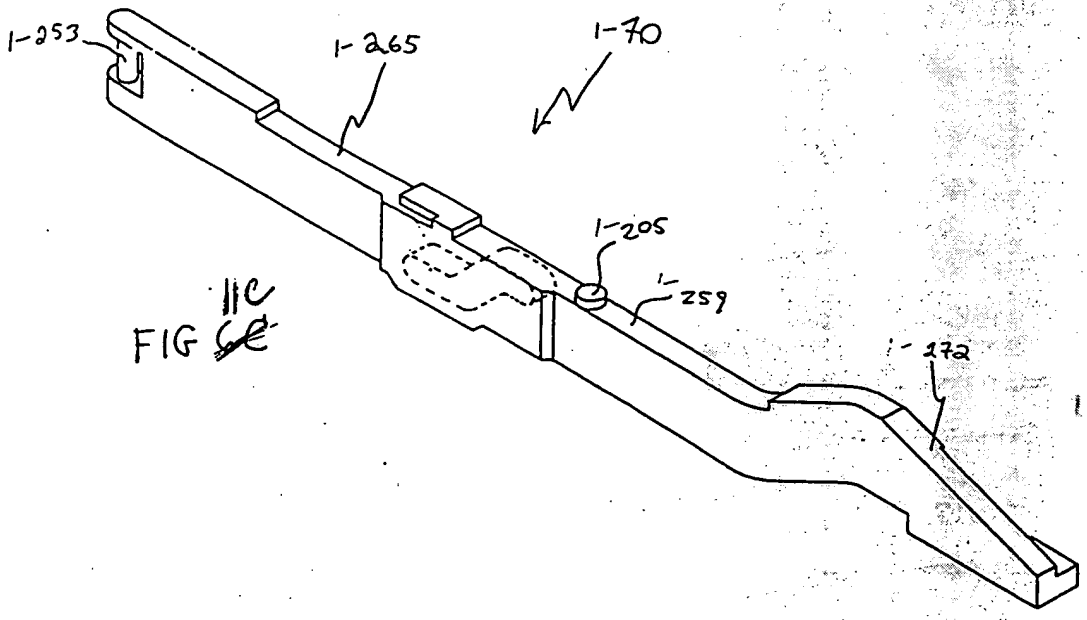


FIG 11C

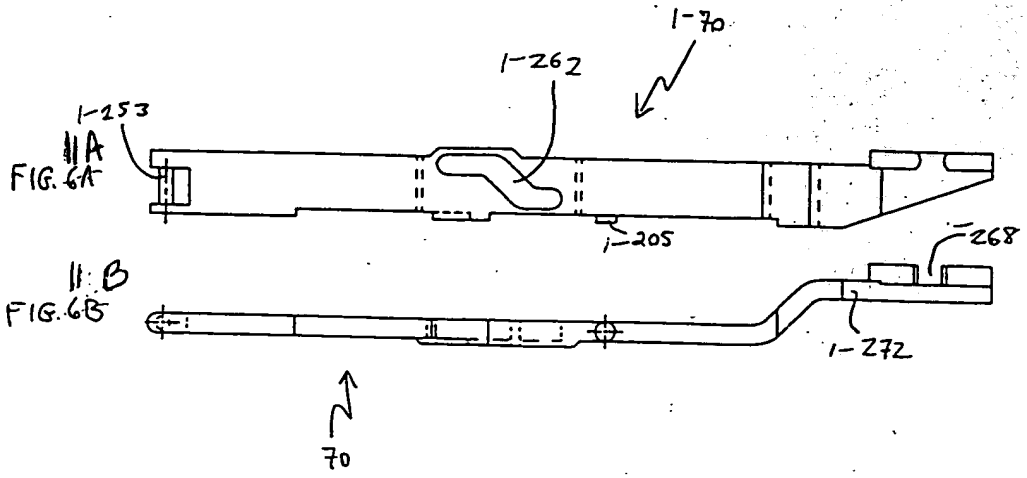
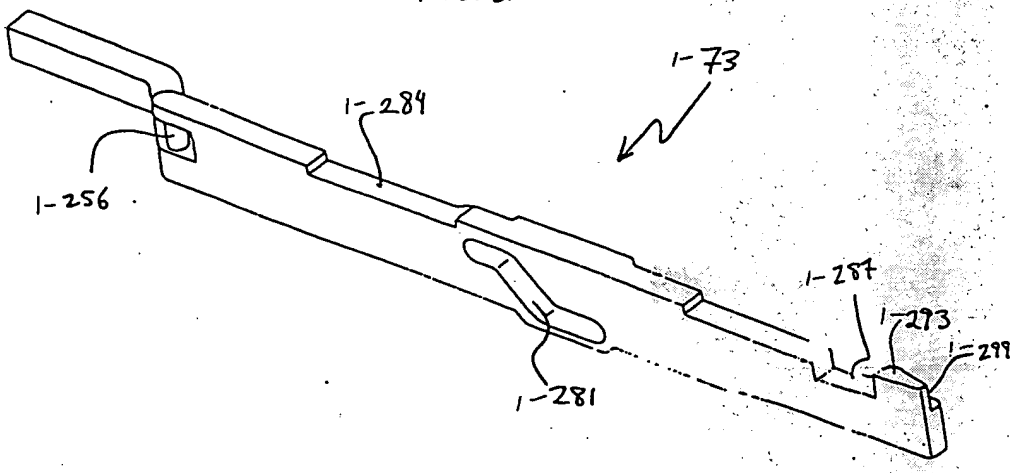


FIG. 6A 11A

FIG. 6B 11B

FIG. ~~7E~~ 12E



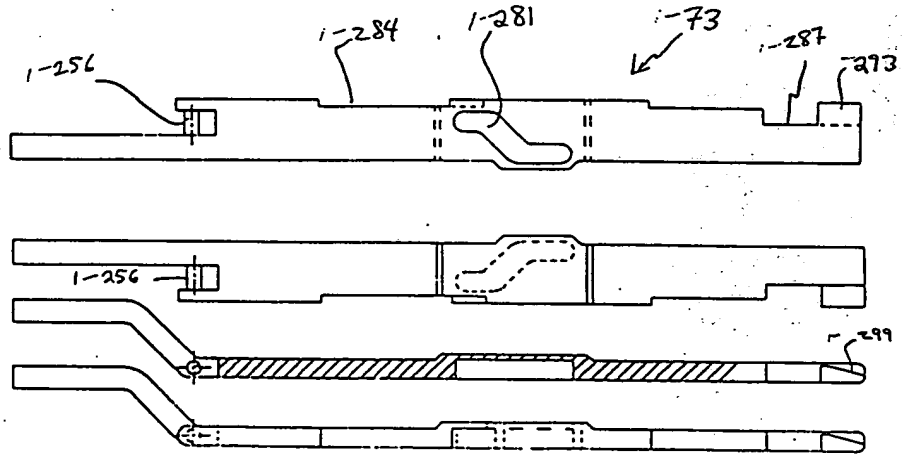


FIG. ~~7A~~ 12A

FIG. ~~7B~~ 12B

FIG. ~~7C~~ 12C

FIG. ~~7D~~ 12D

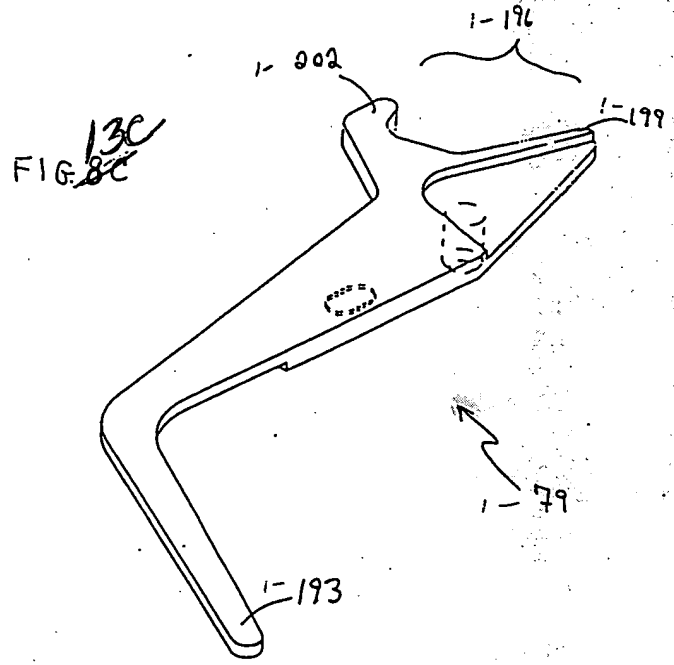


FIG. 13C

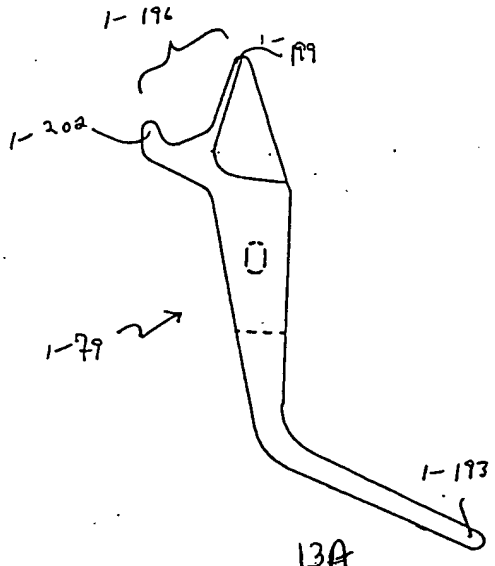


FIG. 13A

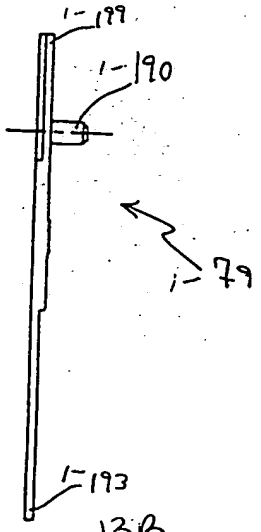


FIG. 13B

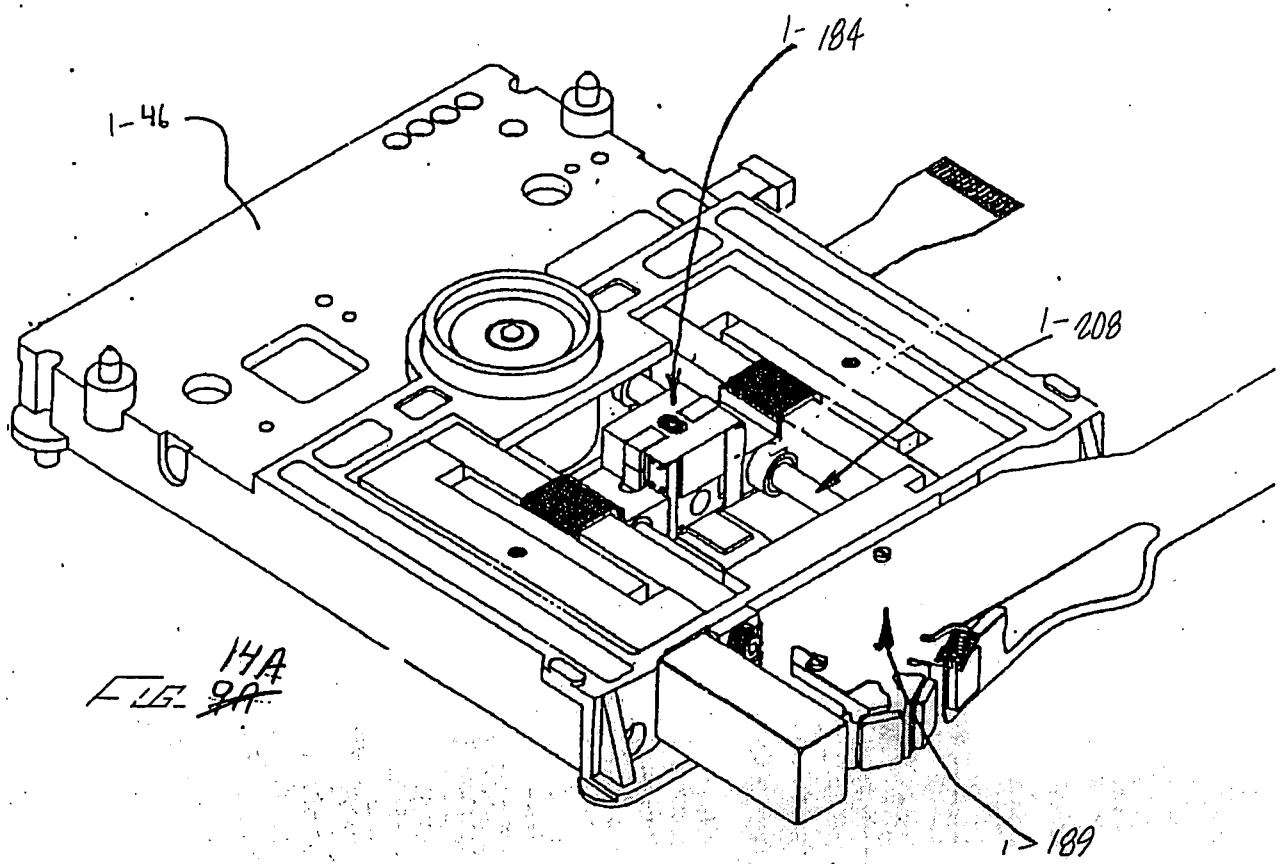


FIG. 14A

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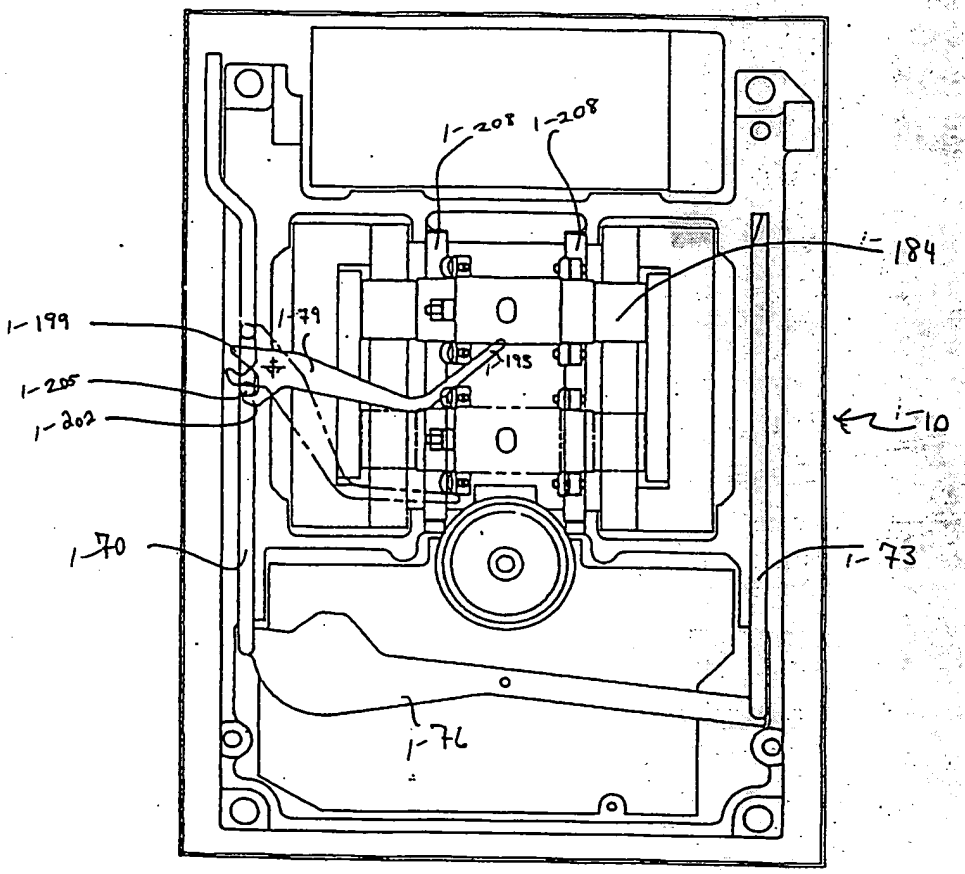

FIG. 14B
~~9B~~

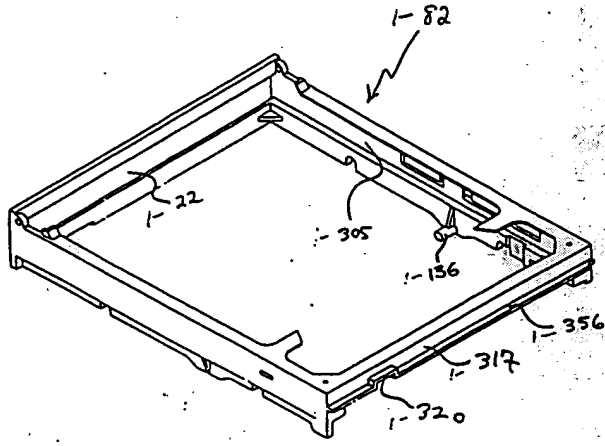

FIG. 15A ~~10A~~

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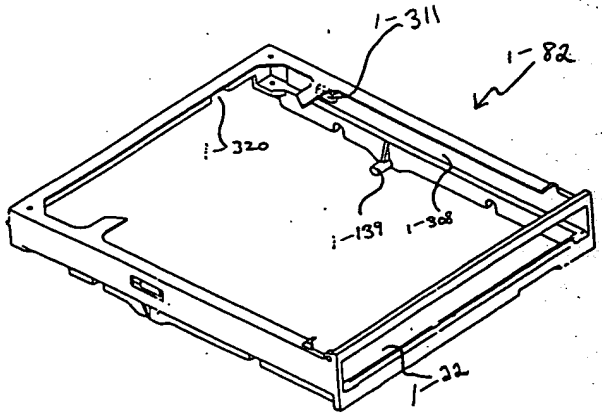

FIG. 15B ~~10B~~

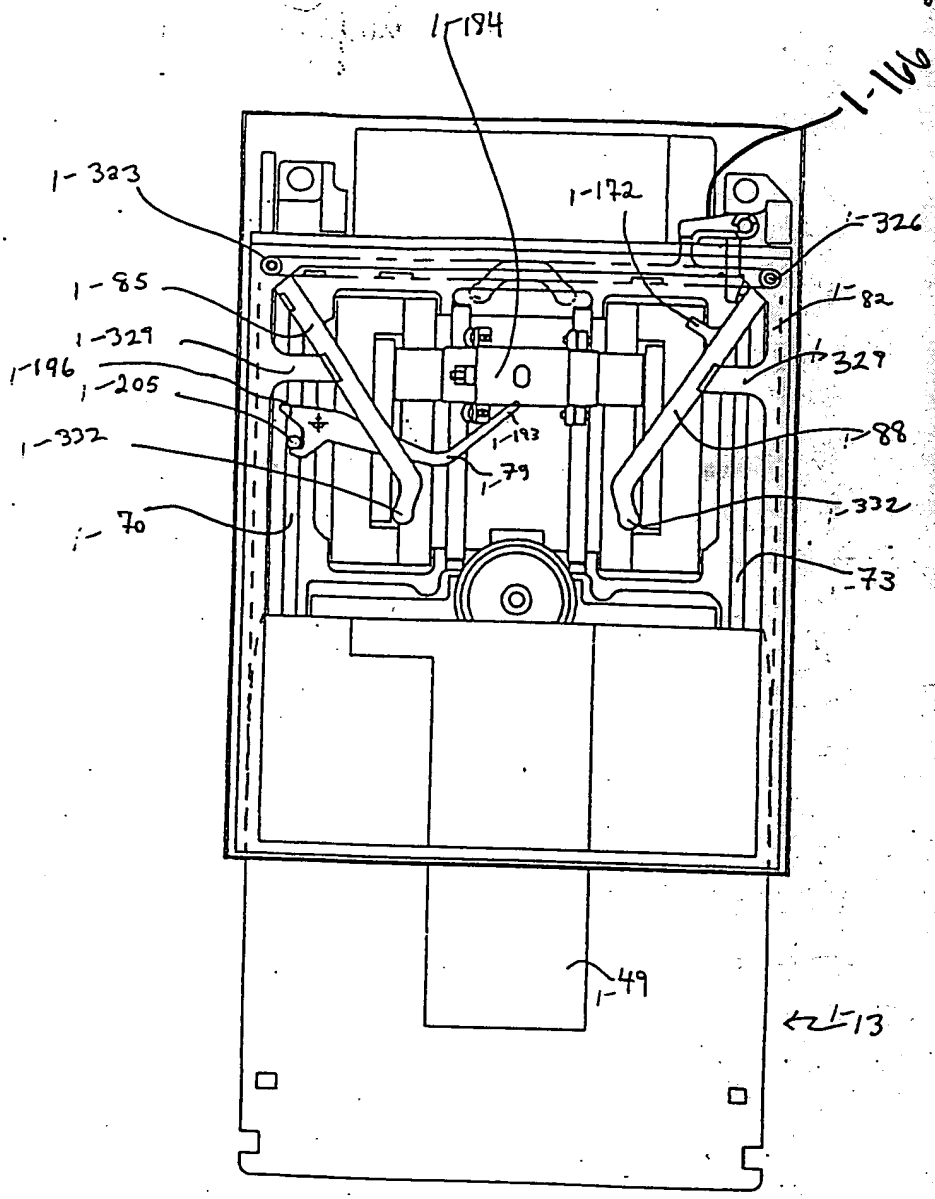
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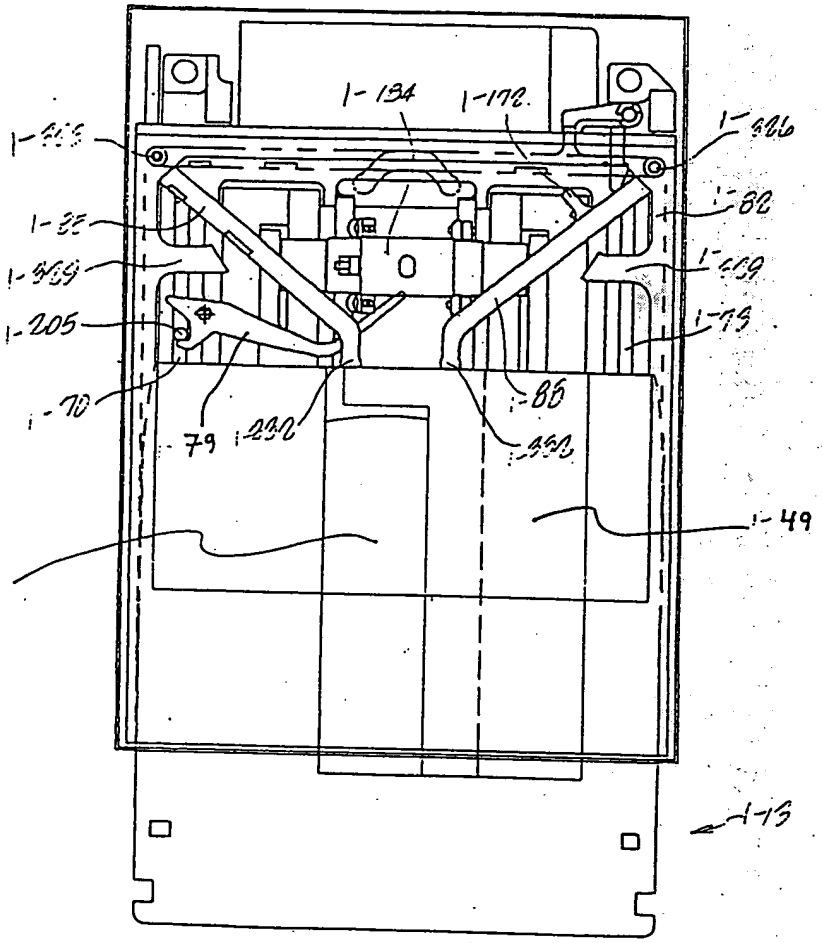
FIG. 16A



16B

FIG. 16B

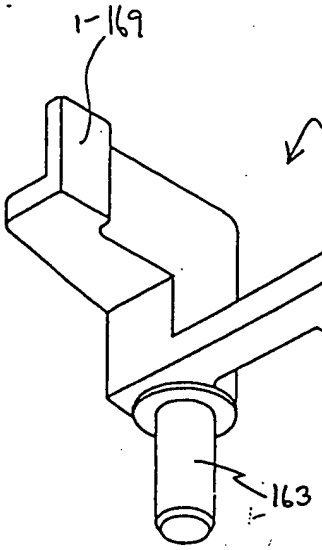


FIG. ~~12B~~ 17B

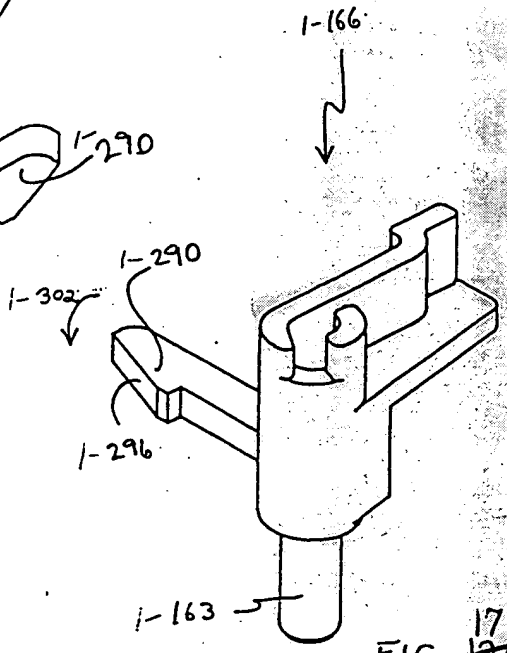


FIG. ~~12A~~ 17A

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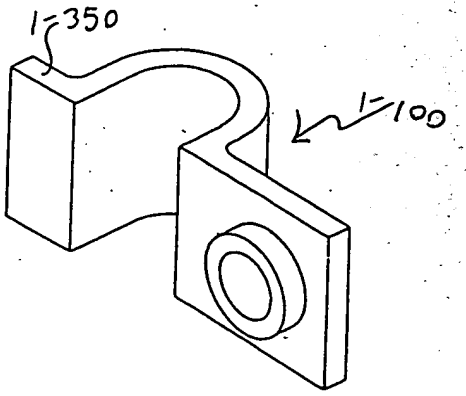

FIG. ~~13~~ 18

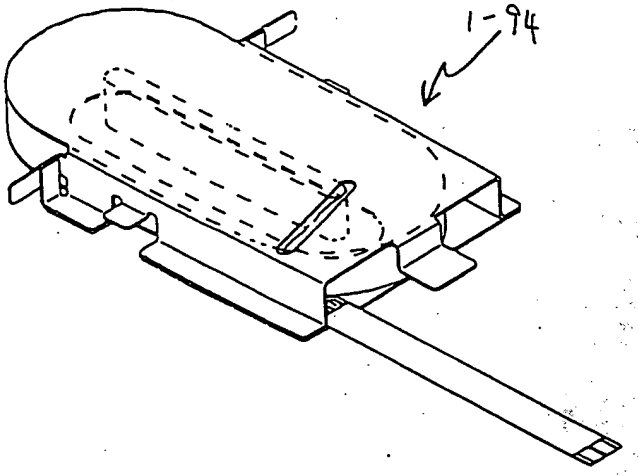

FIG. 19

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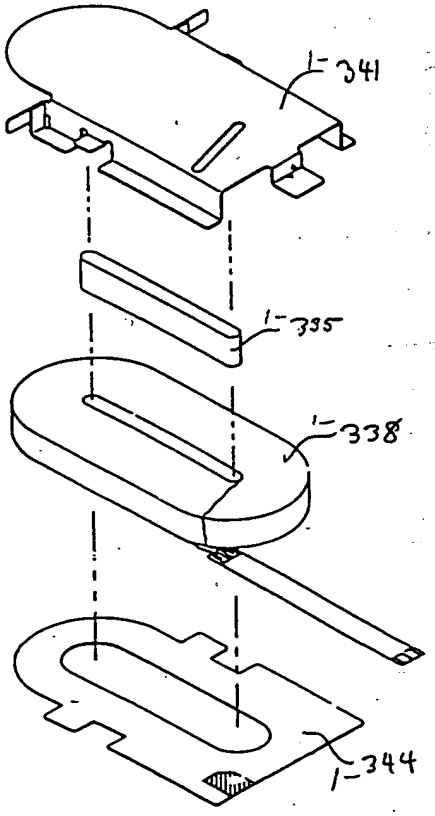


FIG. 20

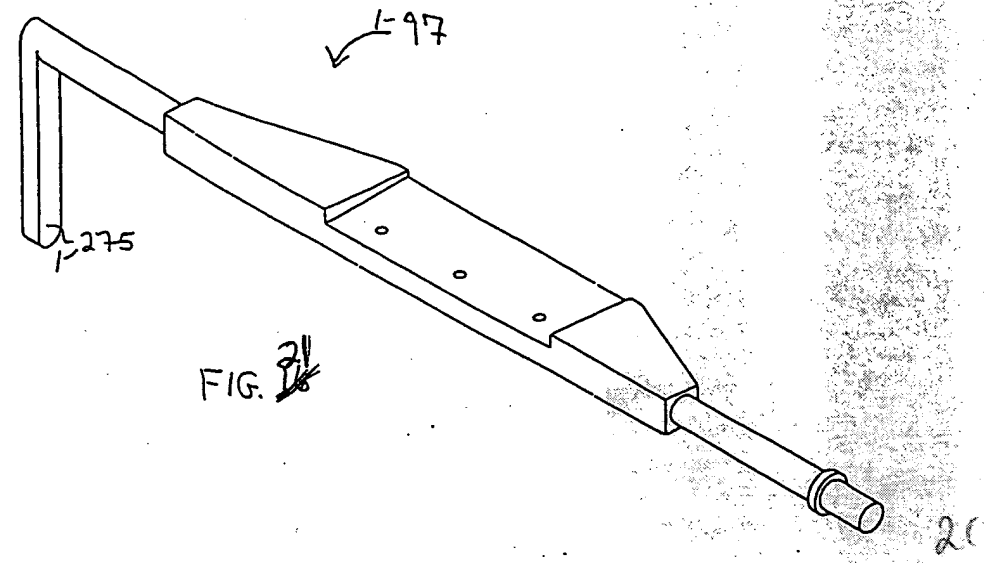


FIG. 21

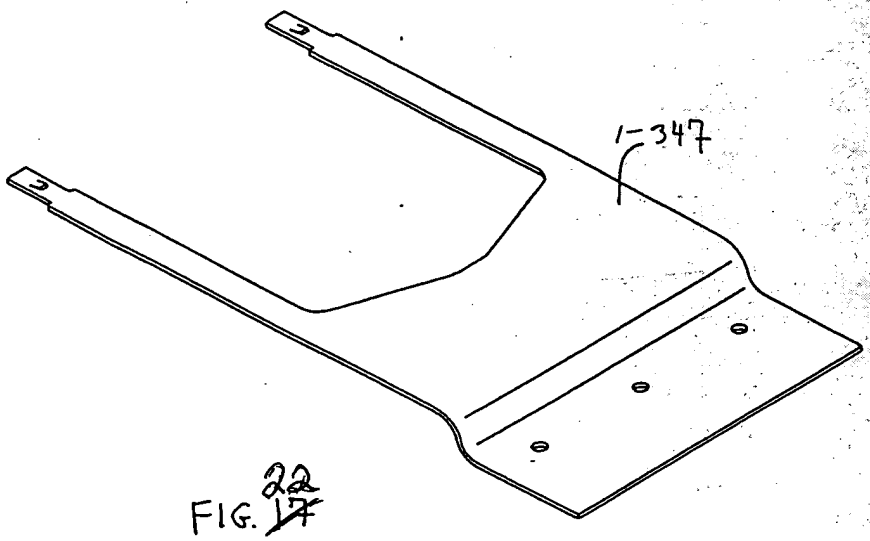


FIG. 22

FIG. 25

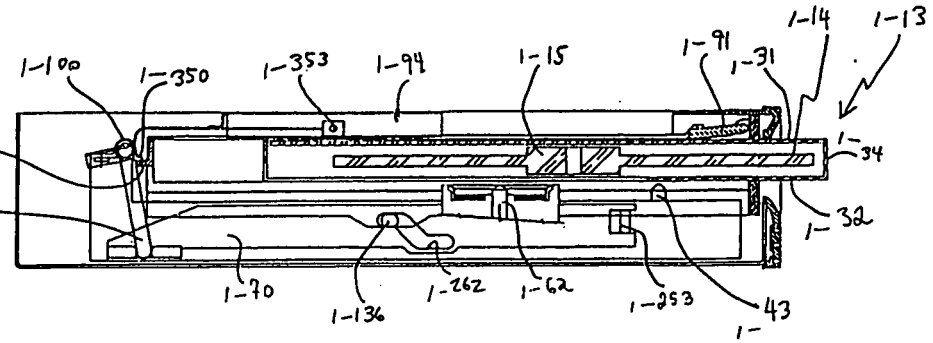


FIG. 24

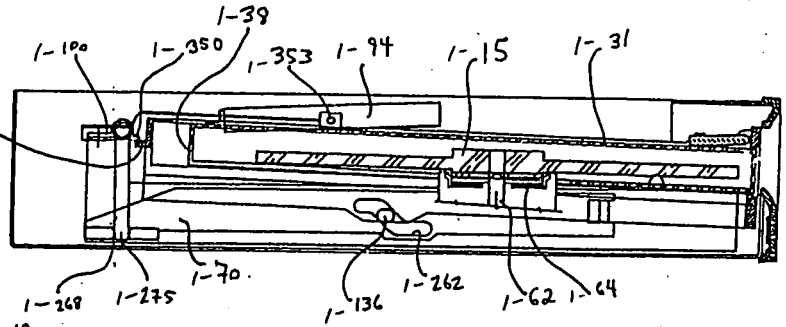
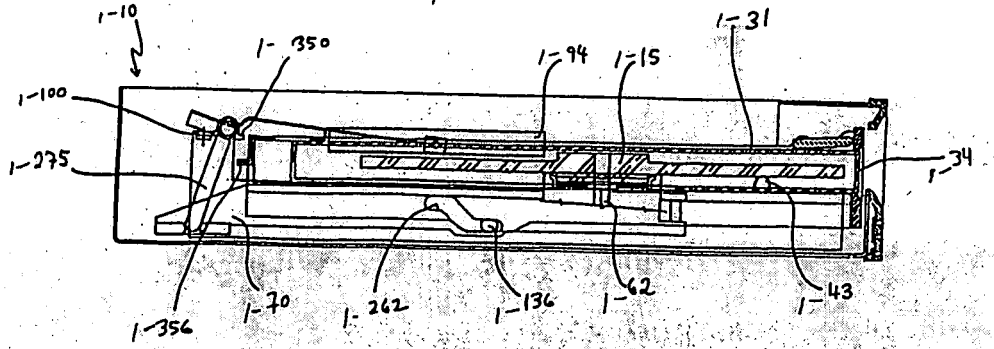


FIG. 23



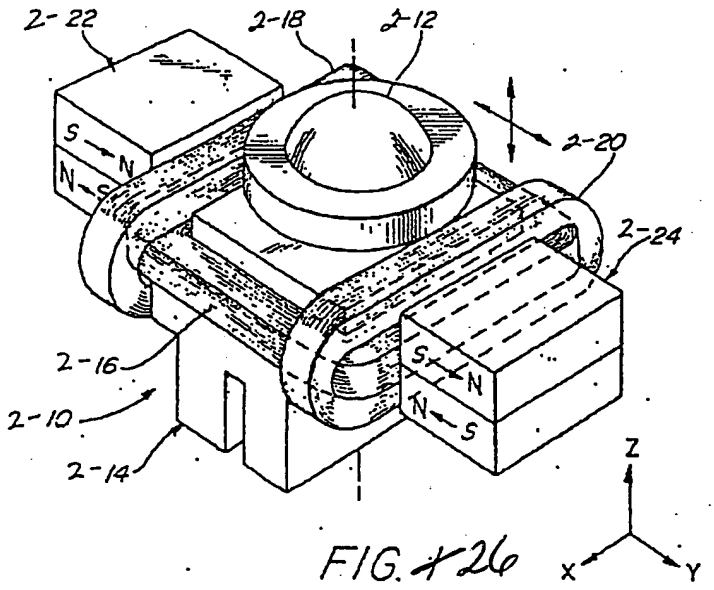


FIG. 26

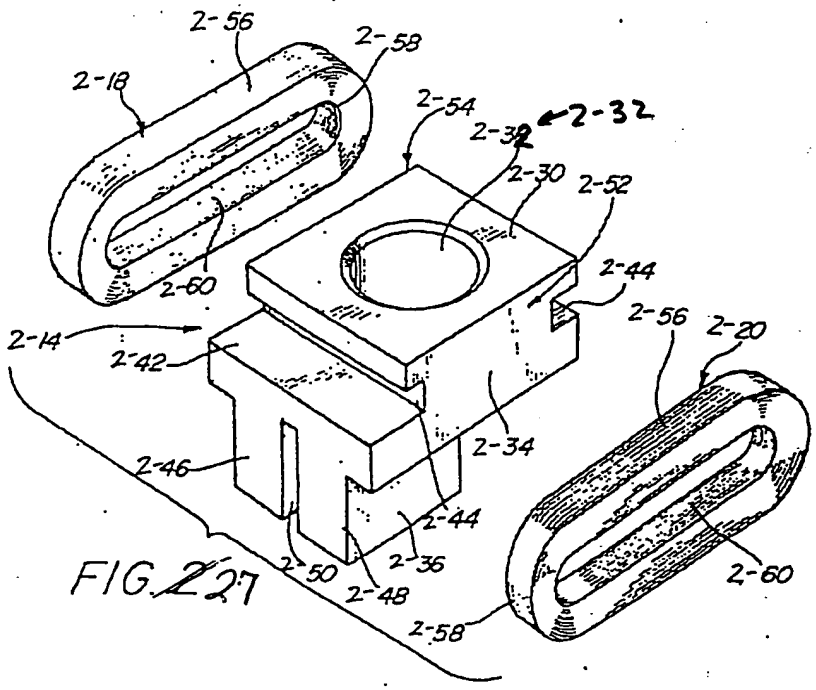


FIG. 27

FIG. 3A

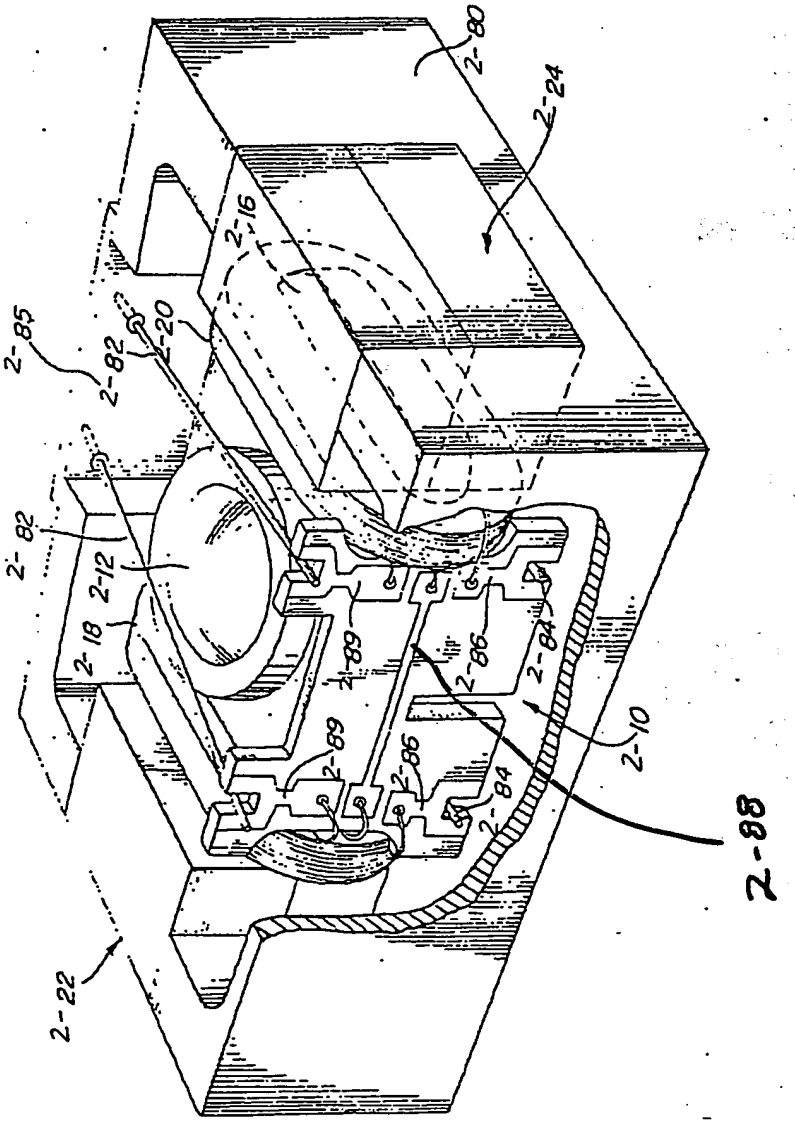


FIG. 4 29

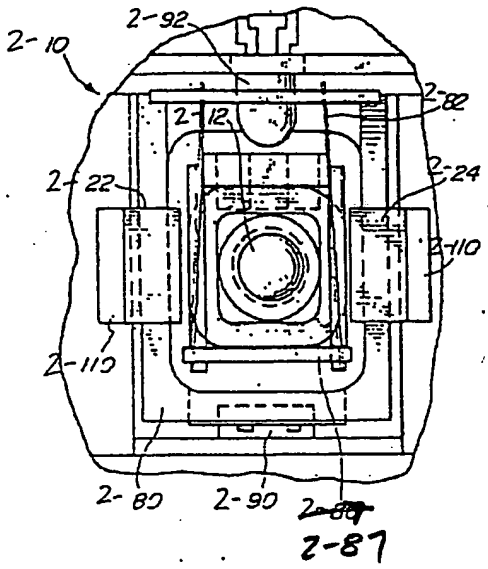


FIG. 5 30

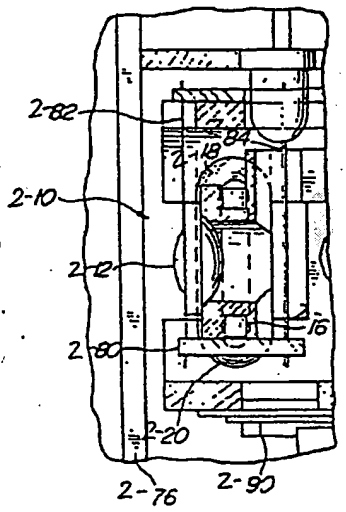


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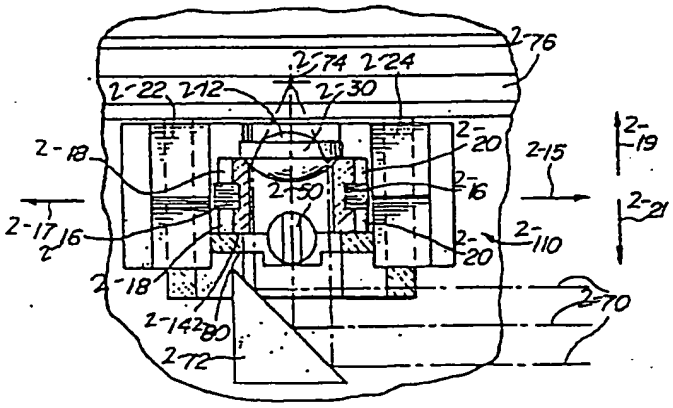
FIG. 6 31



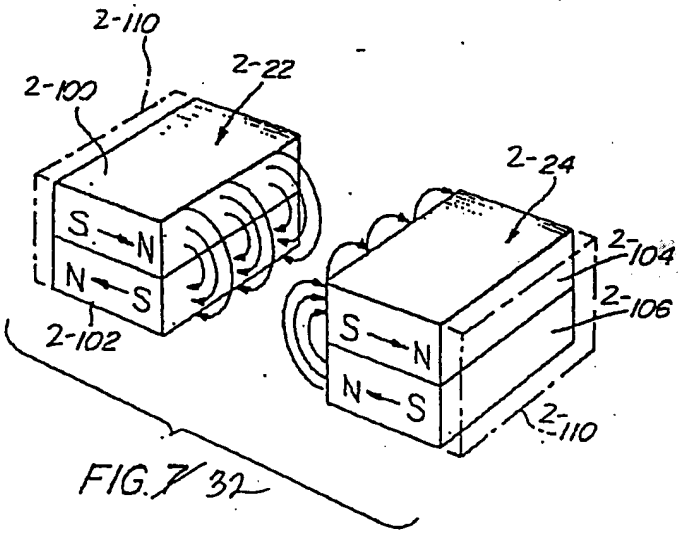


FIG. 7 32

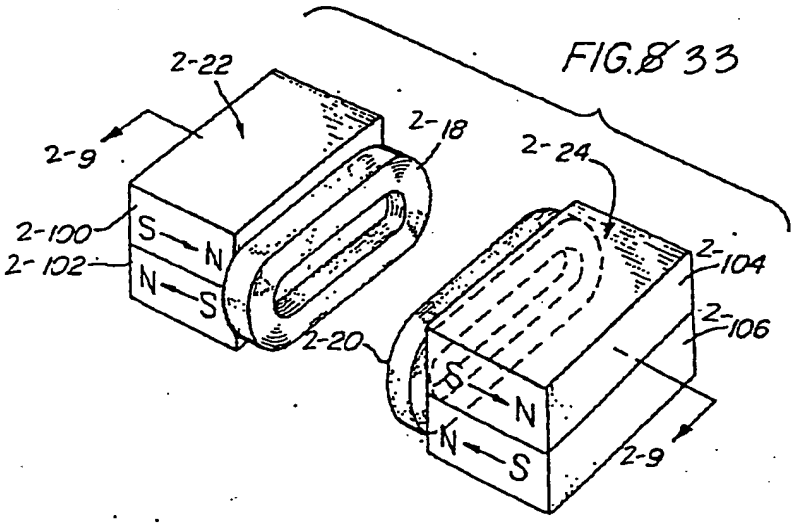


FIG. 8 33

FIG. 9 34

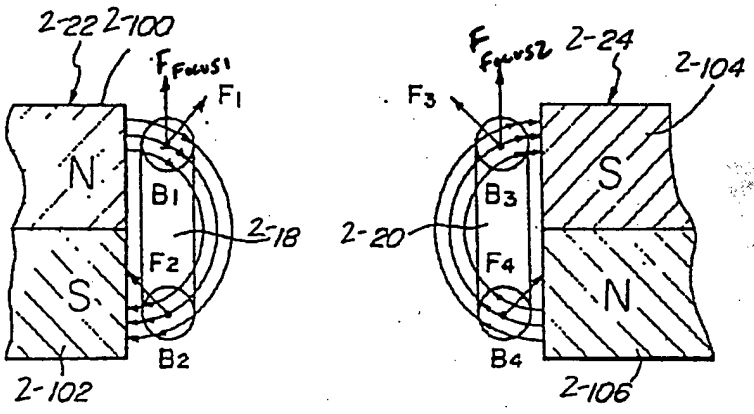
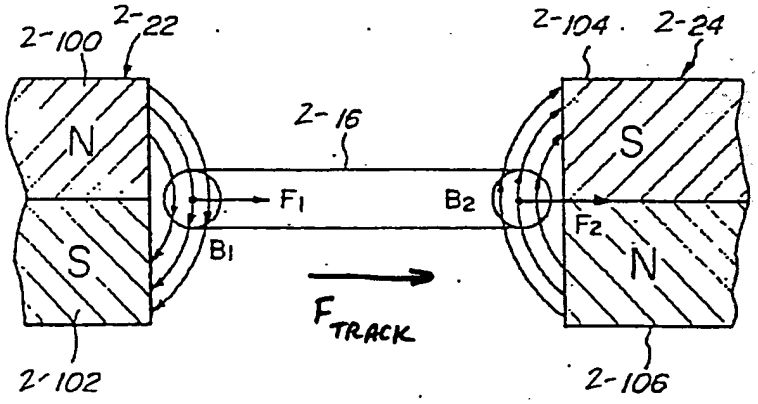
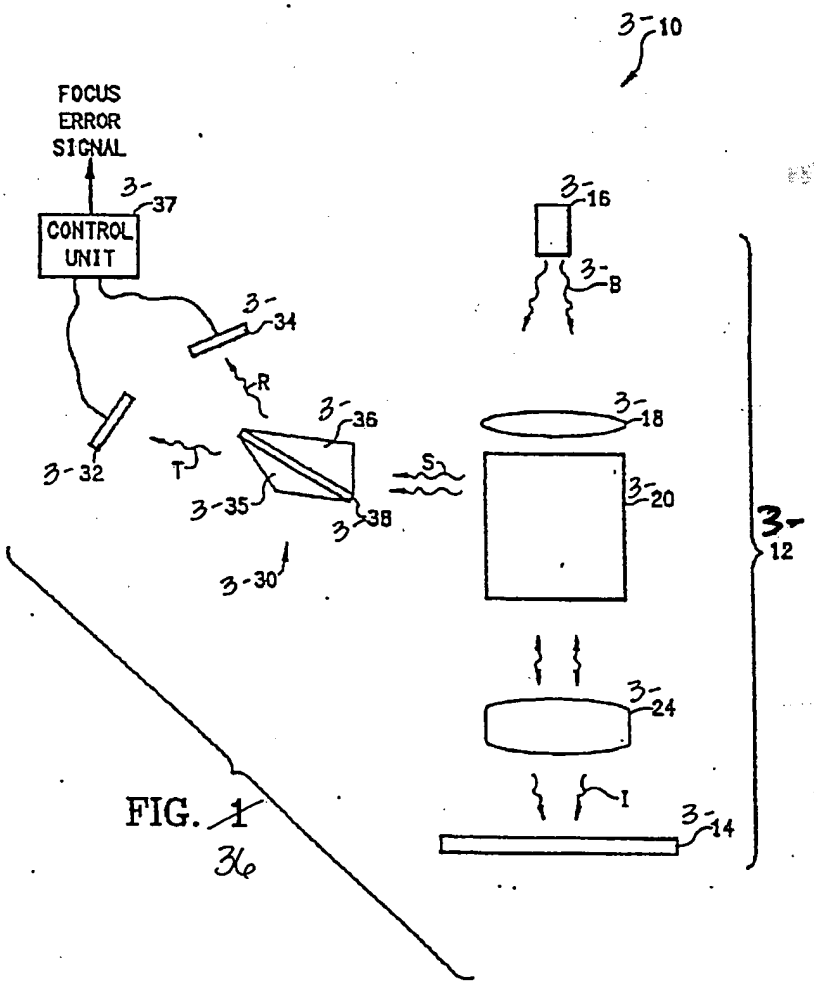


FIG. 10 35

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CONTROL UNIT
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FIG. 1

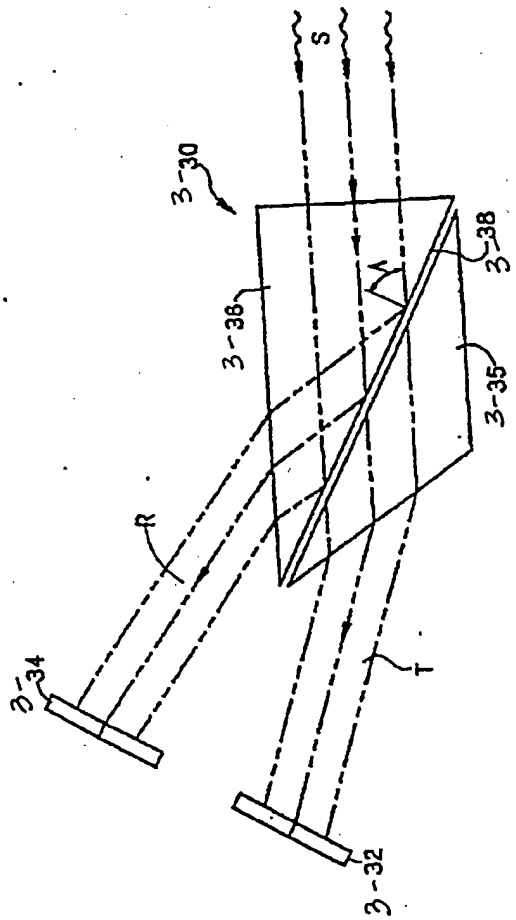
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FIG. 2

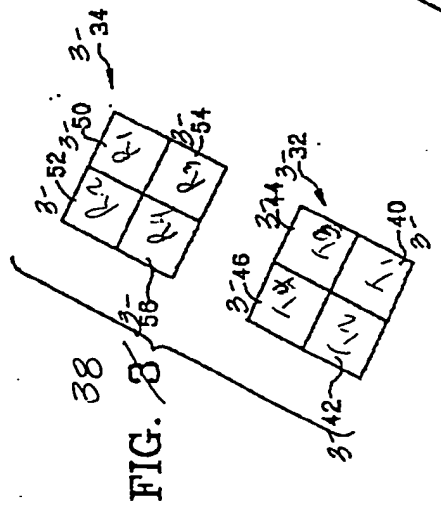
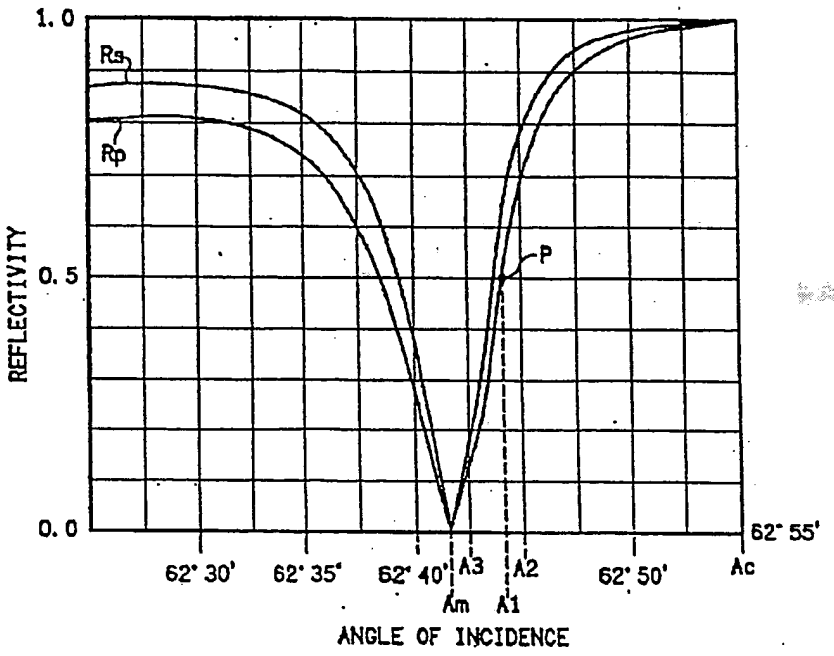

3-34
3-50
3-52
3-54
3-58
3-32
3-44
3-46
3-40
3-42

FIG. 3

FIG. 4 39



50

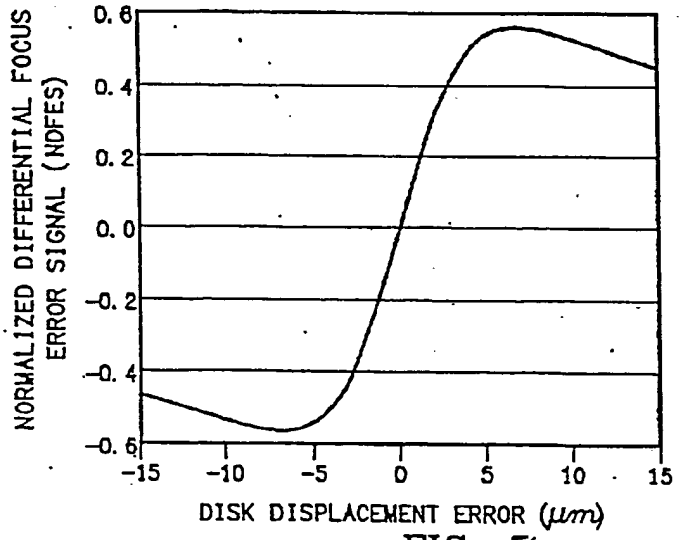


FIG. 5 40

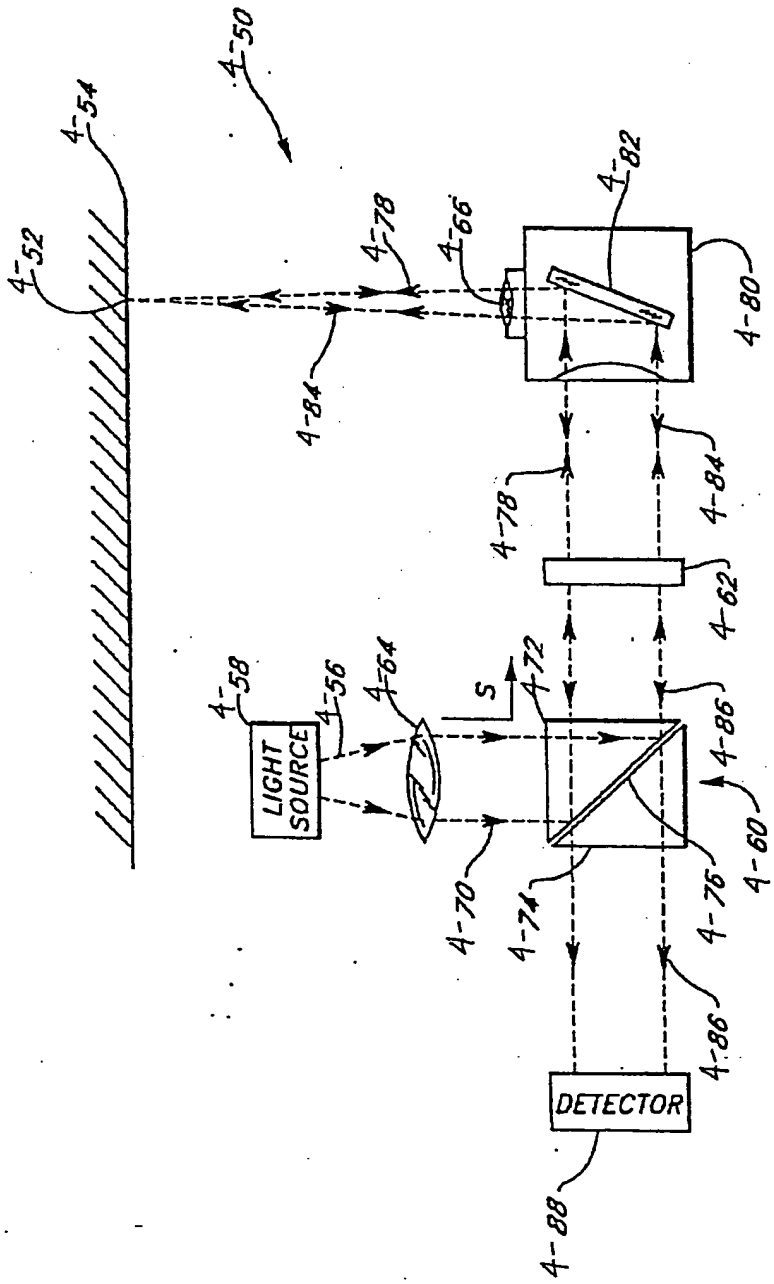

4-50
4-54
4-52
4-78
4-66
4-82
4-80
4-84
4-84
4-78
4-62
4-58
4-56
4-64
S
4-72
4-86
LIGHT SOURCE
4-70
4-74
4-76
4-60
4-86
DETECTOR
4-88

FIG. 4

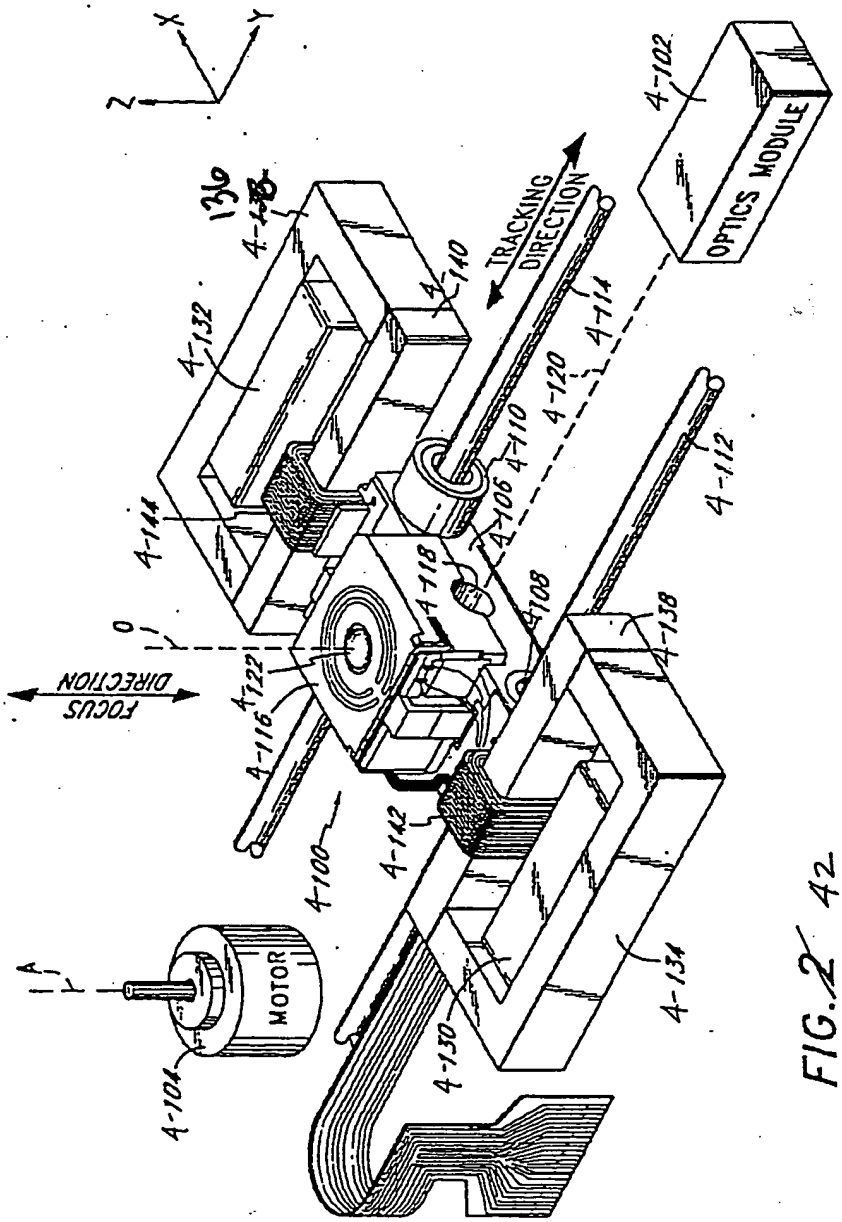

Z
X
Y
4-102
OPTICS MODULE
TRACKING DIRECTION
4-140
4-132
4-114
4-120
4-110
4-106
4-112
4-144
4-118
4-108
4-138
FOCUS DIRECTION
4-122
4-116
4-142
4-100
4-134
4-150
MOTOR
4-104

FIG. 2

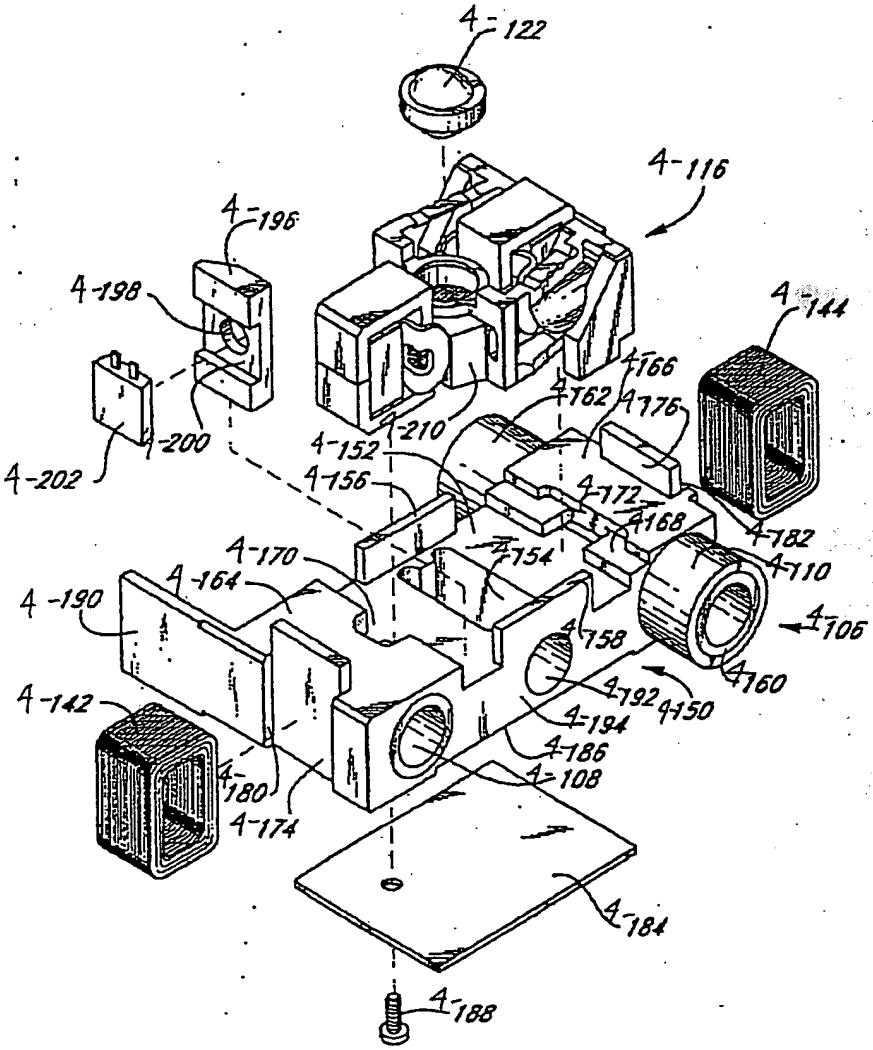

4-122
4-116
4-196
4-198
4-144
4-166
4-162
4-176
4-200
4-210
4-152
4-202
4-156
4-172
4-168
4-182
4-170
4-154
4-110
4-164
4-190
4-106
4-158
4-160
4-192
4-150
4-142
4-194
4-186
4-180
4-108
4-174
4-184
4-188

FIG. 3 43

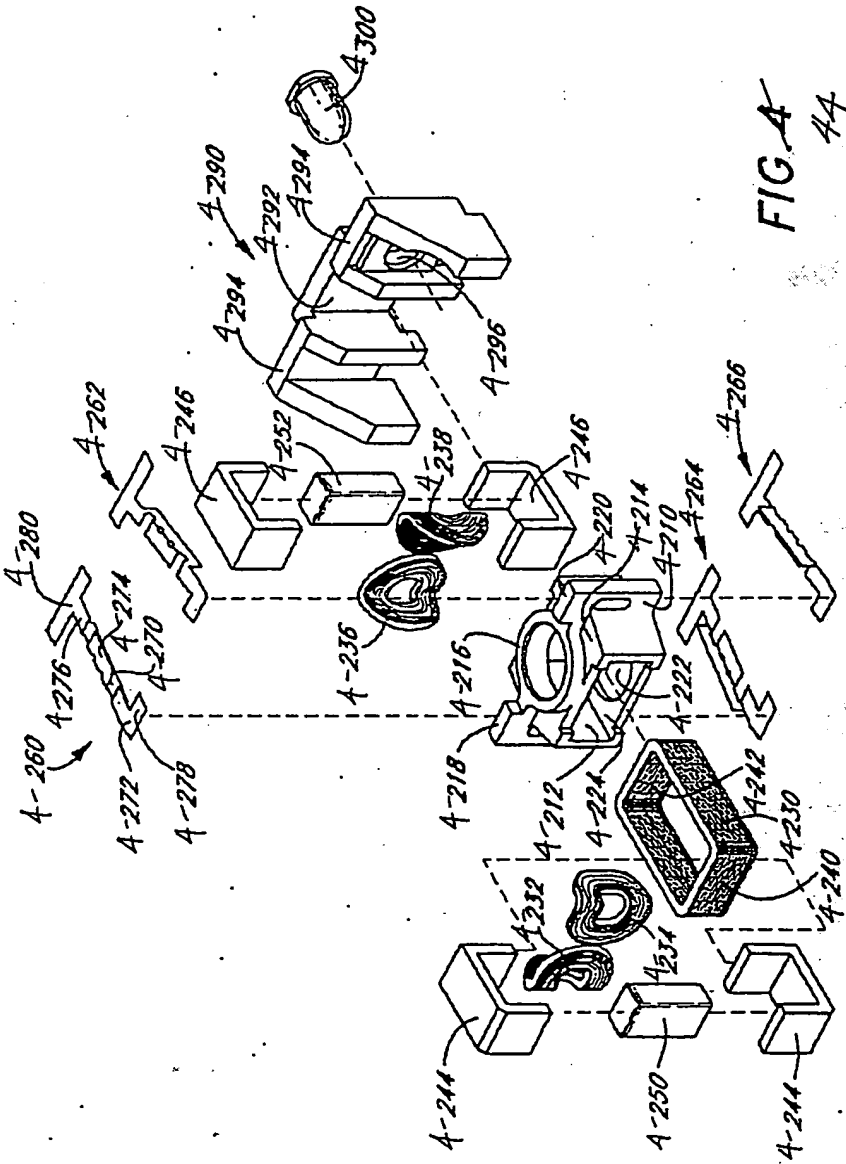


FIG. 4

44

34

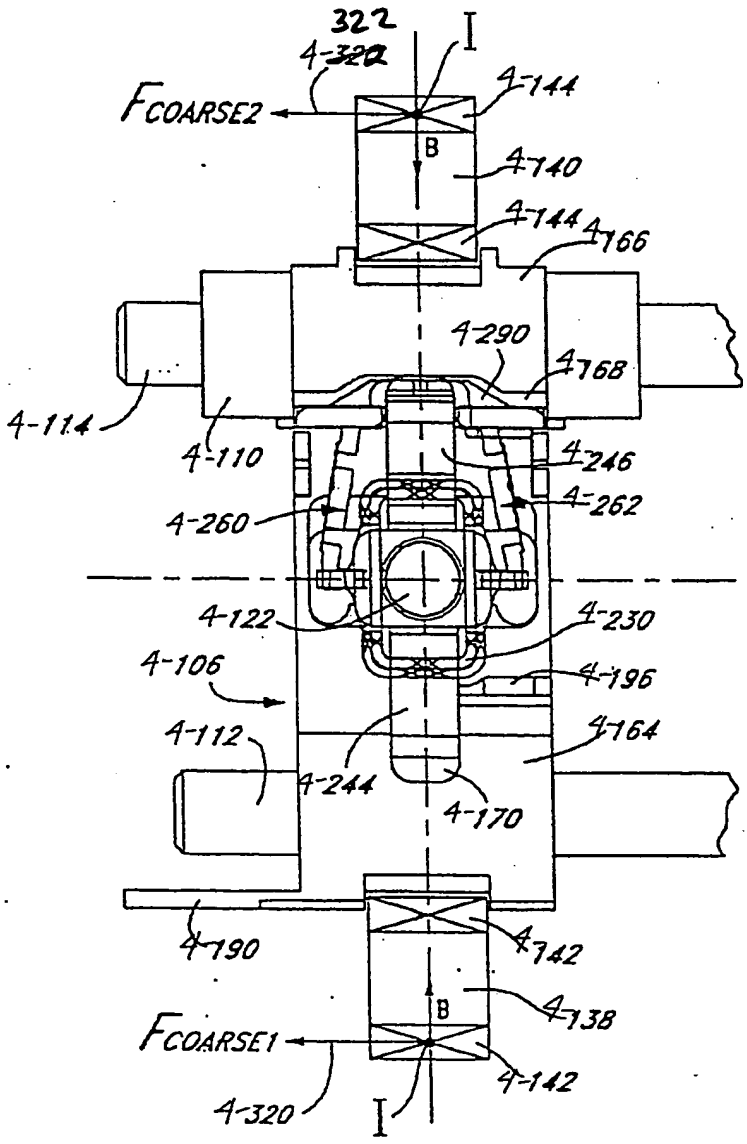


FIG. 5

45

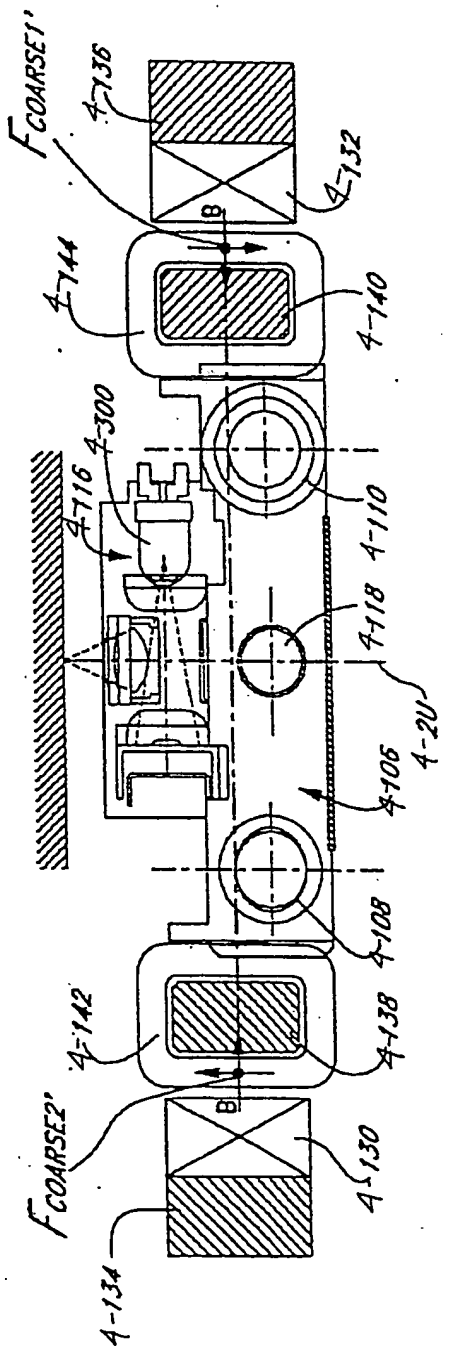

F COARSE1'
4-136
4-132
B
4-144
4-140
4-300
4-716
4-110
4-118
4-120
4-106
4-108
4-142
4-138
B
F COARSE2'
4-130
4-134

FIG. 6

46

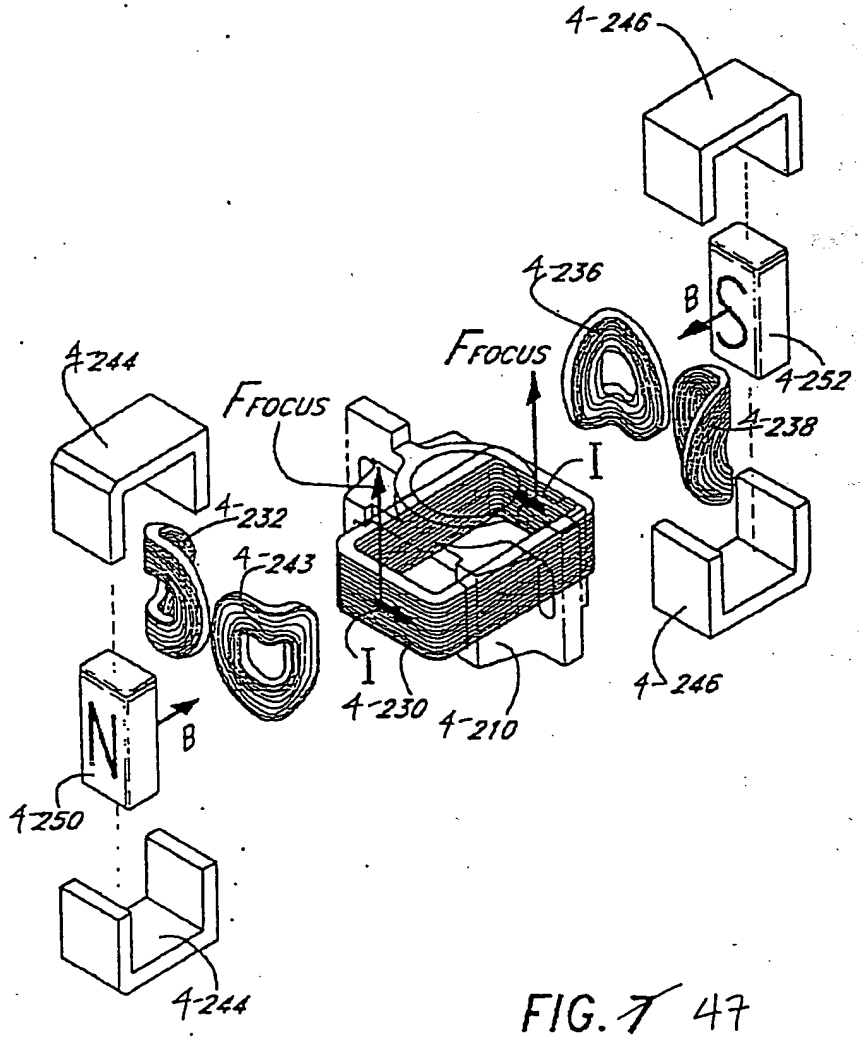

4-246
4-236
B
S
FFOCUS
4-252
4-238
4-244
FFOCUS
I
4-232
4-243
I
4-230
4-210
4-246
N
B
4-250
4-244

FIG. 7 47

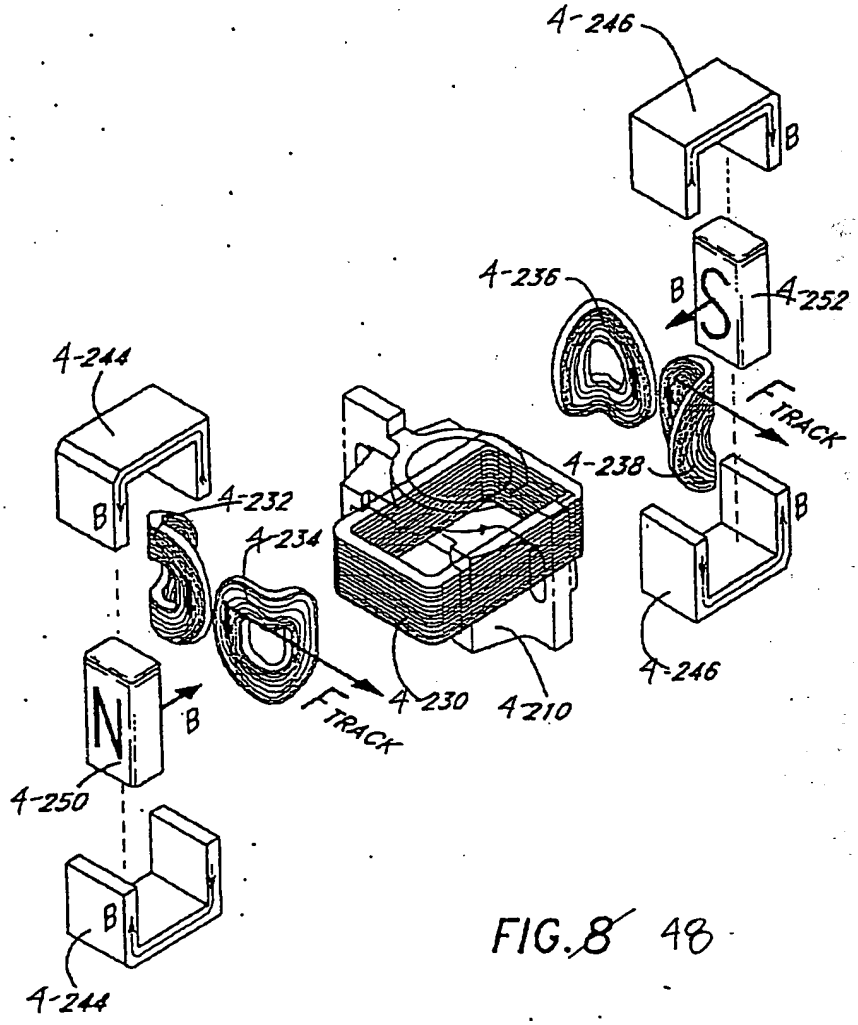

4-246
B
4-236
B
S
4-252
4-244
F TRACK
4-238
B
4-232
4-234
B
4-246
N
B
F TRACK
4-230
4-210
4-250
B
4-244

FIG. 8 48

38

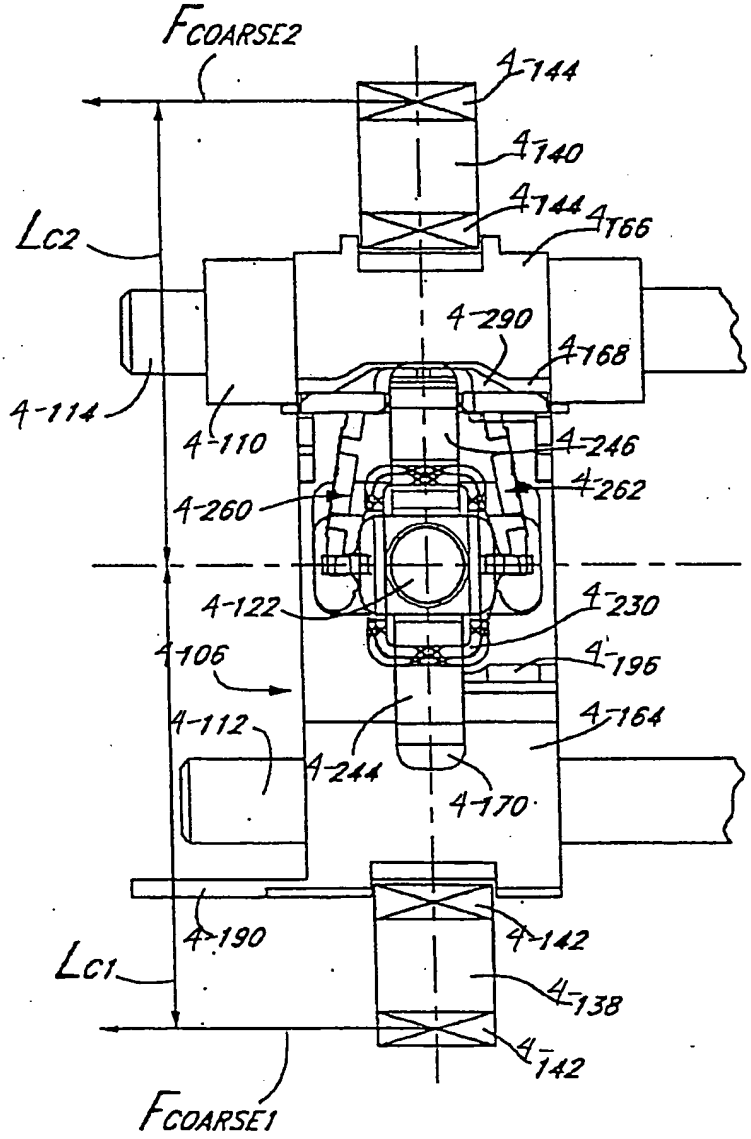

FCOARSE2
4-144
4-140
4-144
4-166
LC2
4-290
4-168
4-114
4-110
4-246
4-262
4-260
4-122
4-230
4-106
4-196
4-112
4-164
4-244
4-170
4-190
4-142
LC1
4-138
4-142
FCOARSE1

FIG. 9a

49a

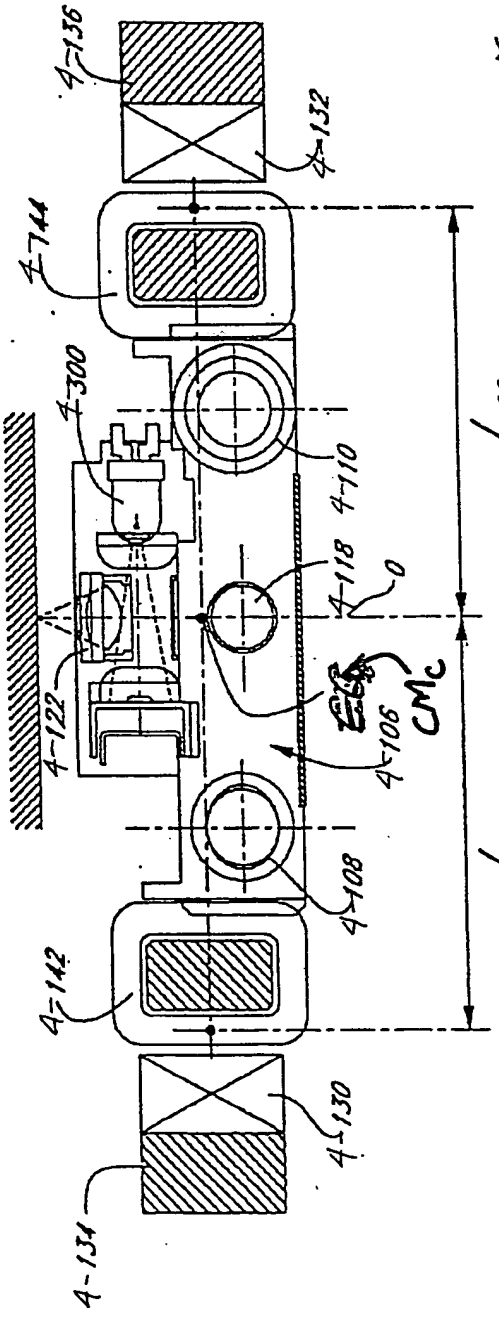

4-136
4-132
4-144
4-300
4-110
4-118
O
4-122
CMc
4-106
4-108
4-142
4-130
4-134
L_{C2}
L_{C1}

FIG. 9b
496

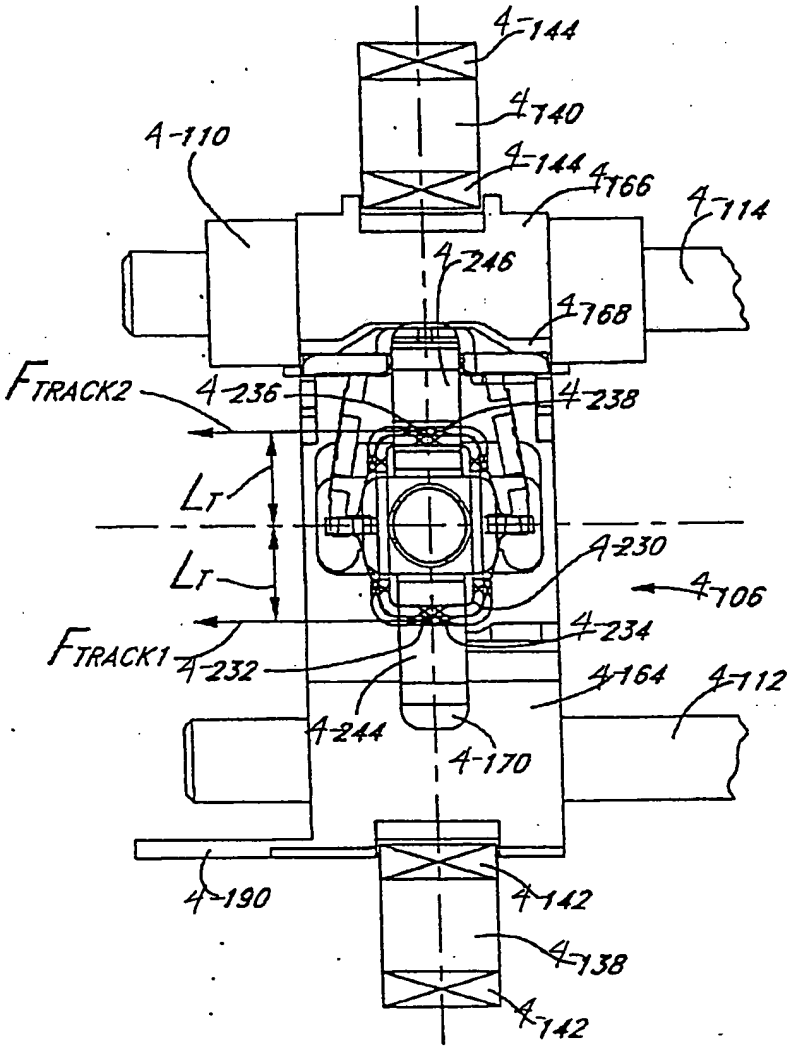
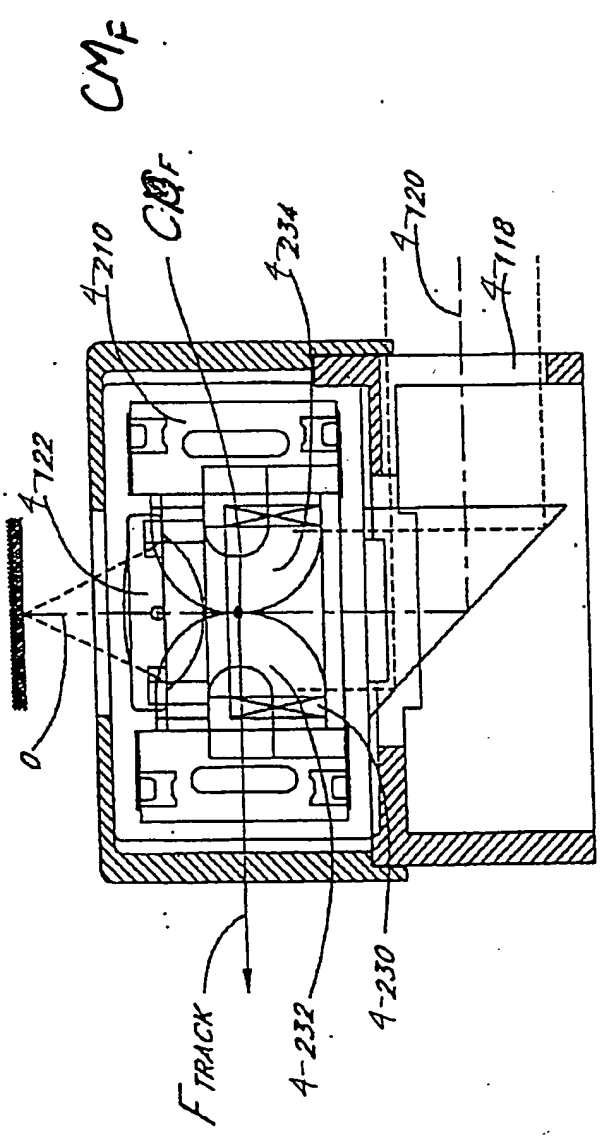

4-144
4-140
4-110
4-144
4-166
4-114
4-246
4-168
FTRACK2
4-236
4-238
LT
LT
4-230
4-106
4-234
FTRACK1
4-232
4-164
4-112
4-244
4-170
4-190
4-142
4-138
4-142

FIG. 10a

50a.

FIG. 10b



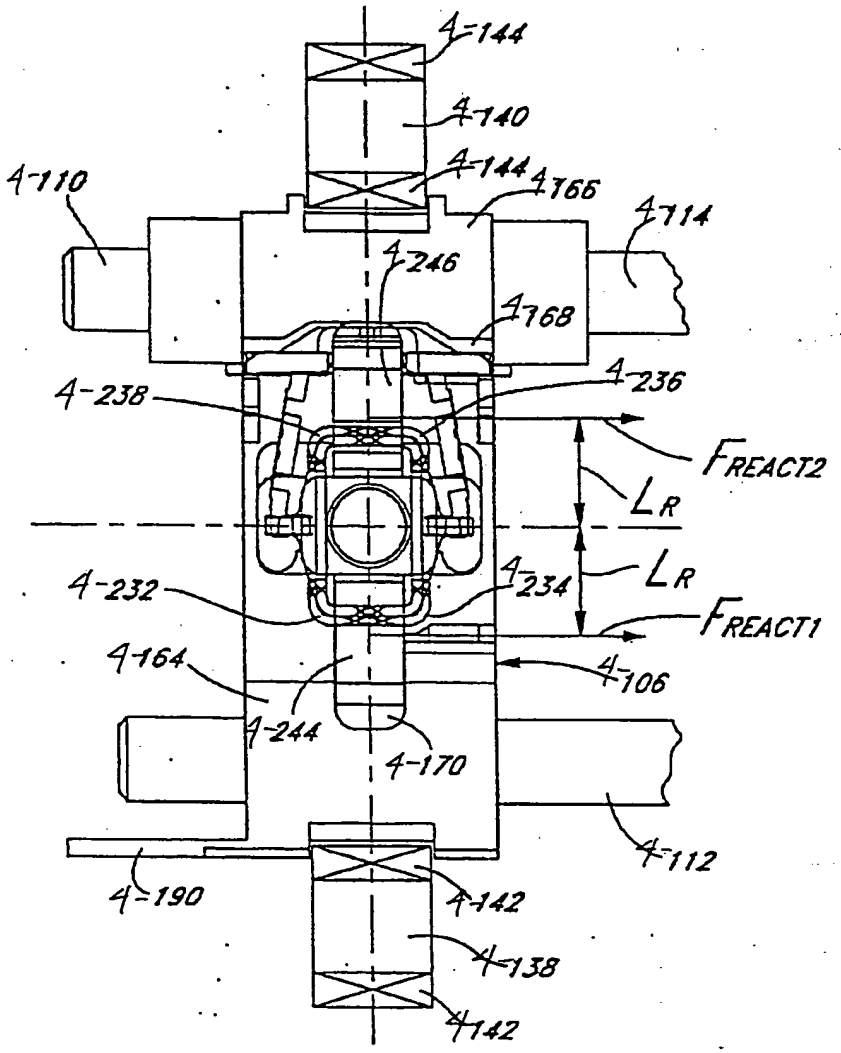

4-144
4-140
4-144
4-166
4-110
4-114
4-246
4-168
4-236
4-238
FREACT2
LR
LR
4-234
4-232
FREACT1
4-164
4-106
4-244
4-170
4-112
4-190
4-142
4-138
4-142

FIG. 11a

51a.

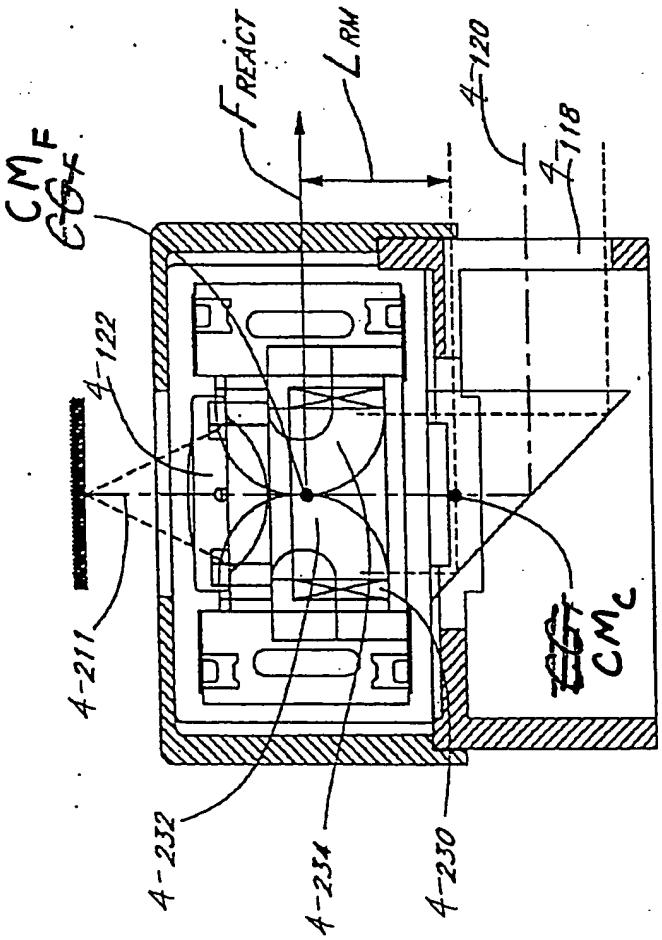


FIG. 4b
5/6

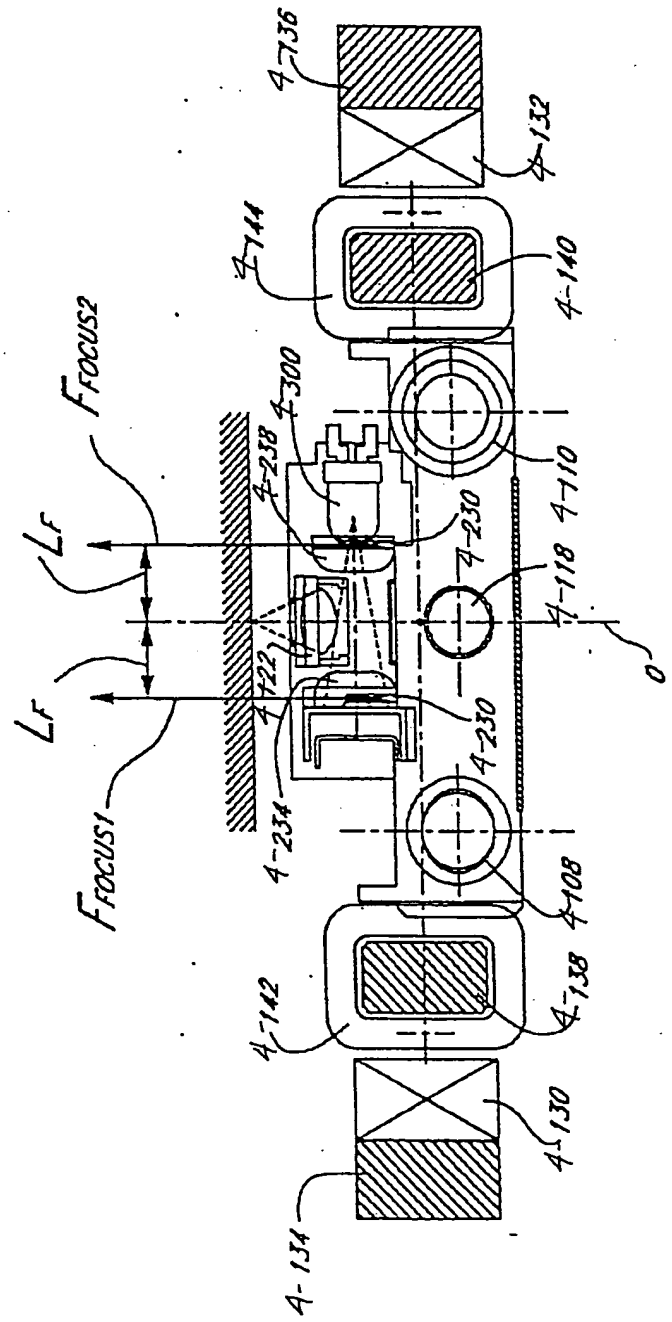


FIG. 12a

52a

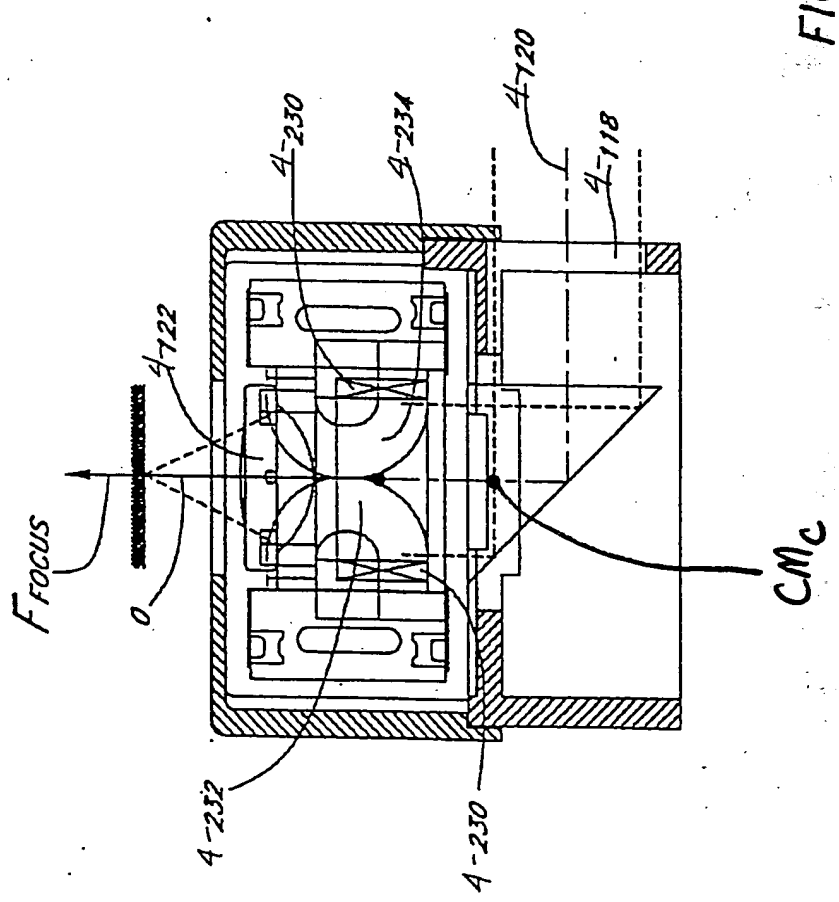


FIG. 12b
626

46

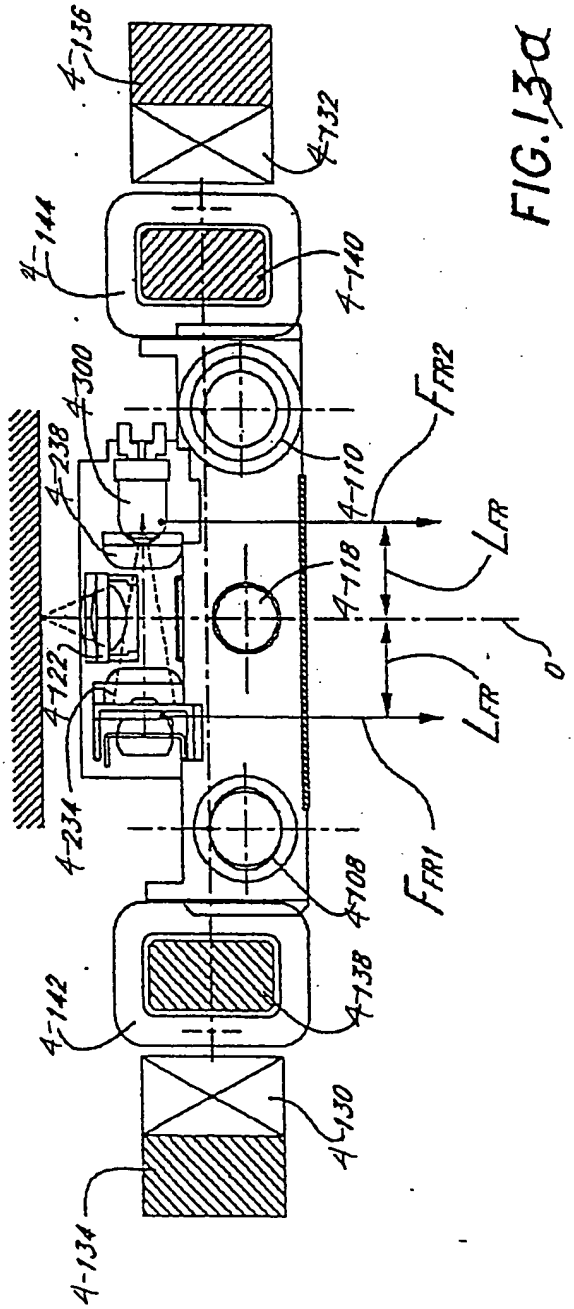
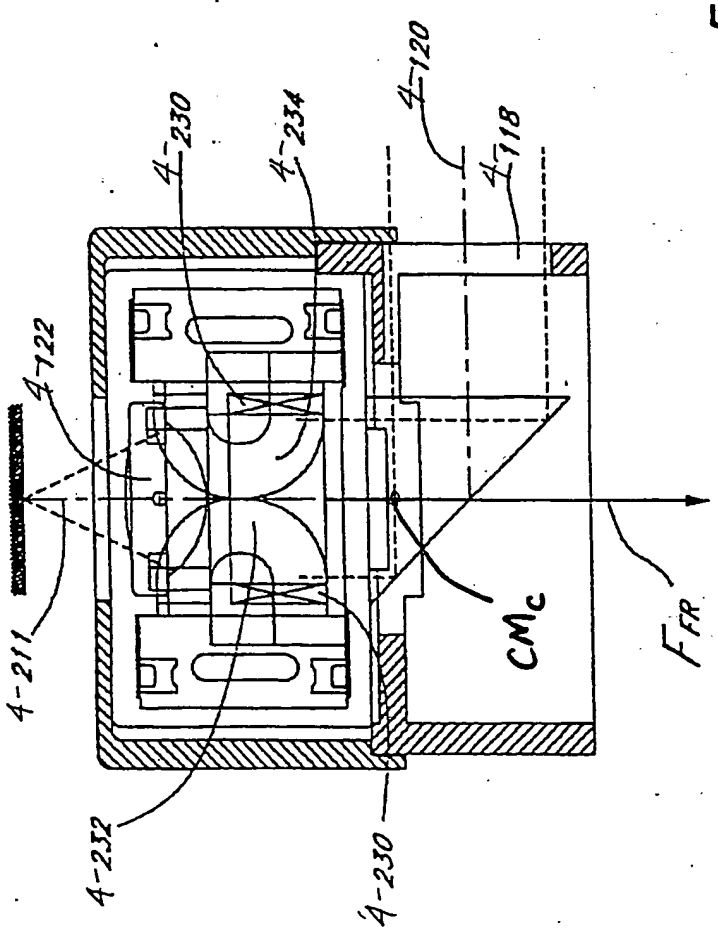


FIG. 13a

53a

47

FIG. 136



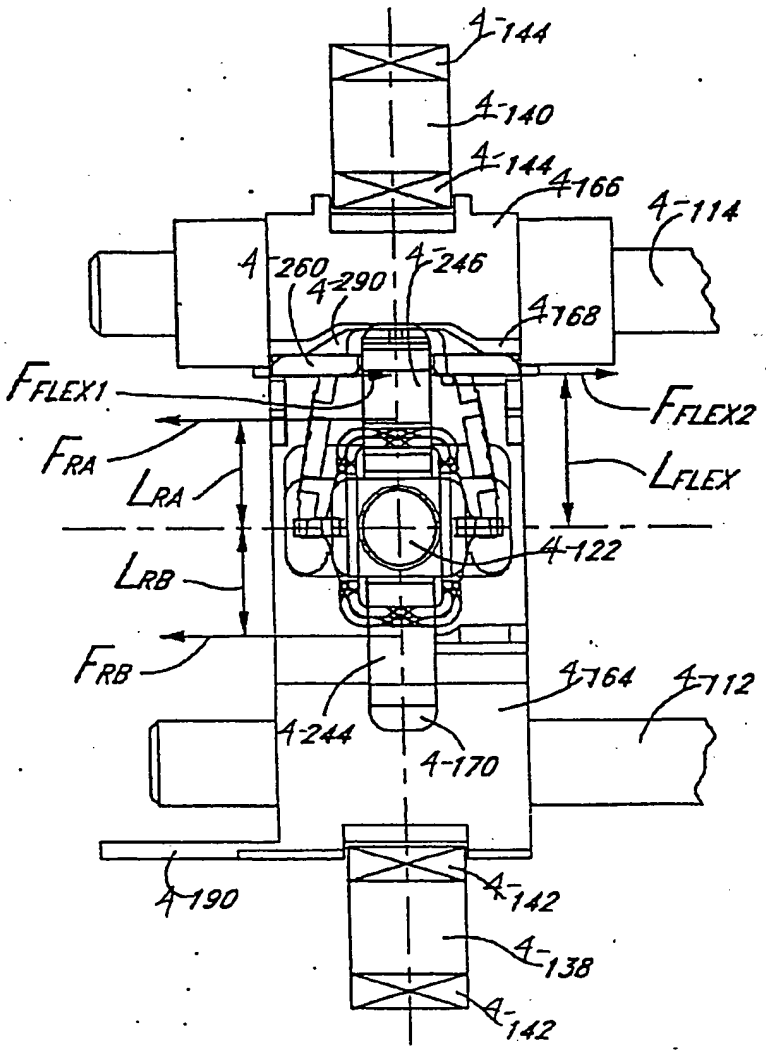

4-144
4-140
4-144
4-166
4-114
4-260
4-290
4-246
4-168
FFLEX1
FFLEX2
FRA
LRA
LFLEX
4-122
LRB
FRB
4-164
4-112
4-244
4-170
4-190
4-142
4-138
4-142

FIG. 14 54

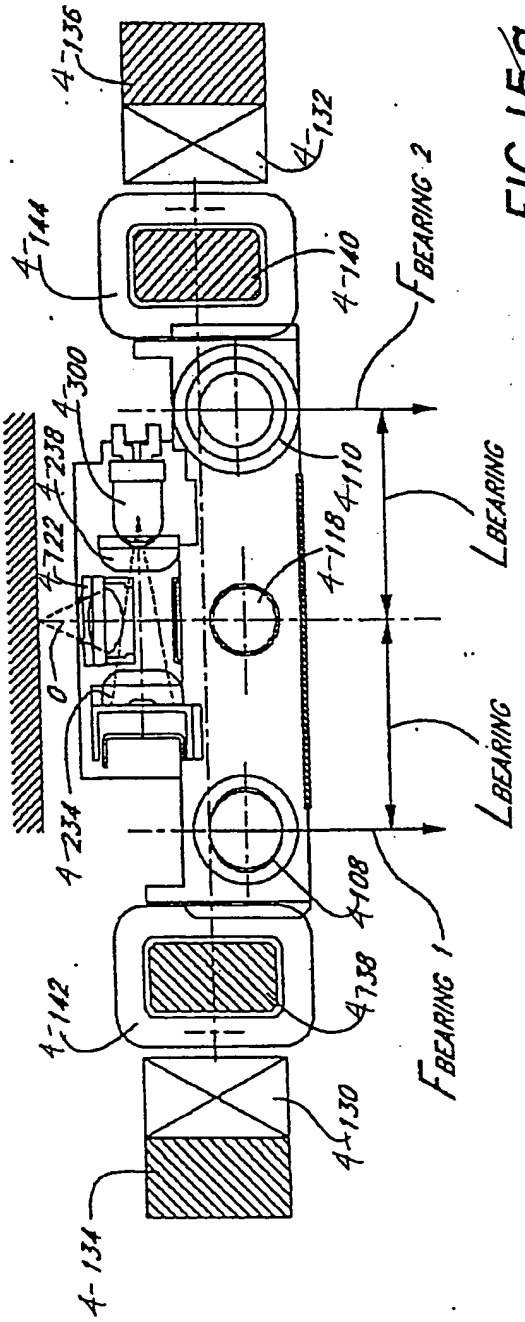


FIG. 15a

55a

50

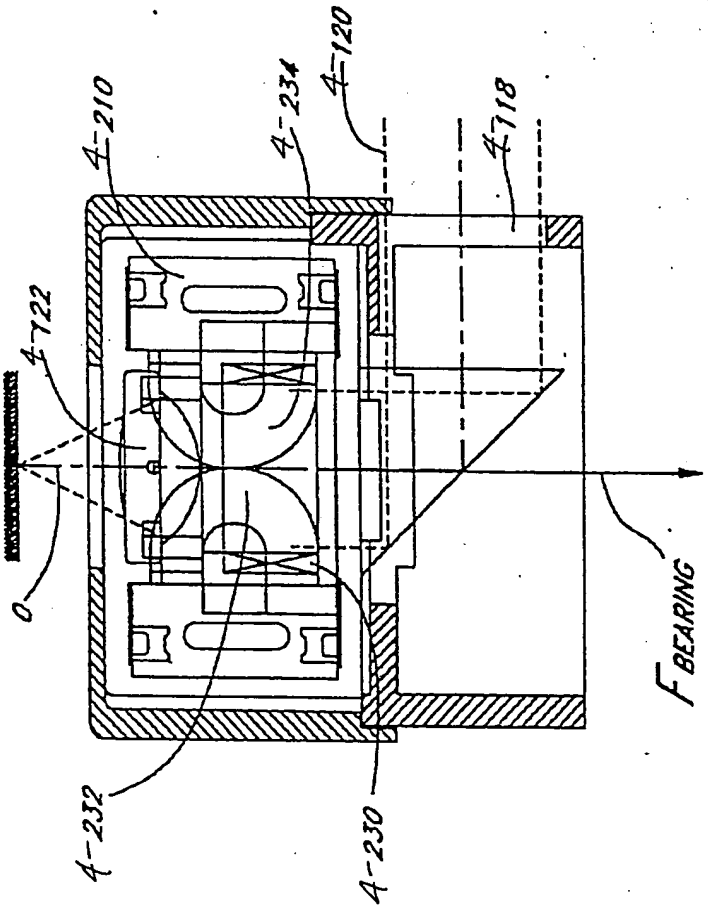


FIG. 15b
556

51

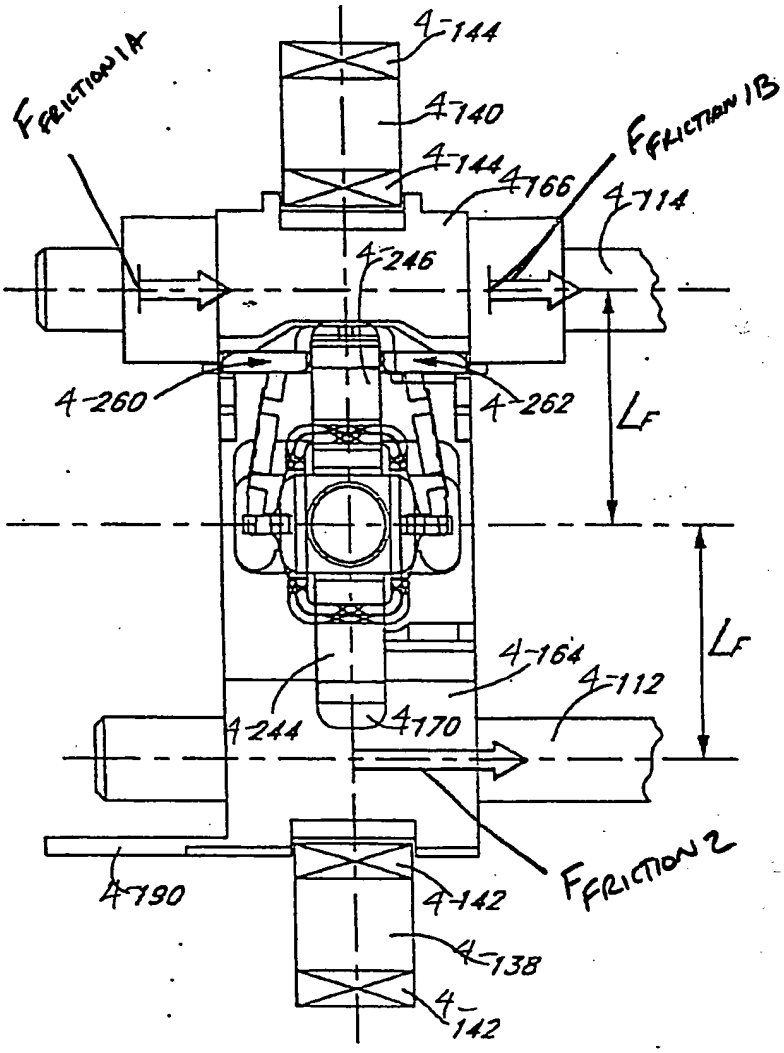

4-144
FFRICTION 1A
4-140
FFRICTION 1B
4-144
4-166
4-714
4-246
4-260
4-262
LF
LF
4-164
4-112
4-244
4-170
FFRICTION 2
4-190
4-142
4-138
4-142

FIG. 16a

56a

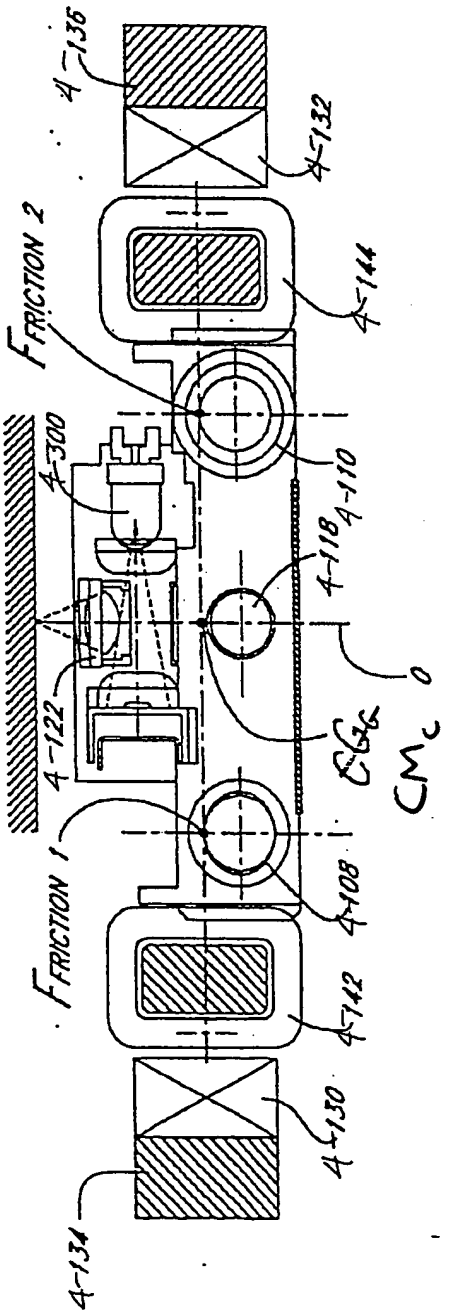
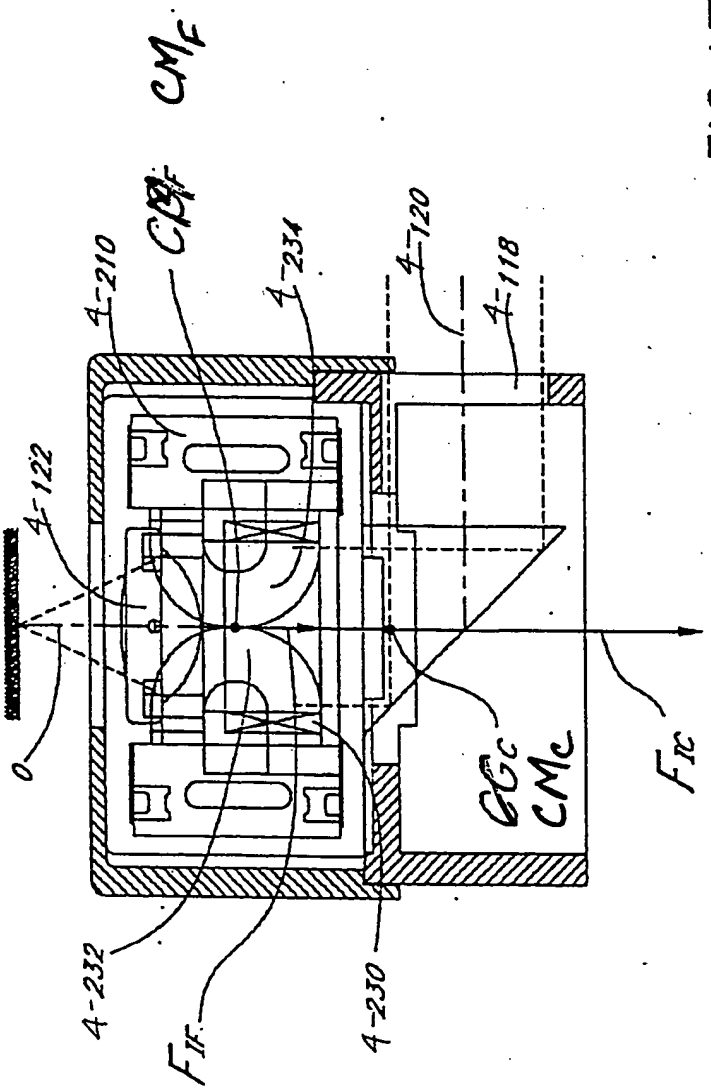


FIG. 16b

566.

53

FIG. 17



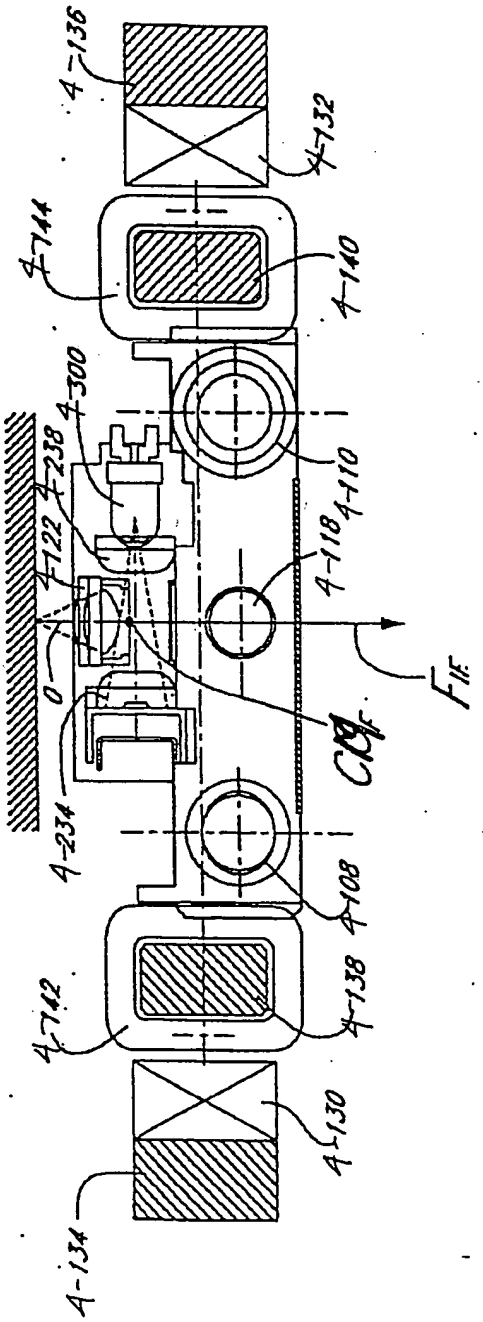


FIG. 18a

58a

55

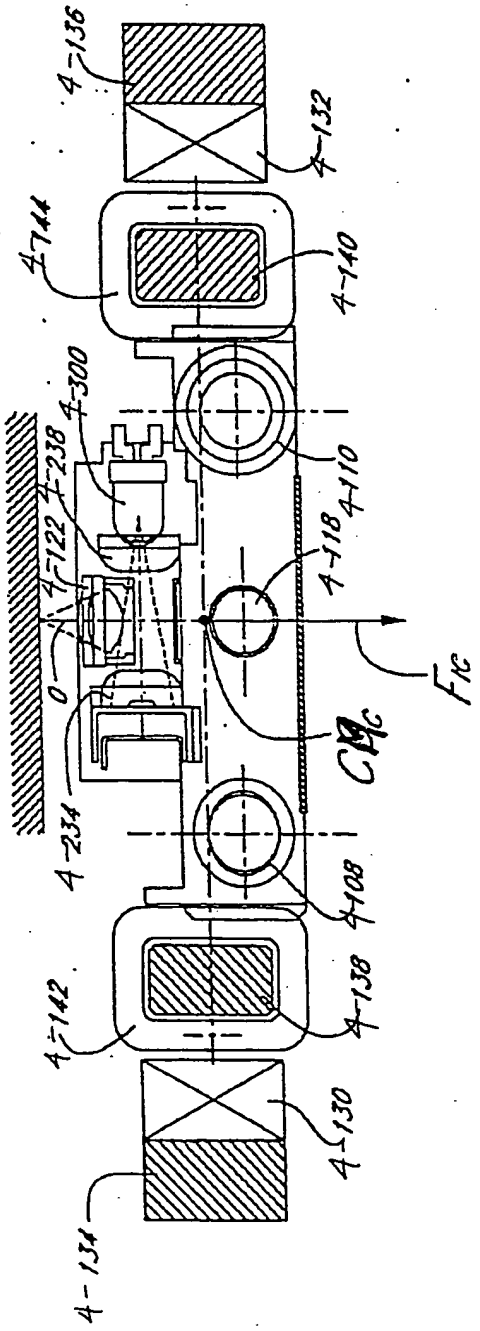


FIG. 18b

586

56

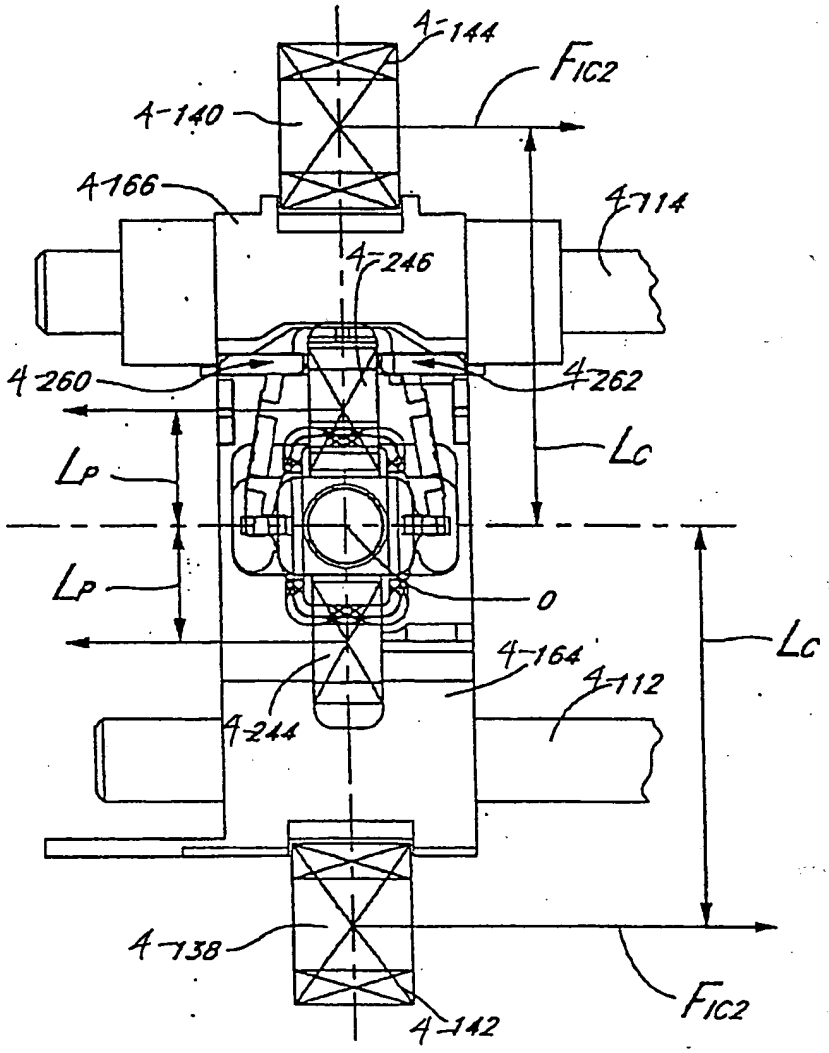
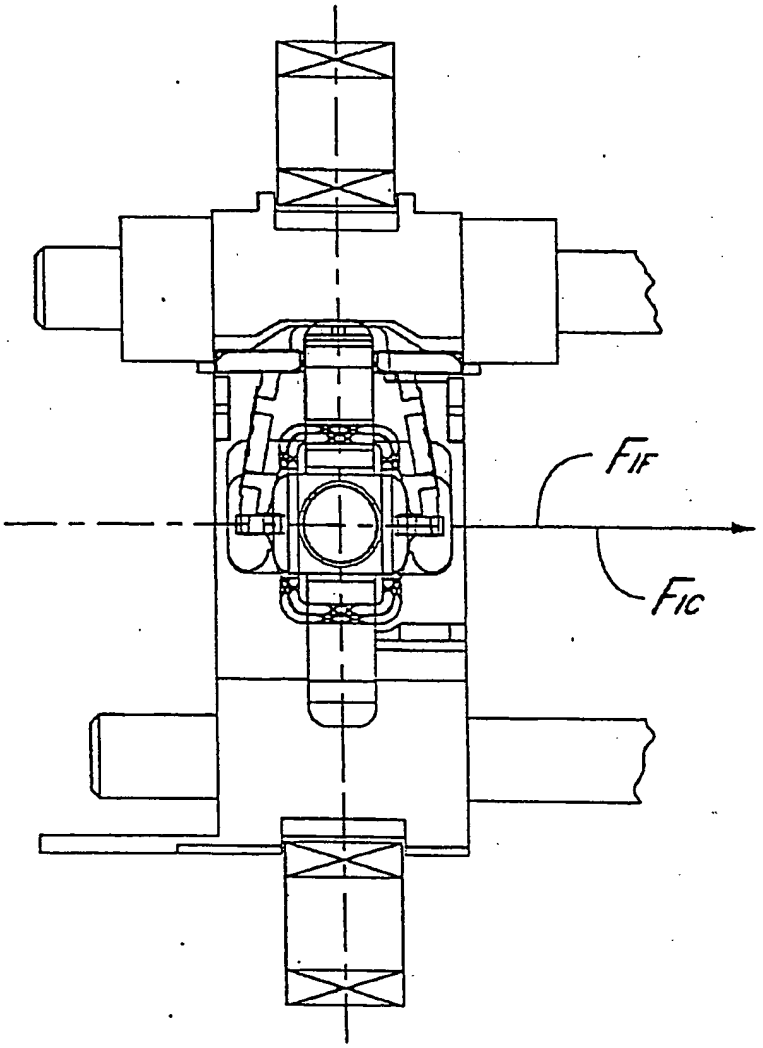

4-144
FIC2
4-140
4-166
4-114
4-246
4-260
4-262
Lc
Lp
Lp
0
Lc
4-164
4-112
4-244
4-138
FIC2
4-142

FIG. 19a

59a



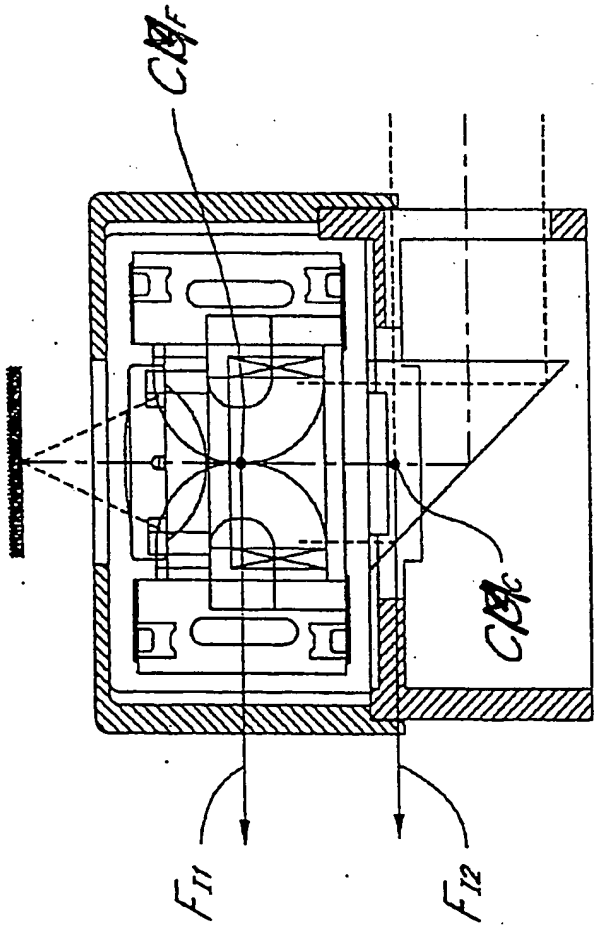
58

FIG. 19b

59b.

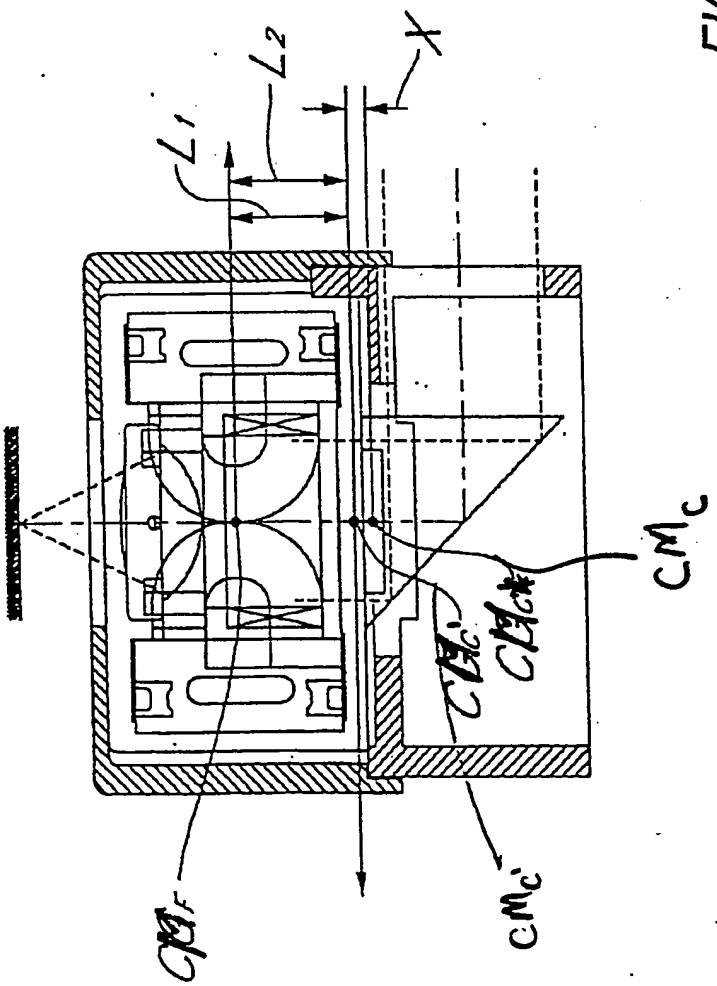
FIG. 20a

60a



54

FIG. 20b



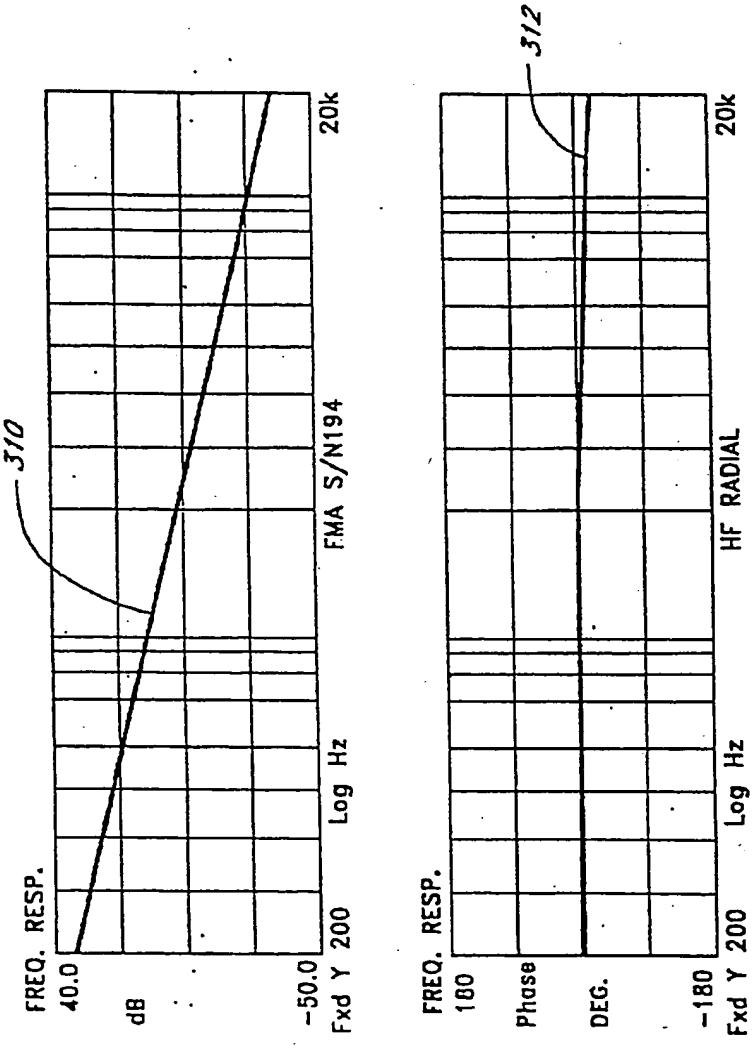


FIG. 21a

61a

61

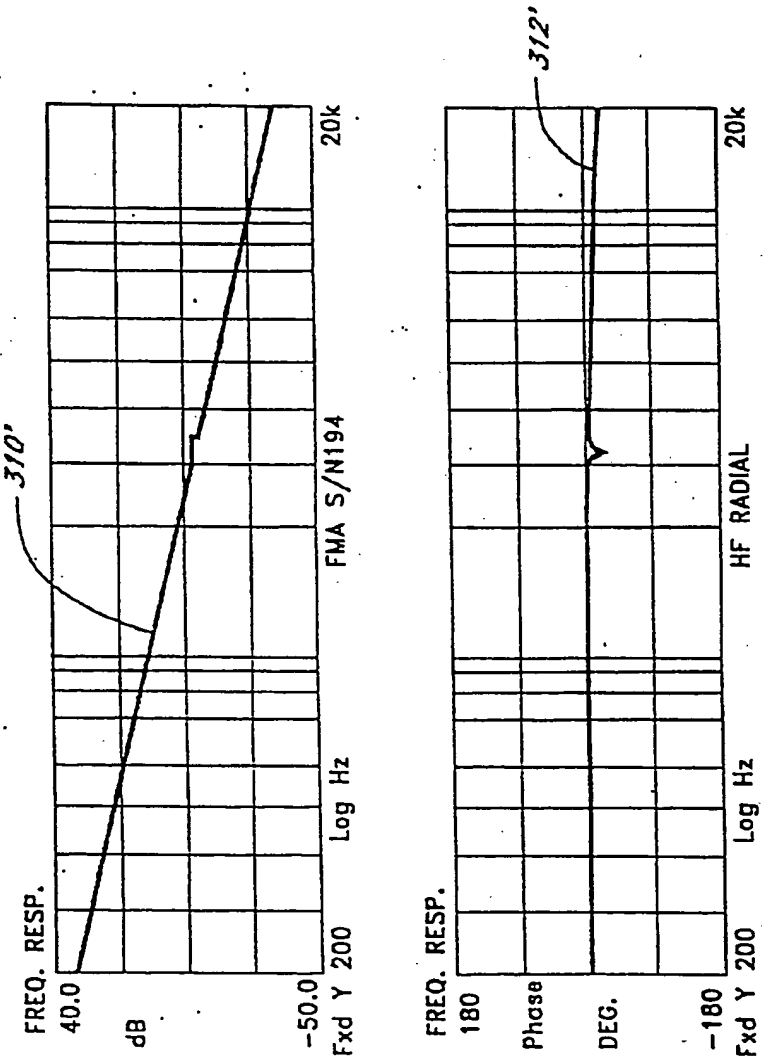

310'
FREQ. RESP.
40.0
dB
-50.0
Fxd Y 200
Log Hz
20k
FMA S/N194
312'
FREQ. RESP.
180
Phase
DEG.
-180
Fxd Y 200
Log Hz
20k
HF RADIAL

FIG. 2B

62

616

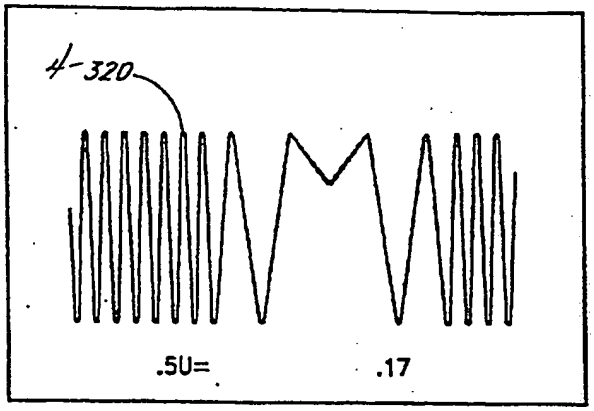


FIG. 22a
62a

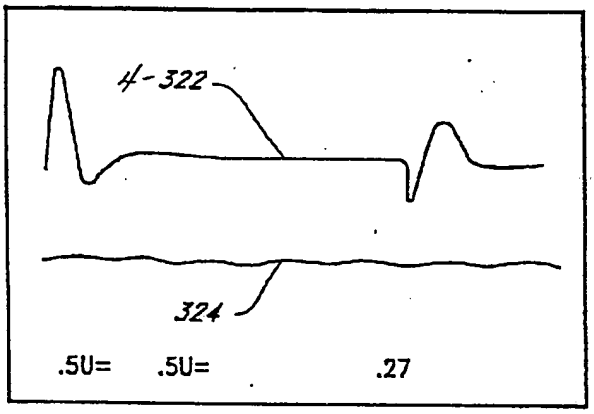


FIG. 22b
62b

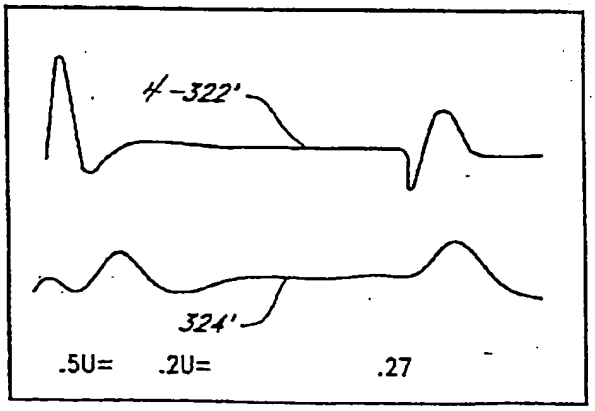


FIG. 22c
62c

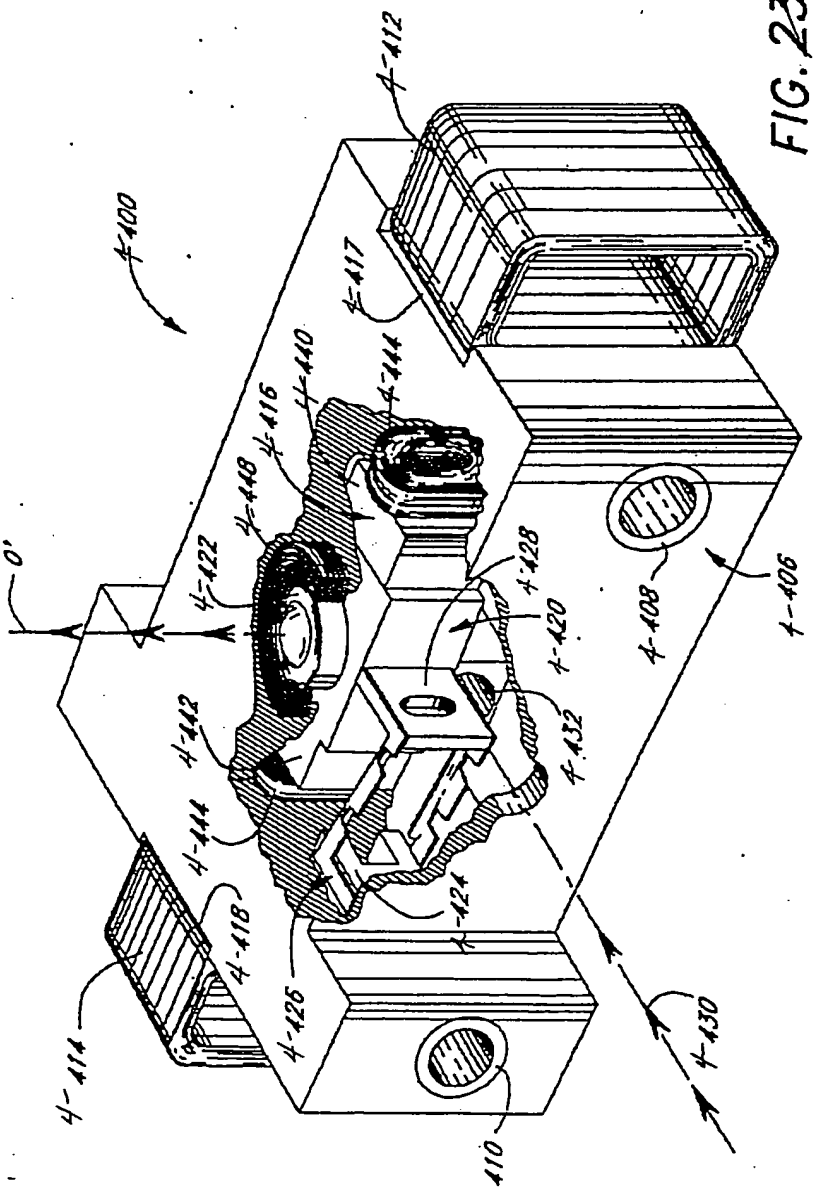

4-412
4-417
4-400
4-444
4-440
4-416
4-448
4-422
0'
4-428
4-420
4-408
4-406
4-442
4-432
4-444
4-424
4-418
4-426
4-414
4-430
410

FIG. 23

63

64

FIG. 24

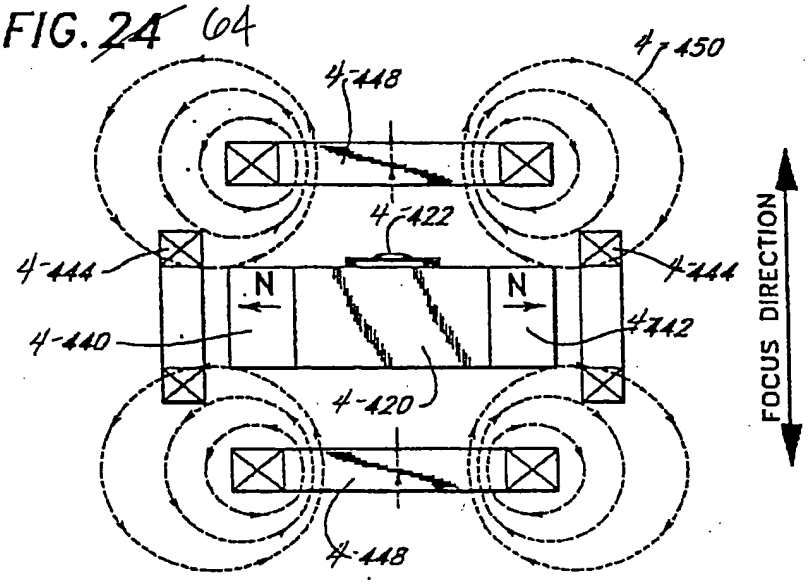

64
4-450
4-448
4-422
4-444
4-444
N
N
4-440
4-442
4-420
4-448
FOCUS DIRECTION

FIG. 25

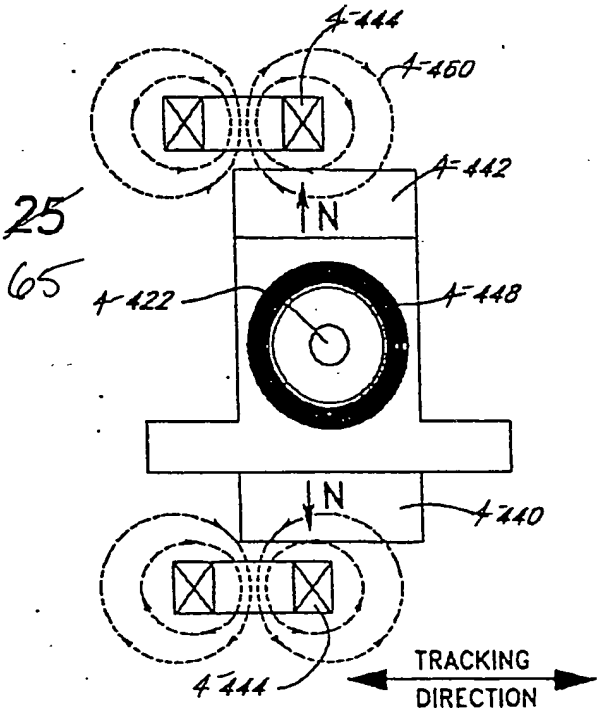

65
4-444
4-460
4-442
N
4-422
4-448
N
4-440
4-444
TRACKING
DIRECTION

FIG. 1 (PRIOR ART)

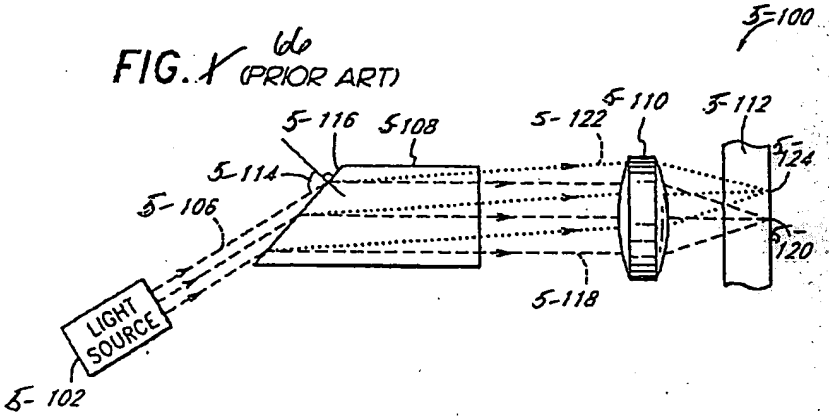
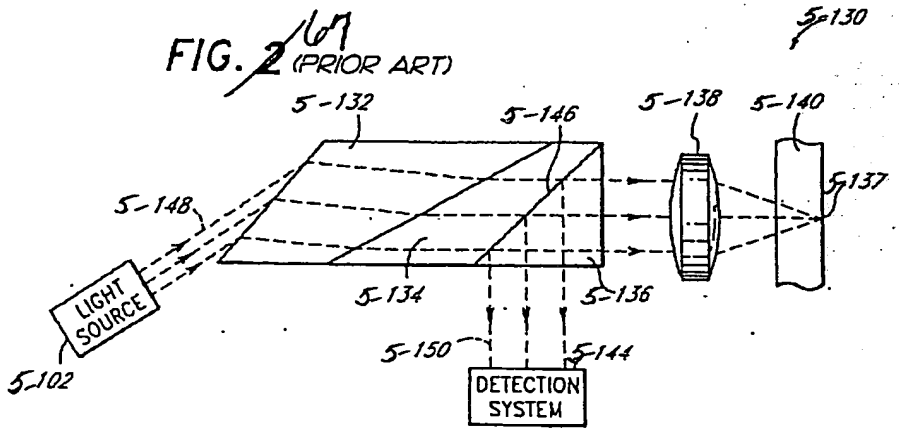


FIG. 2 (PRIOR ART)



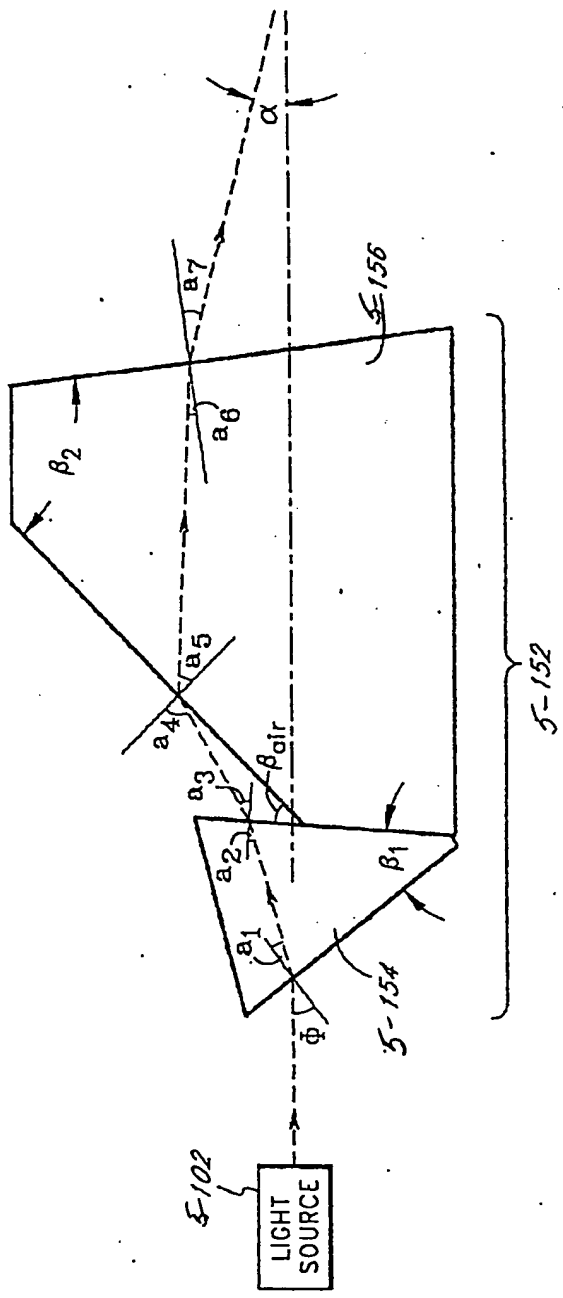


FIG. 3B

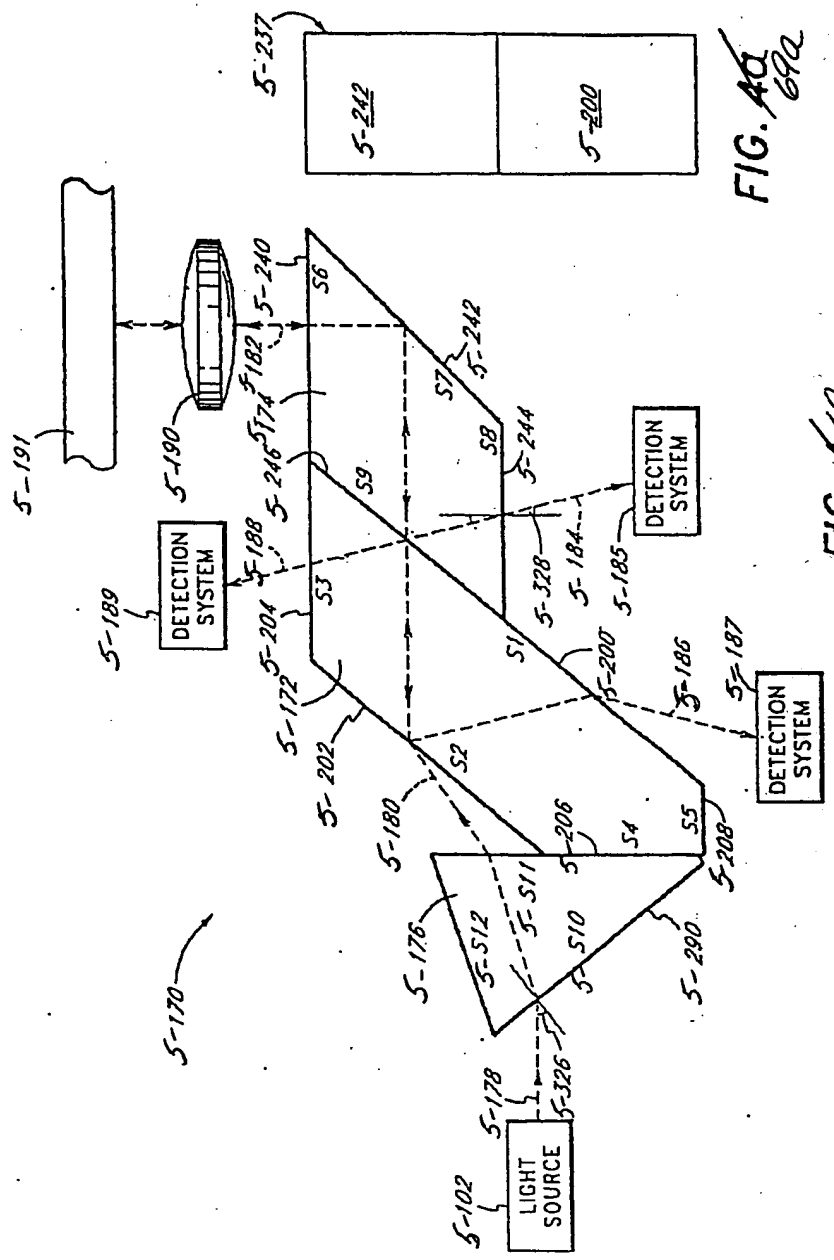


FIG. 4A

FIG. 4B

FIG. 5a 70a

FIG. 5 70

FIG. 5b 70b

FIG. 6a
7/a

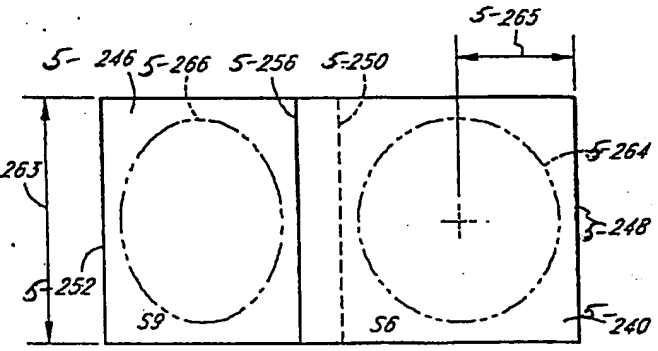


FIG. 6 7/

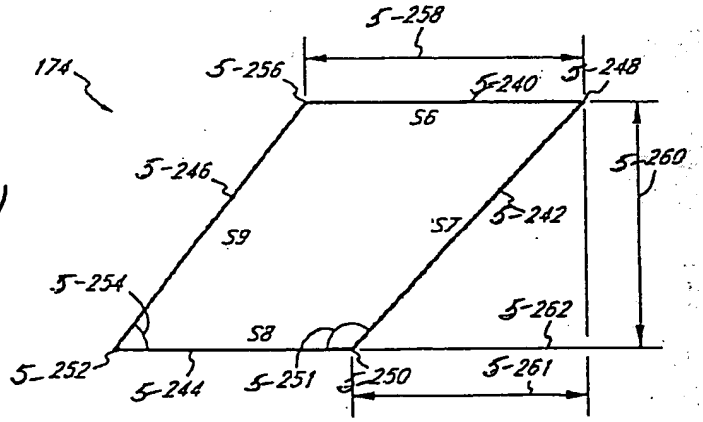
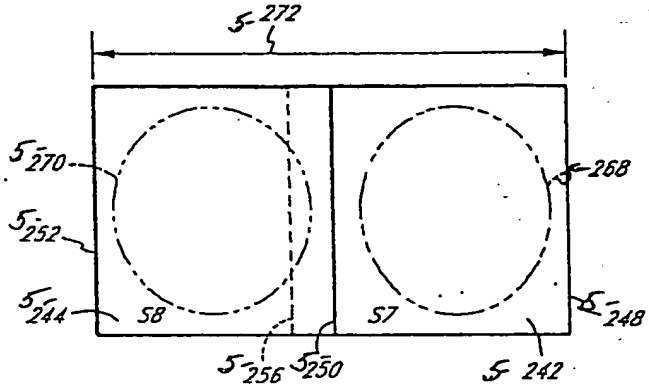


FIG. 6b
7/b



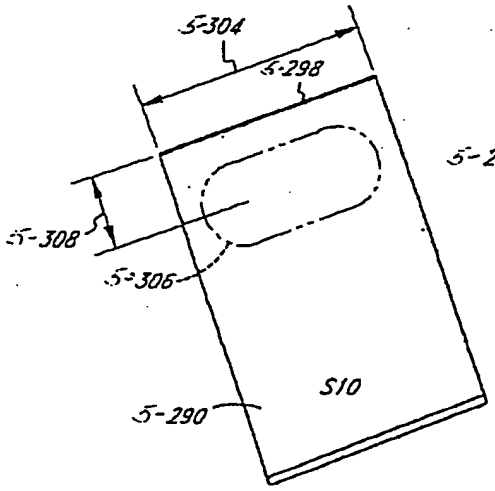


FIG. 7a
72a

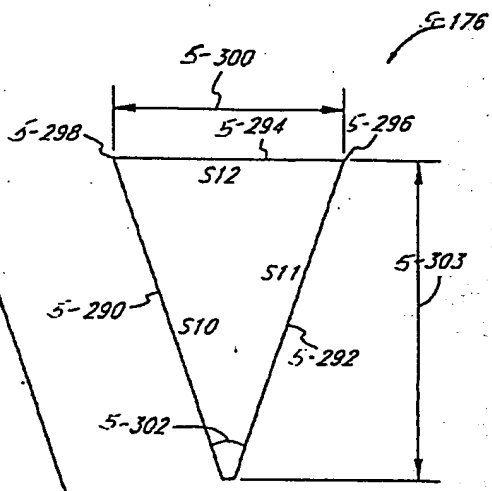


FIG. 7 72

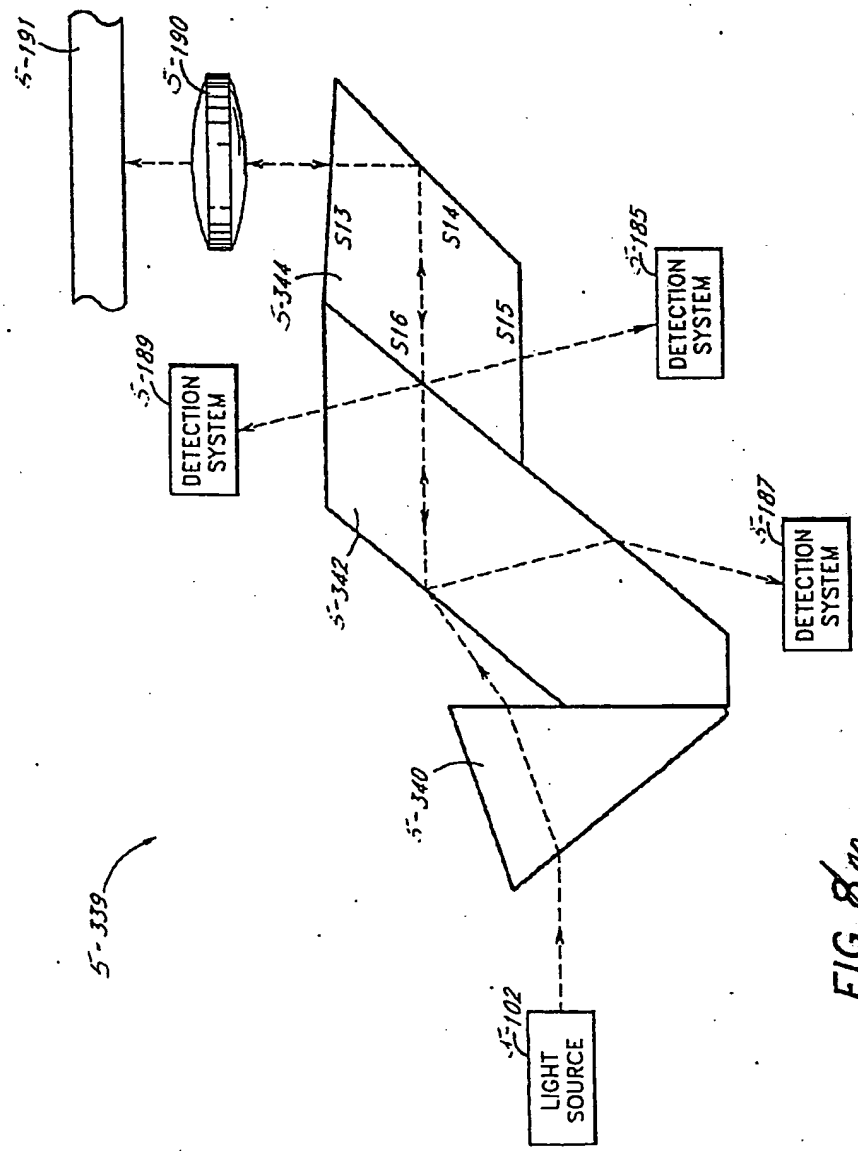

5-191
5-190
S13
S14
5-344
S16
S15
5-185
DETECTION SYSTEM
5-189
DETECTION SYSTEM
5-187
DETECTION SYSTEM
5-342
5-340
5-339
5-102
LIGHT SOURCE

FIG. 8B

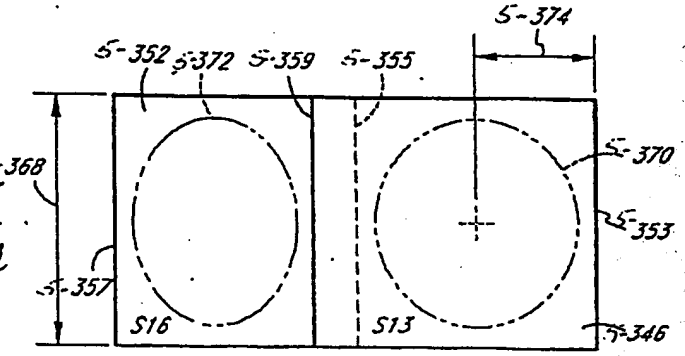


FIG. 9a
74a

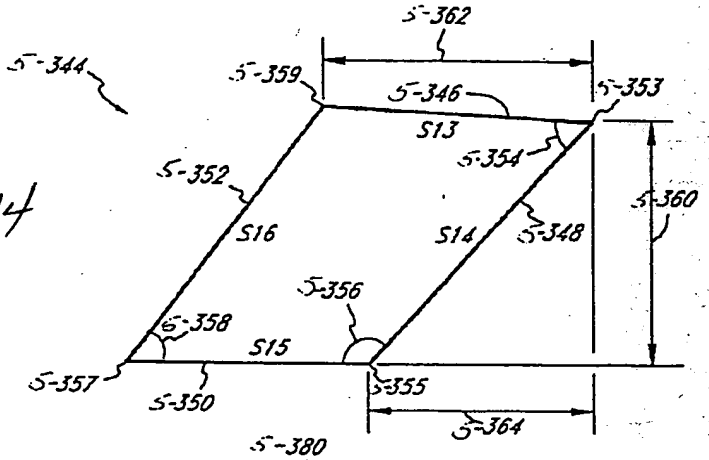


FIG. 9b

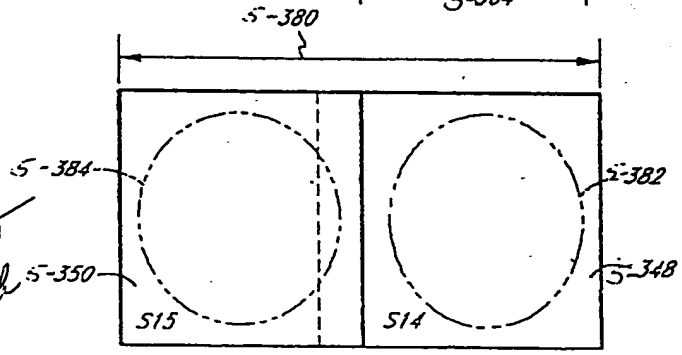


FIG. 9b
74b

Fig. 75

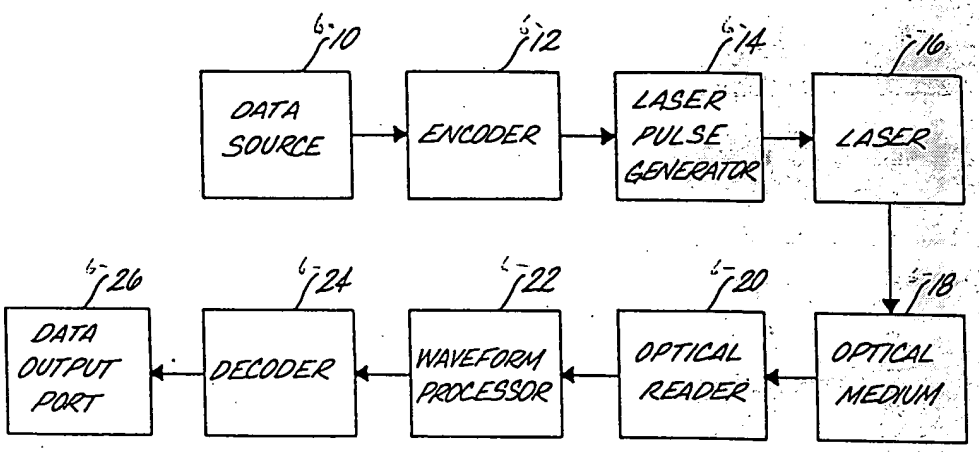

10
DATA SOURCE
12
ENCODER
14
LASER PULSE GENERATOR
16
LASER
18
OPTICAL MEDIUM
20
OPTICAL READER
22
WAVEFORM PROCESSOR
24
DECODER
26
DATA OUTPUT PORT

Fig. 2 ~~Fig~~ 7b

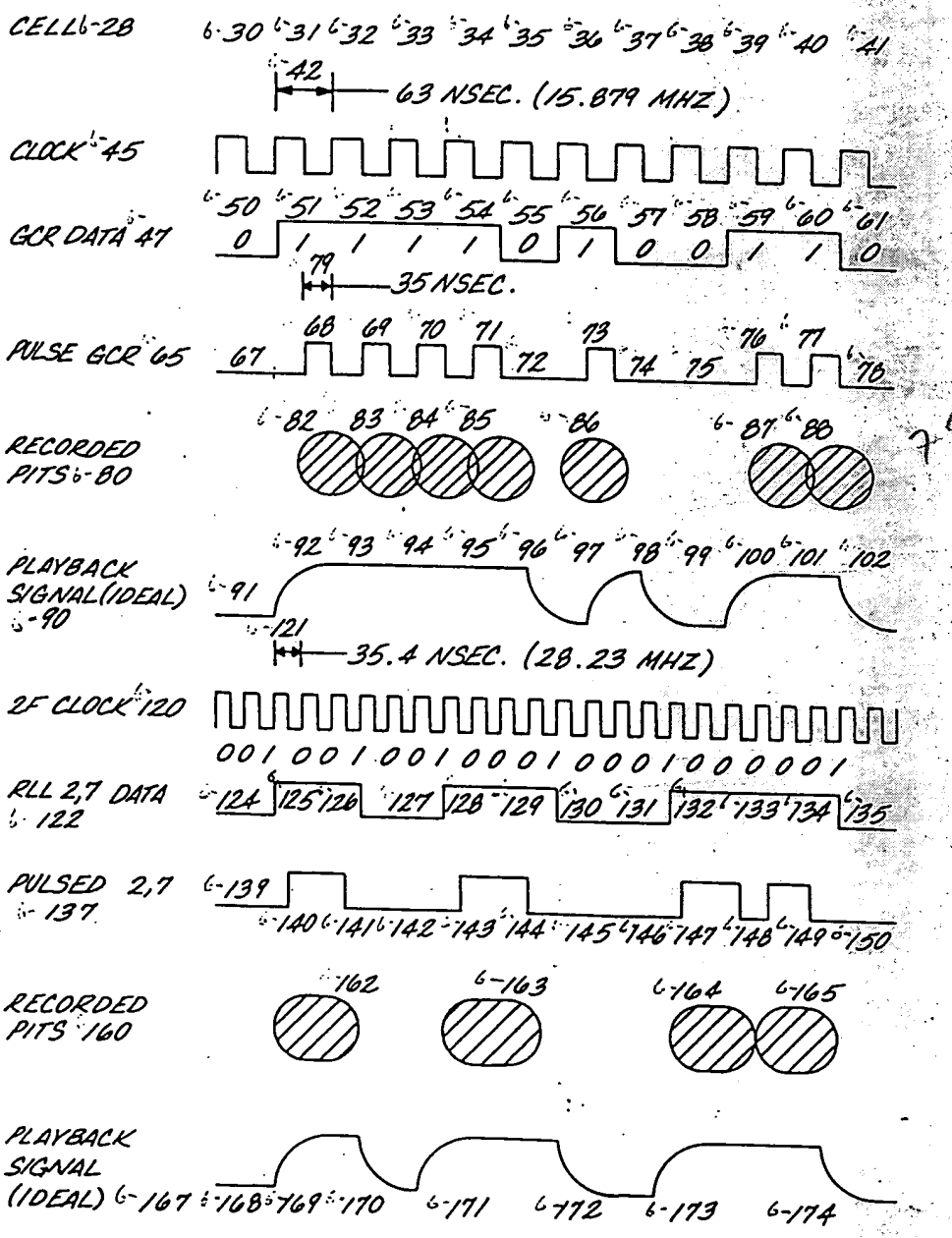

CELL 28
30 31 32 33 34 35 36 37 38 39 40 41
42
63 NSEC. (15.879 MHZ)
CLOCK 45
GCR DATA 47
50 51 52 53 54 55 56 57 58 59 60 61
0 1 1 1 1 0 1 0 0 1 1 0
79
35 NSEC.
PULSE GCR 65
67 68 69 70 71 72 73 74 75 76 77 78
RECORDED PITS 80
82 83 84 85 86 87 88
PLAYBACK SIGNAL (IDEAL) 90
91 92 93 94 95 96 97 98 99 100 101 102
121
35.4 NSEC. (28.23 MHZ)
2F CLOCK 120
0 0 1 0 0 1 0 0 1 0 0 0 1 0 0 0 1 0 0 0 0 0 1
RLL 2,7 DATA 122
124 125 126 127 128 129 130 131 132 133 134 135
PULSED 2,7 137
139 140 141 142 143 144 145 146 147 148 149 150
RECORDED PITS 160
162 163 164 165
PLAYBACK SIGNAL (IDEAL)
167 168 169 170 171 172 173 174

Fig. 6 #77

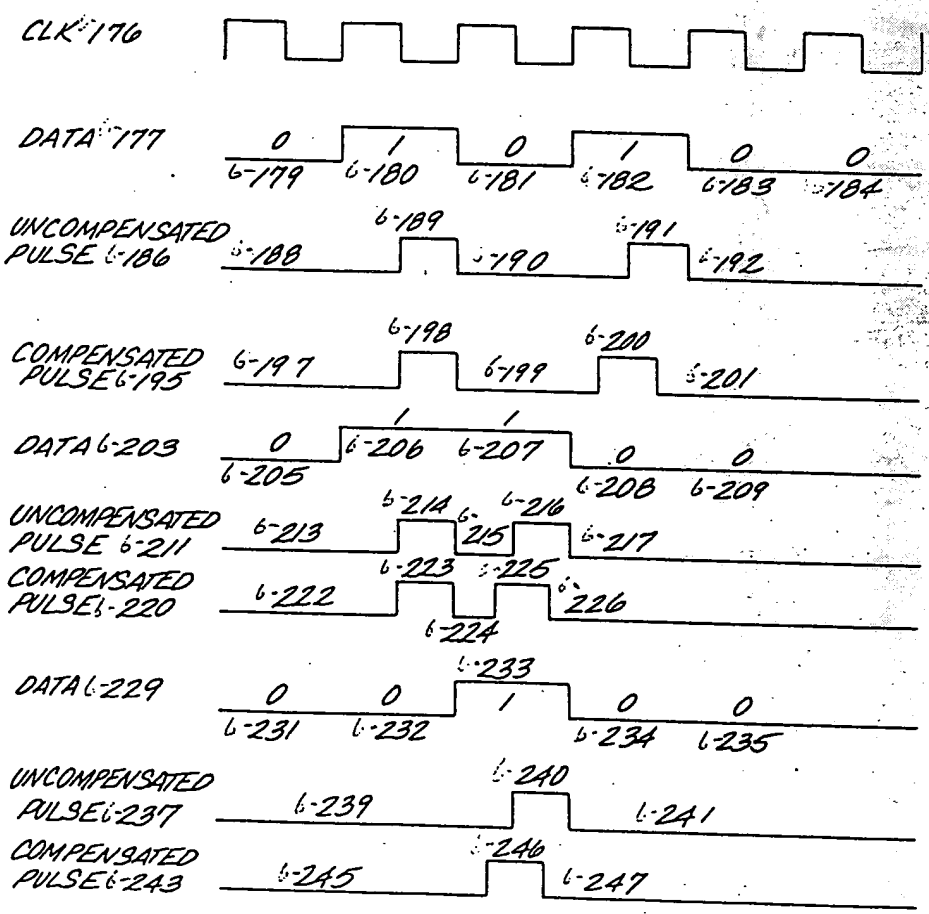

CLK 176
DATA 177
0
1
0
1
0
0
6-179
6-180
6-181
6-182
6-183
6-184
76
UNCOMPENSATED PULSE 6-186
6-188
6-189
6-190
6-191
6-192
COMPENSATED PULSE 6-195
6-197
6-198
6-199
6-200
6-201
DATA 6-203
0
1
1
0
0
6-205
6-206
6-207
6-208
6-209
UNCOMPENSATED PULSE 6-211
6-213
6-214
6-215
6-216
6-217
COMPENSATED PULSE 6-220
6-222
6-223
6-224
6-225
6-226
DATA 6-229
0
0
1
0
0
6-231
6-232
6-233
6-234
6-235
UNCOMPENSATED PULSE 6-237
6-239
6-240
6-241
COMPENSATED PULSE 6-243
6-245
6-246
6-247

FIG. 7A

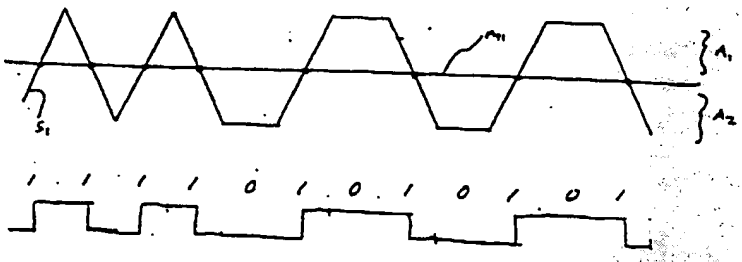
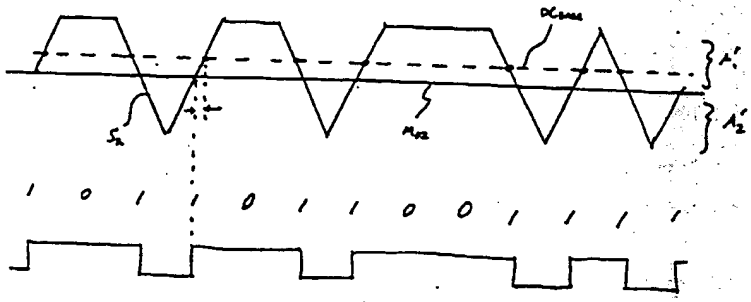


FIG. 7B



77

FIG. 4

78

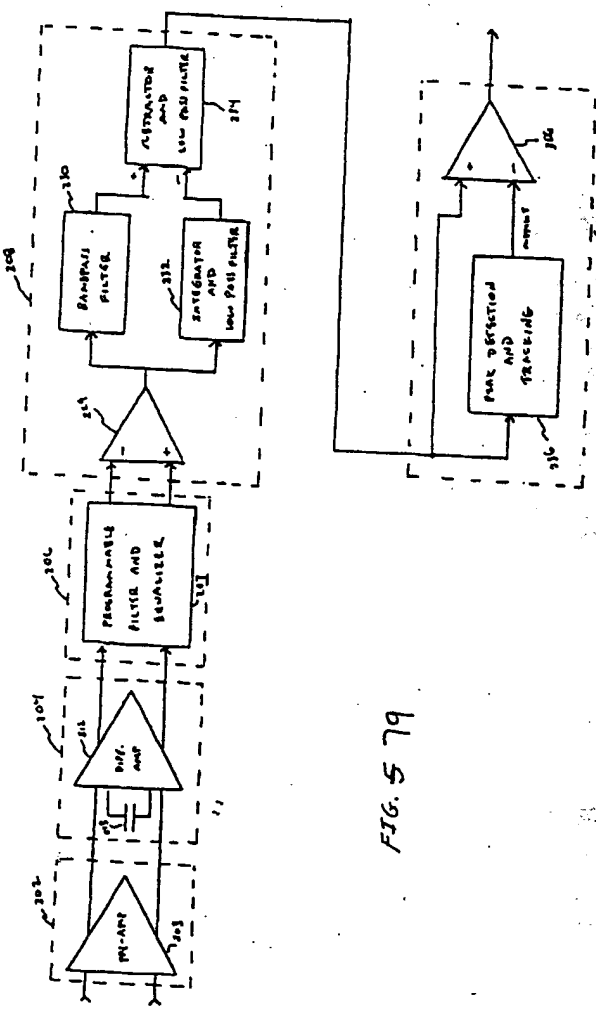


FIG. 5 of 9

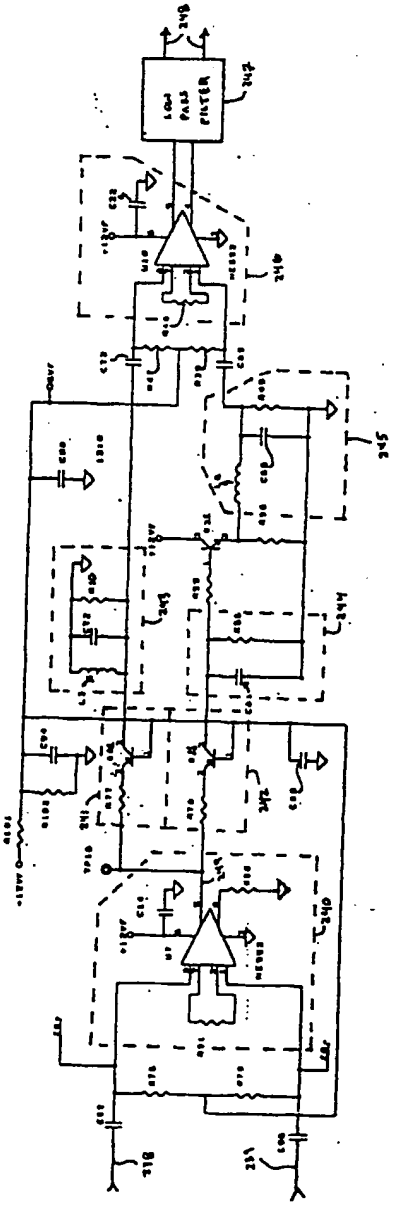


FIGURE 5B 79 B

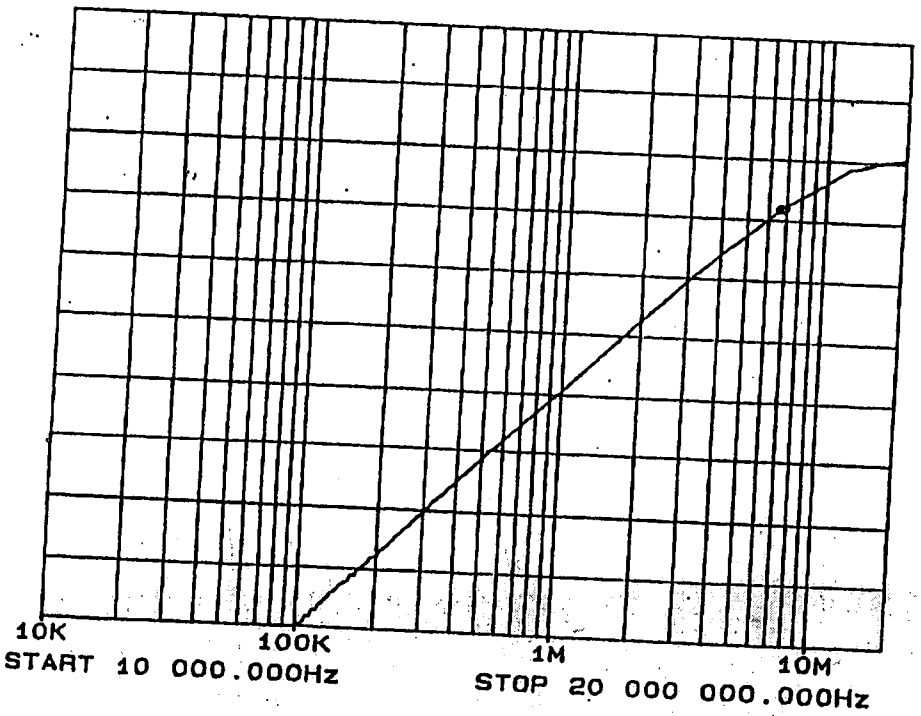
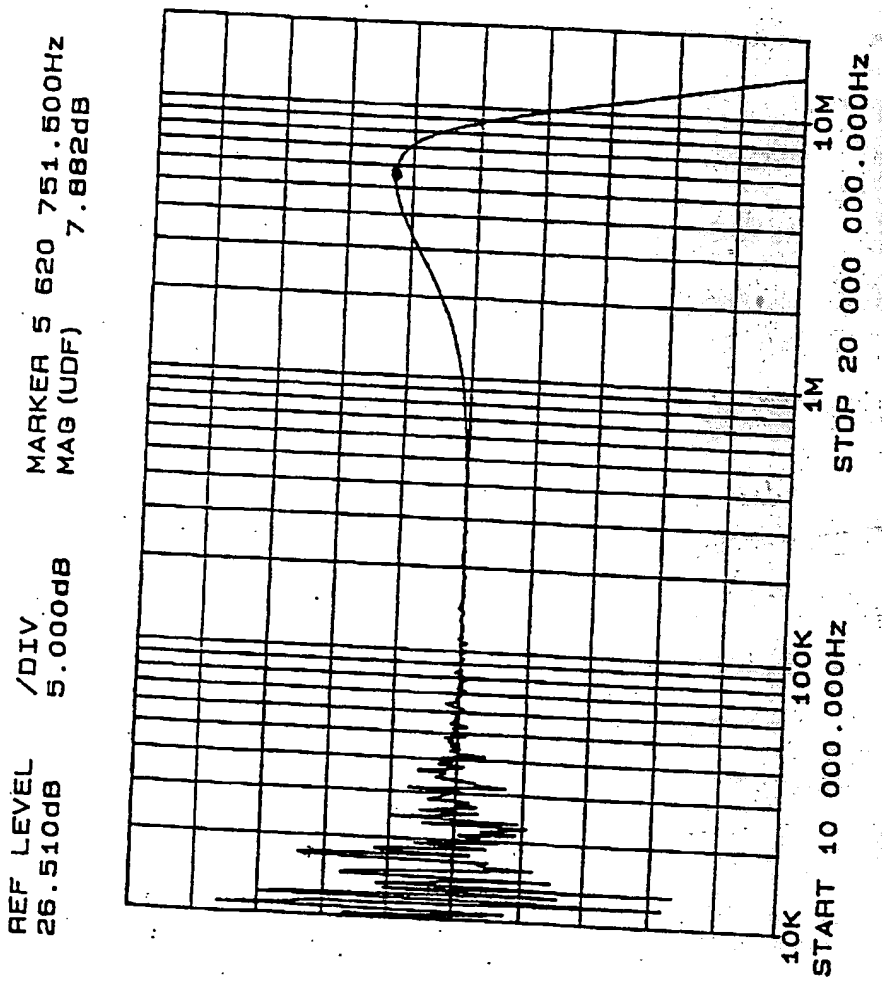


FIG. 6A-80A
DIFFERENTIATOR

FIG. 4B. 80B
EQUA-12E2



REF LEVEL 9.600dB /DIV 6.000dB MARKER 5 105 657.600Hz MAG (UDF) -26.669dB

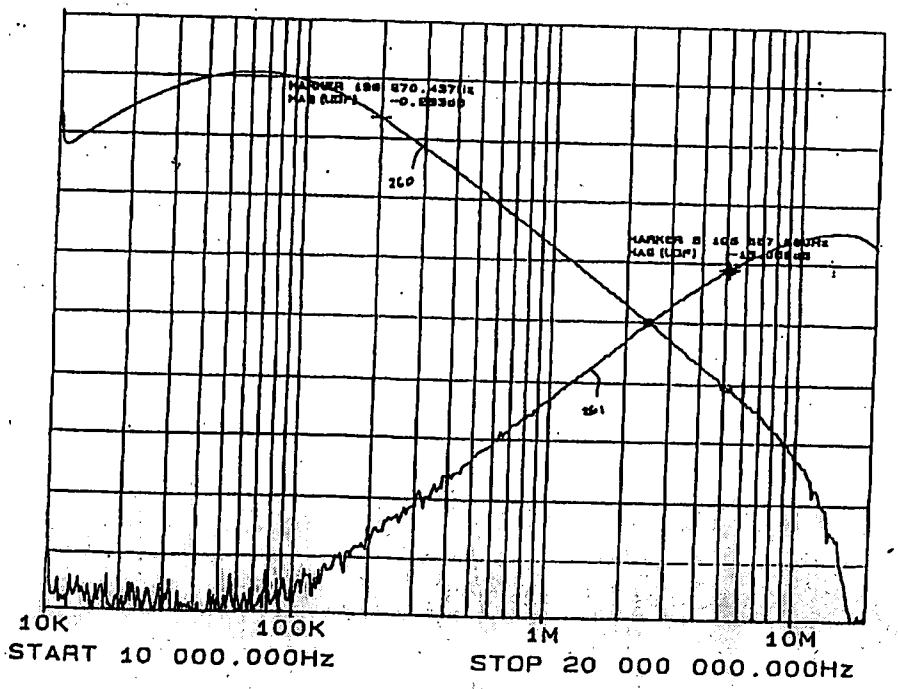


FIG. 6C 80 C
ISOLATOR AND BAND-PASS

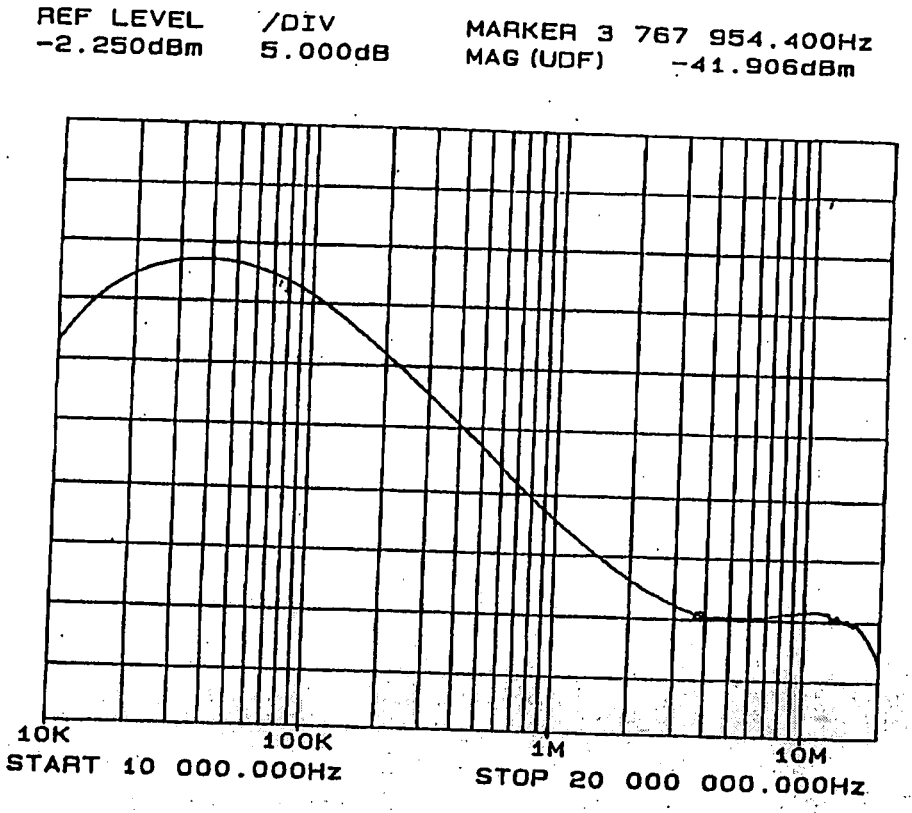


FIG. 6D 80 D

PARTIAL INTEGRATION STAGE

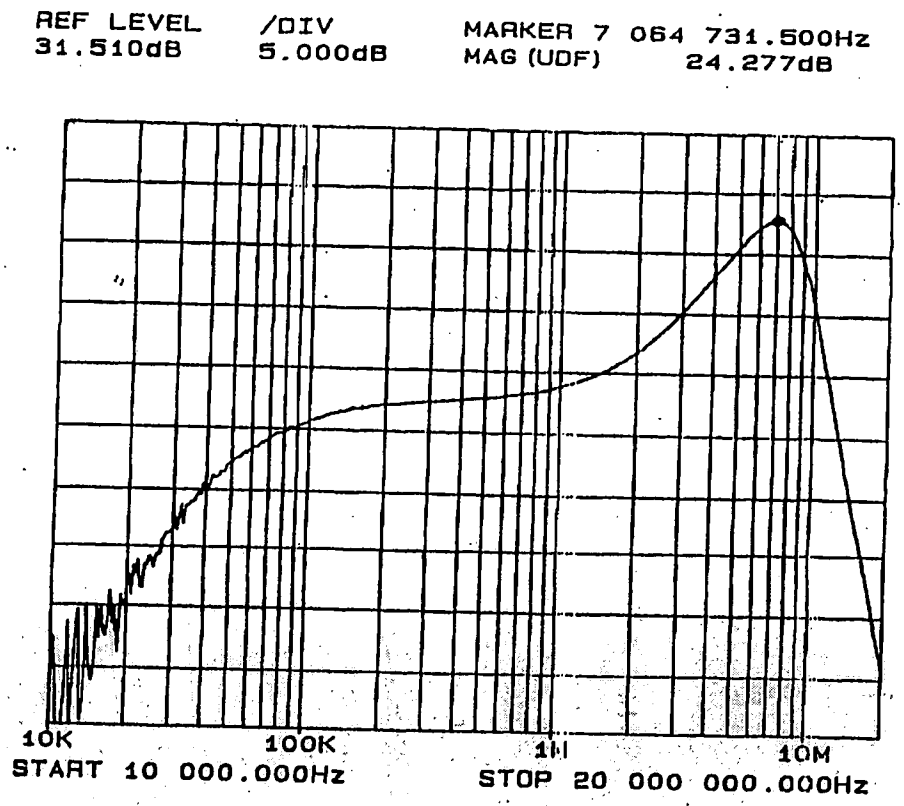
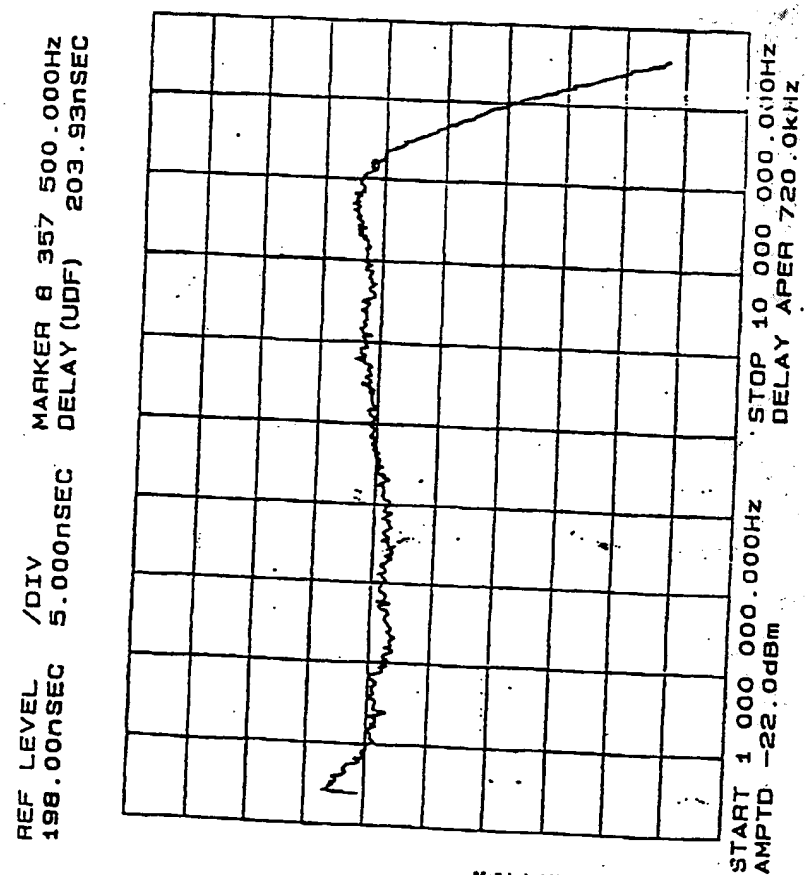


FIG. 6E 80E
DEF RESPONSE

Total Group Delay

FIG. 6F 80 F



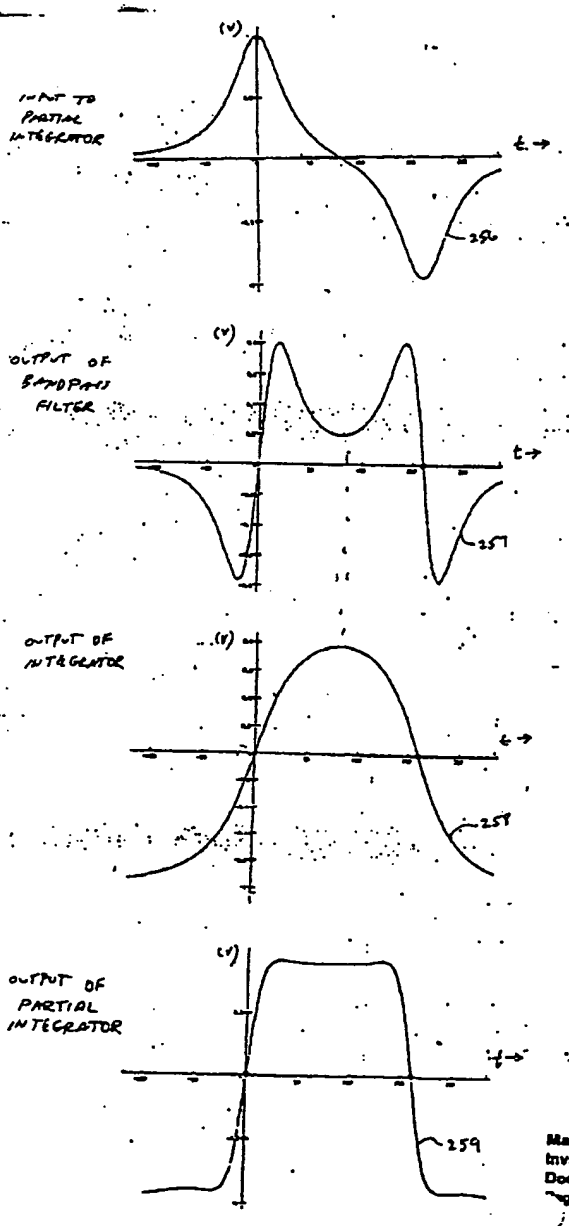

INPUT TO PARTIAL INTEGRATOR
(V)
t→
256
OUTPUT OF BANDPASS FILTER
(V)
t→
257
OUTPUT OF INTEGRATOR
(V)
t→
258
OUTPUT OF PARTIAL INTEGRATOR
(V)
t→
259

Fig. 6G .80 G

87

FIG. 7 81

DATA PULSE
300
302
304
306
HIP Point
R4
R4
R3
R3
Q3
Q4
5V
5V
R2
R2
GND
C1
C1
Q1
Q2
Q5
Q6
R1
R1
5V
5V
Signal
332

FIGURE 8

FIG. 4 83

48 of 91

Fig. 85

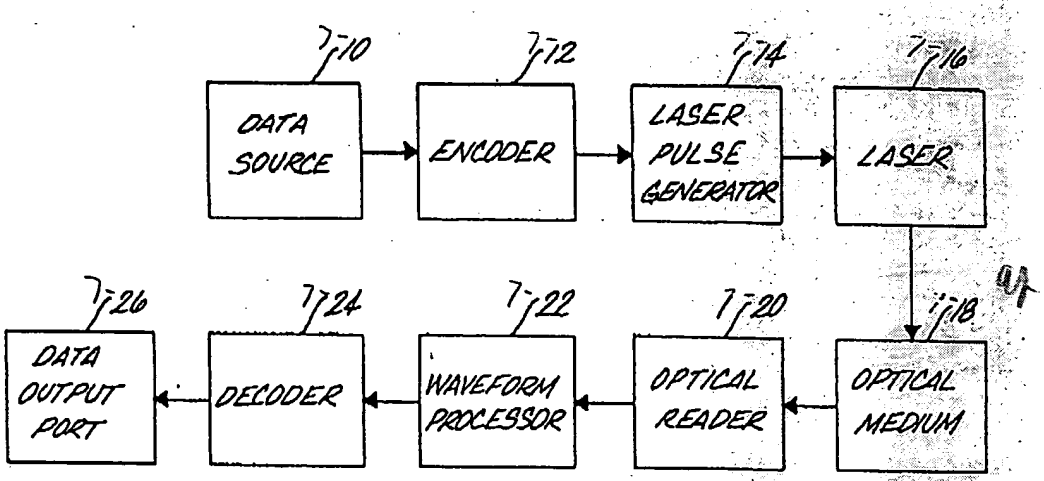

710
DATA SOURCE
712
ENCODER
714
LASER PULSE GENERATOR
716
LASER
718
OPTICAL MEDIUM
720
OPTICAL READER
722
WAVEFORM PROCESSOR
724
DECODER
726
DATA OUTPUT PORT

Fig. 86

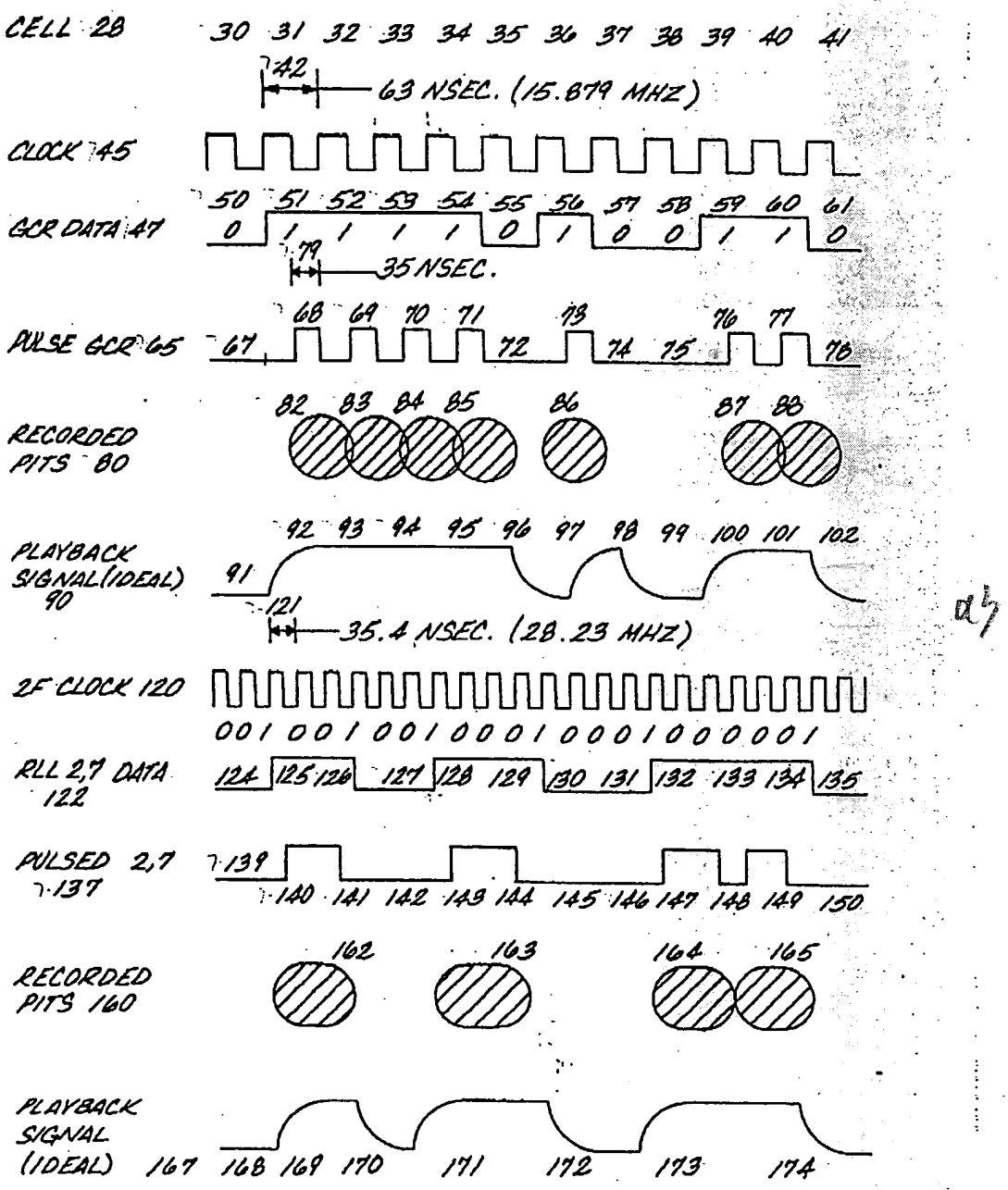
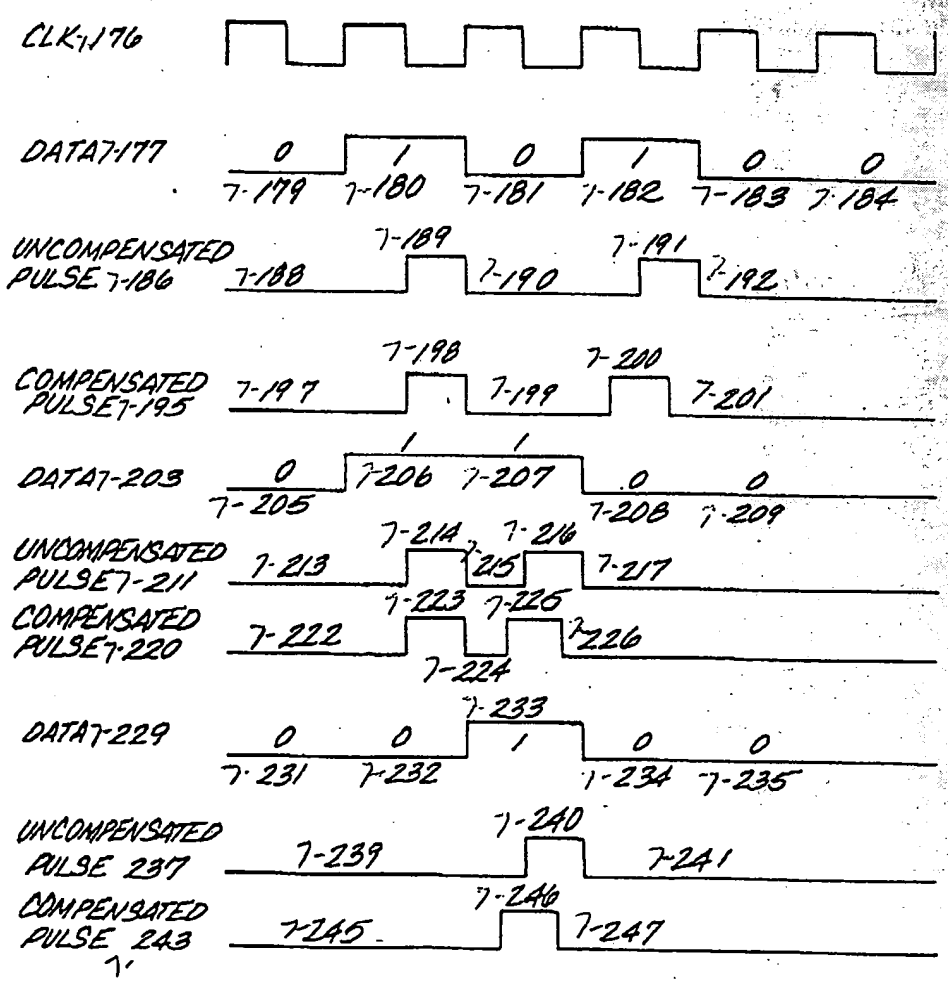

CELL 28
30 31 32 33 34 35 36 37 38 39 40 41
42
63 NSEC. (15.879 MHZ)
CLOCK 45
50 51 52 53 54 55 56 57 58 59 60 61
GCR DATA 47
0 1 1 1 1 0 1 0 0 1 1 0
79
35 NSEC.
PULSE GCR 65
67 68 69 70 71 72 73 74 75 76 77 78
RECORDED PITS 80
82 83 84 85 86 87 88
PLAYBACK SIGNAL (IDEAL) 90
91 92 93 94 95 96 97 98 99 100 101 102
121
35.4 NSEC. (28.23 MHZ)
2F CLOCK 120
001 001 001 0001 0001 000001
RLL 2,7 DATA 122
124 125 126 127 128 129 130 131 132 133 134 135
PULSED 2,7 137
139 140 141 142 143 144 145 146 147 148 149 150
RECORDED PITS 160
162 163 164 165
PLAYBACK SIGNAL (IDEAL) 167
168 169 170 171 172 173 174

Fig. 8 87


CLK 7-176
DATA 7-177
0
1
0
1
0
0
7-179
7-180
7-181
7-182
7-183
7-184
UNCOMPENSATED PULSE 7-186
7-188
7-189
7-190
7-191
7-192
COMPENSATED PULSE 7-195
7-197
7-198
7-199
7-200
7-201
DATA 7-203
0
1
1
0
0
7-205
7-206
7-207
7-208
7-209
UNCOMPENSATED PULSE 7-211
7-213
7-214
7-215
7-216
7-217
COMPENSATED PULSE 7-220
7-222
7-223
7-224
7-226
7-226
DATA 7-229
0
0
1
0
0
7-231
7-232
7-233
7-234
7-235
UNCOMPENSATED PULSE 237
7-239
7-240
7-241
COMPENSATED PULSE 243
7-245
7-246
7-247

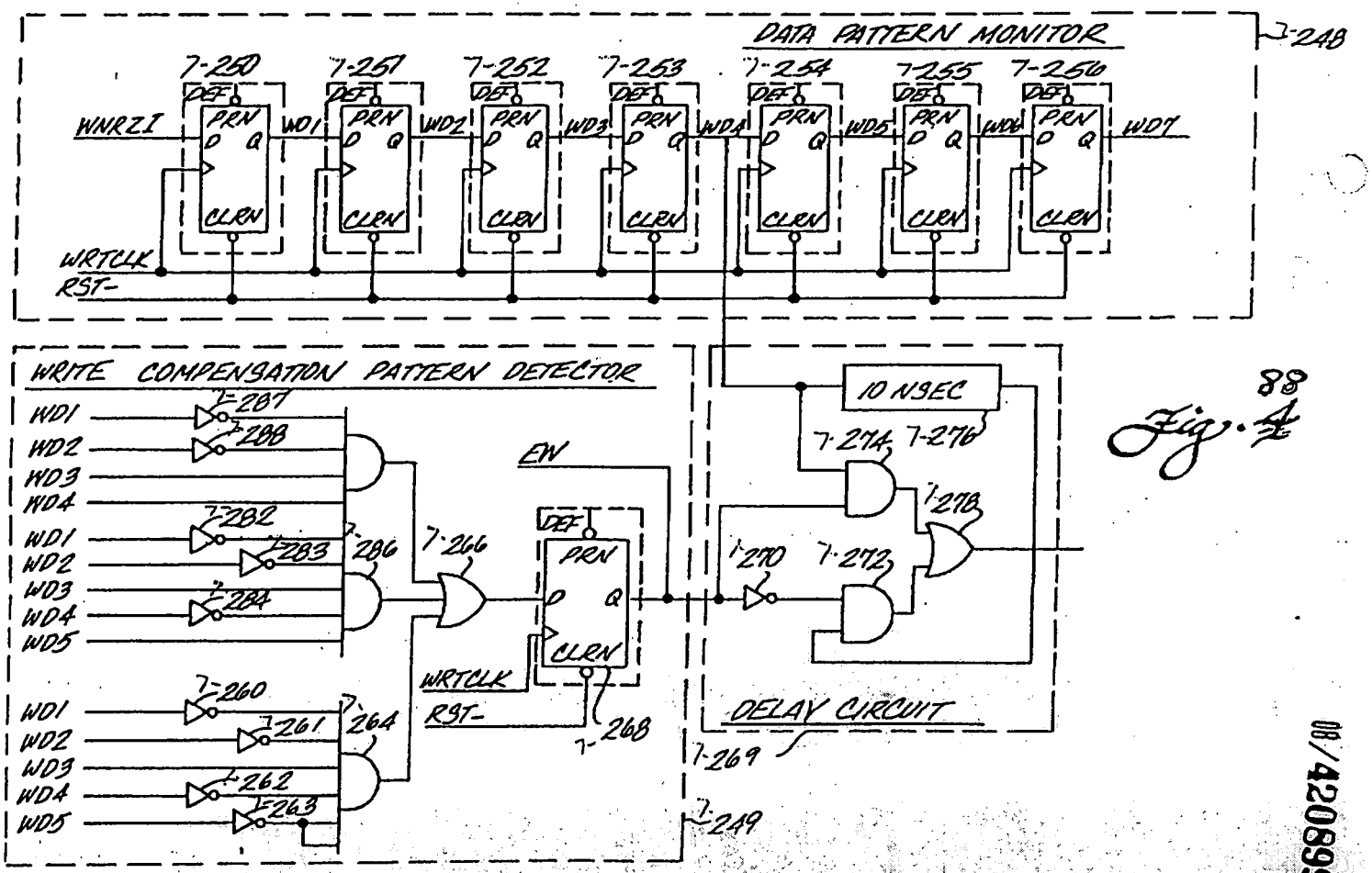


Fig. 4

88

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Fig. 5 89

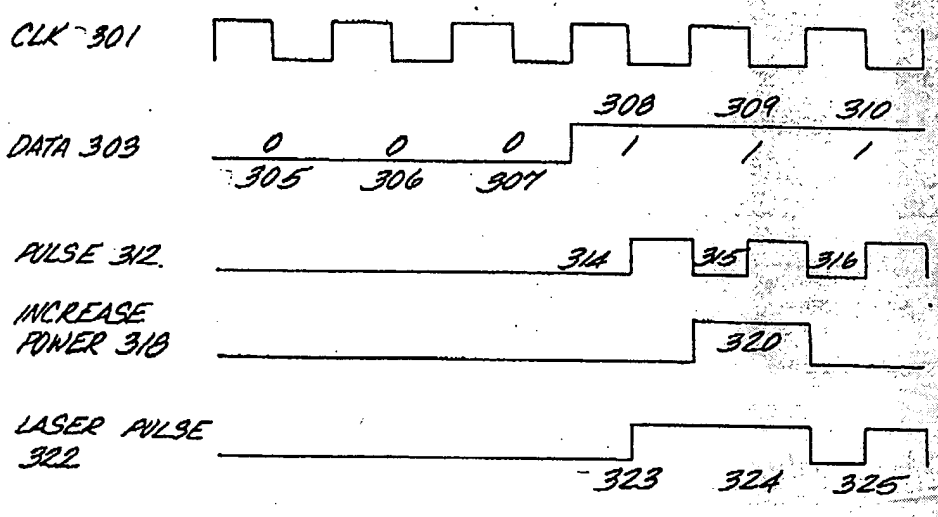


Fig. 7 91

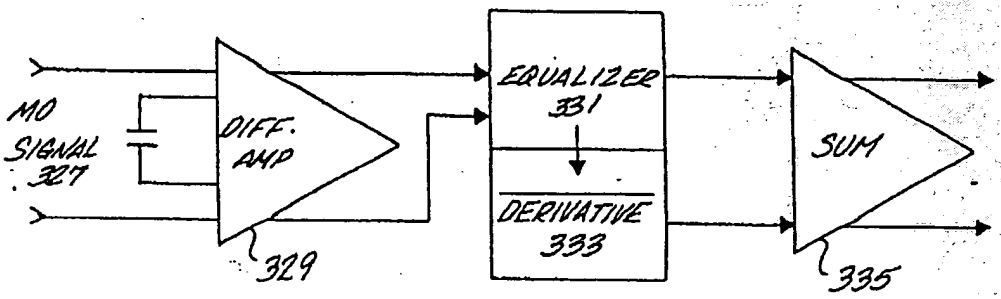
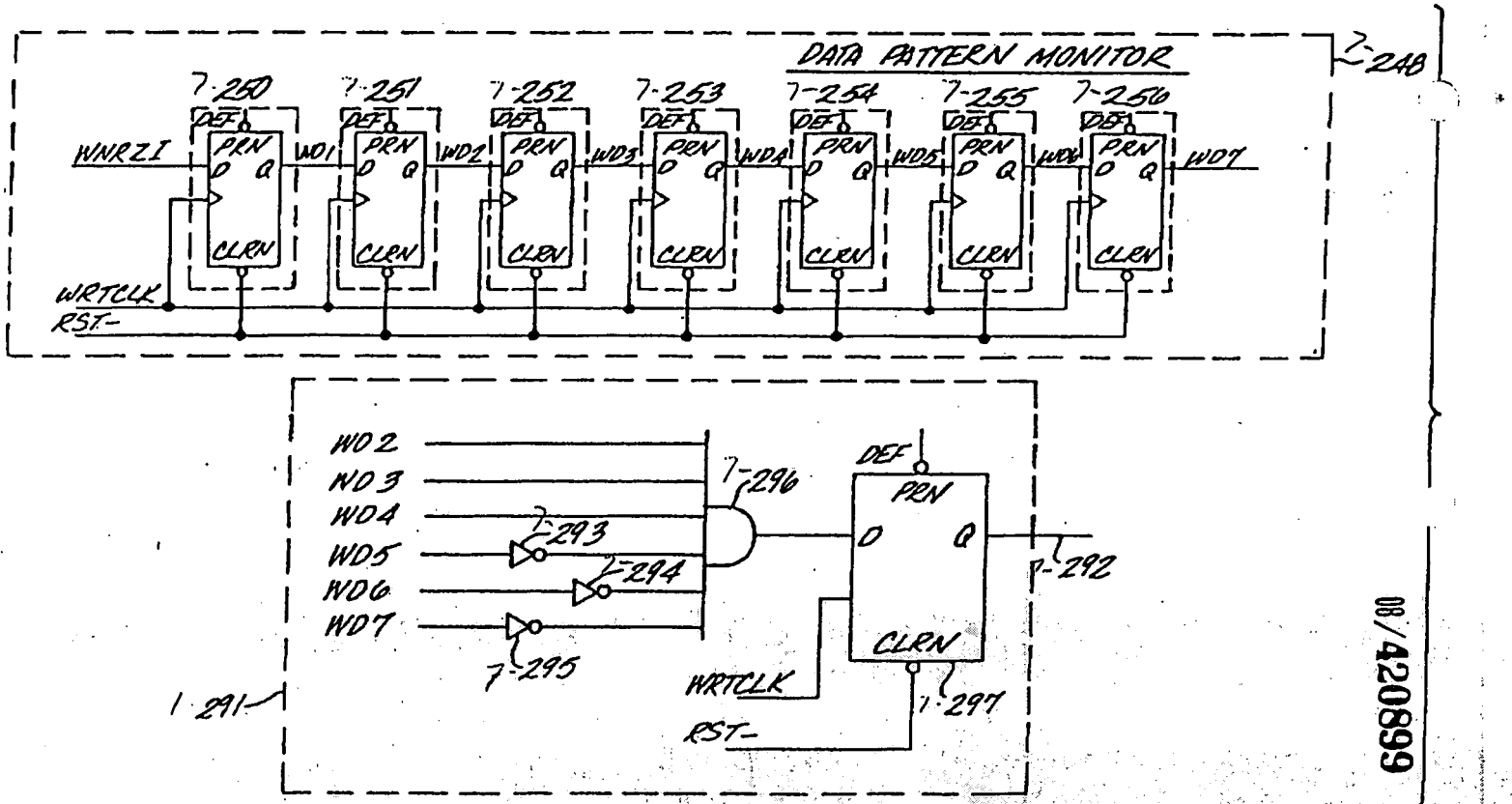
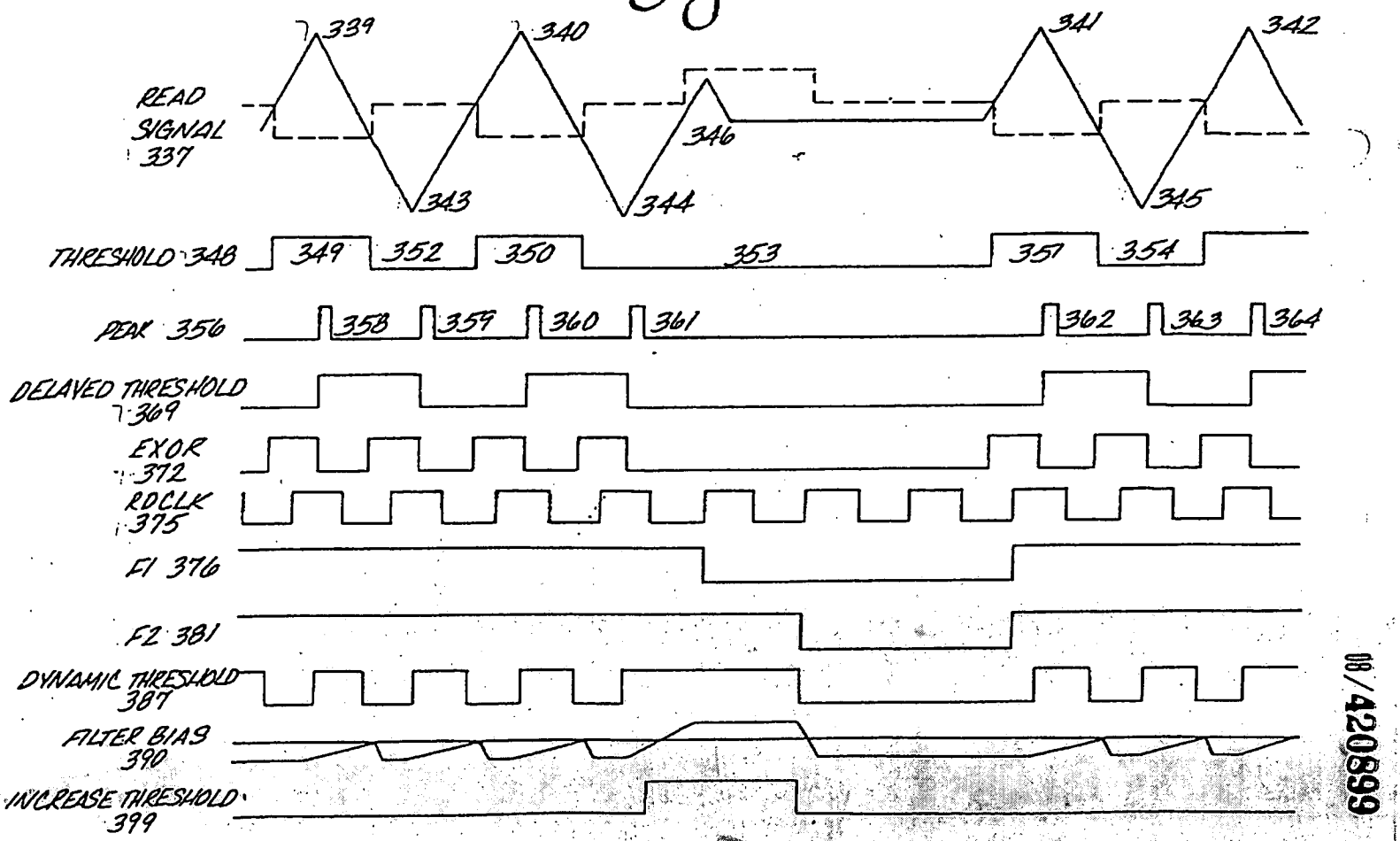


Fig. 16 90



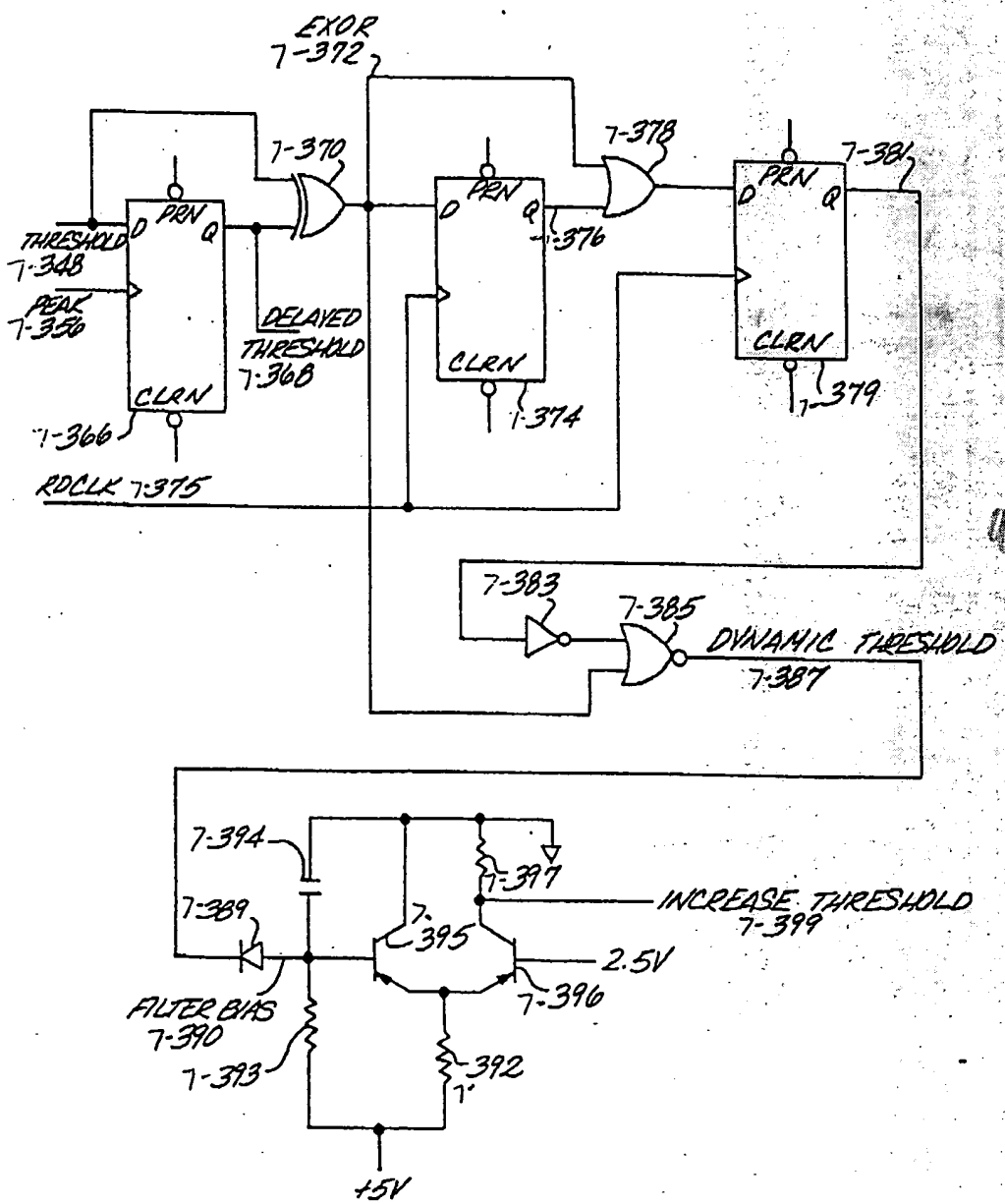
08/420899

Fig. 8 92



08/420,899

Fig. 93


EXOR
7-372
7-370
7-378
7-381
PRN
D
Q
THRESHOLD
7-348
PEAK
7-350
DELAYED
THRESHOLD
7-368
7-376
CLRN
7-374
7-379
7-366
RDCLK 7-375
7-383
7-385
DYNAMIC THRESHOLD
7-387
7-394
7-397
INCREASE THRESHOLD
7-399
7-389
7-395
2.5V
7-396
FILTER BIAS
7-390
7-393
7-392
+5V

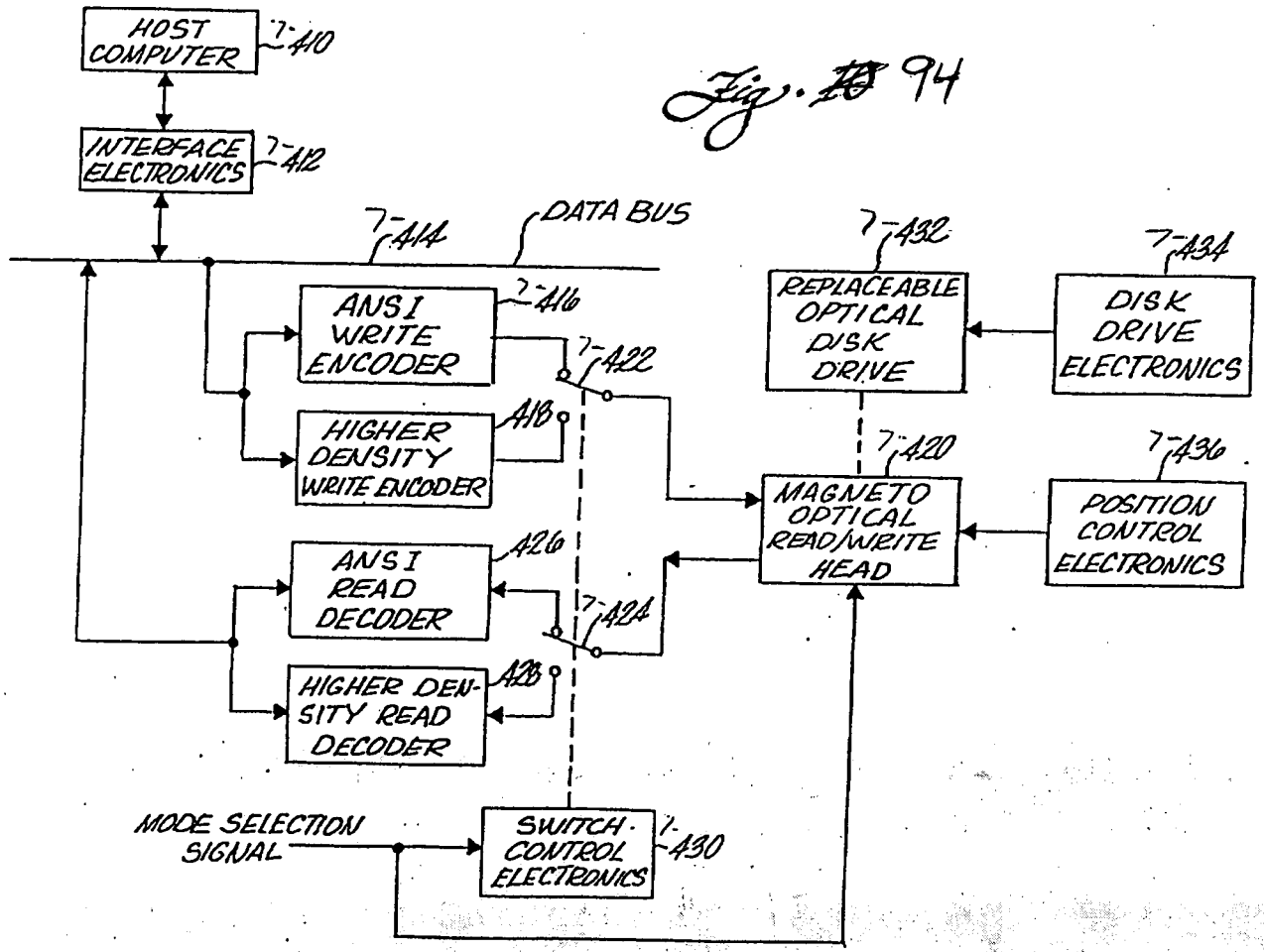


Fig. ~~10~~ 94

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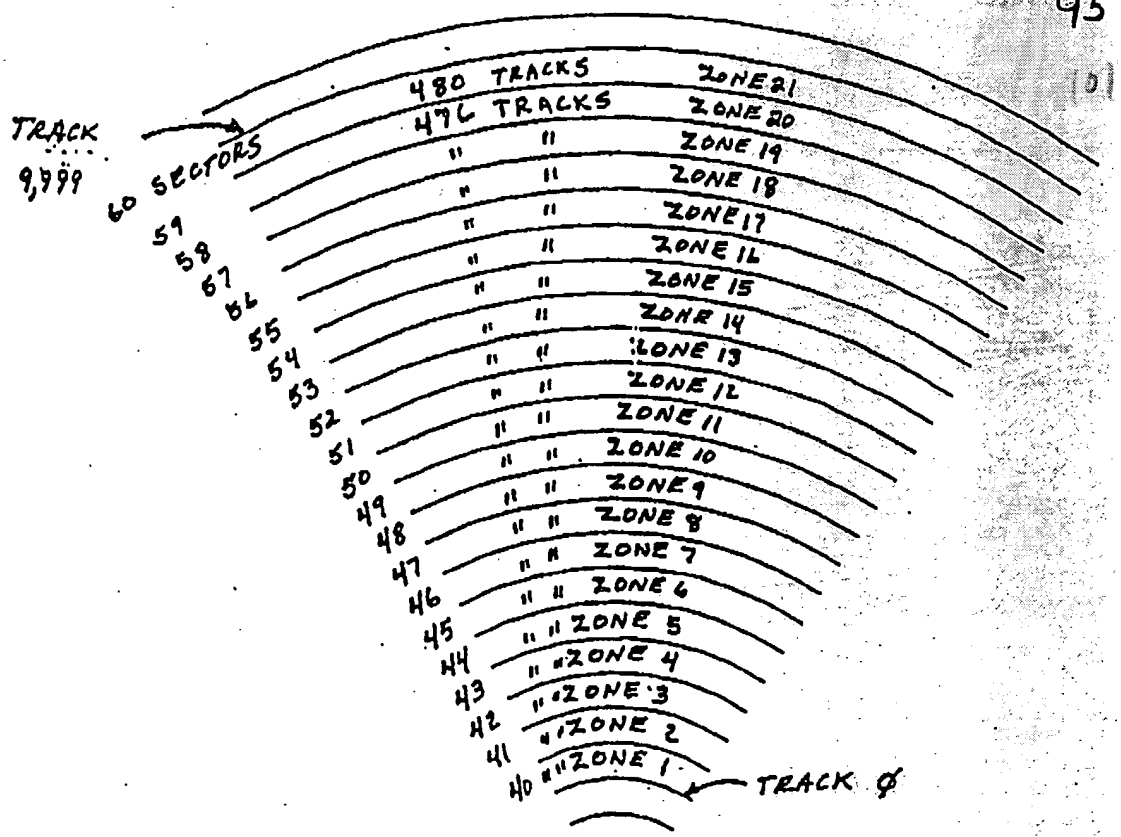
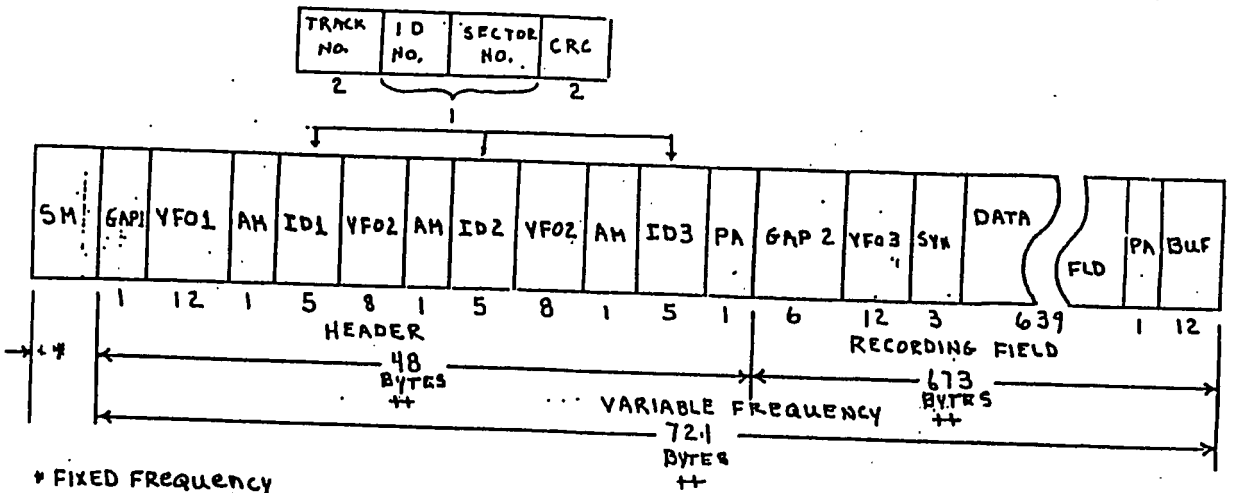
TRACK LAYOUT

FIG. 4
95



SECTOR FORMAT

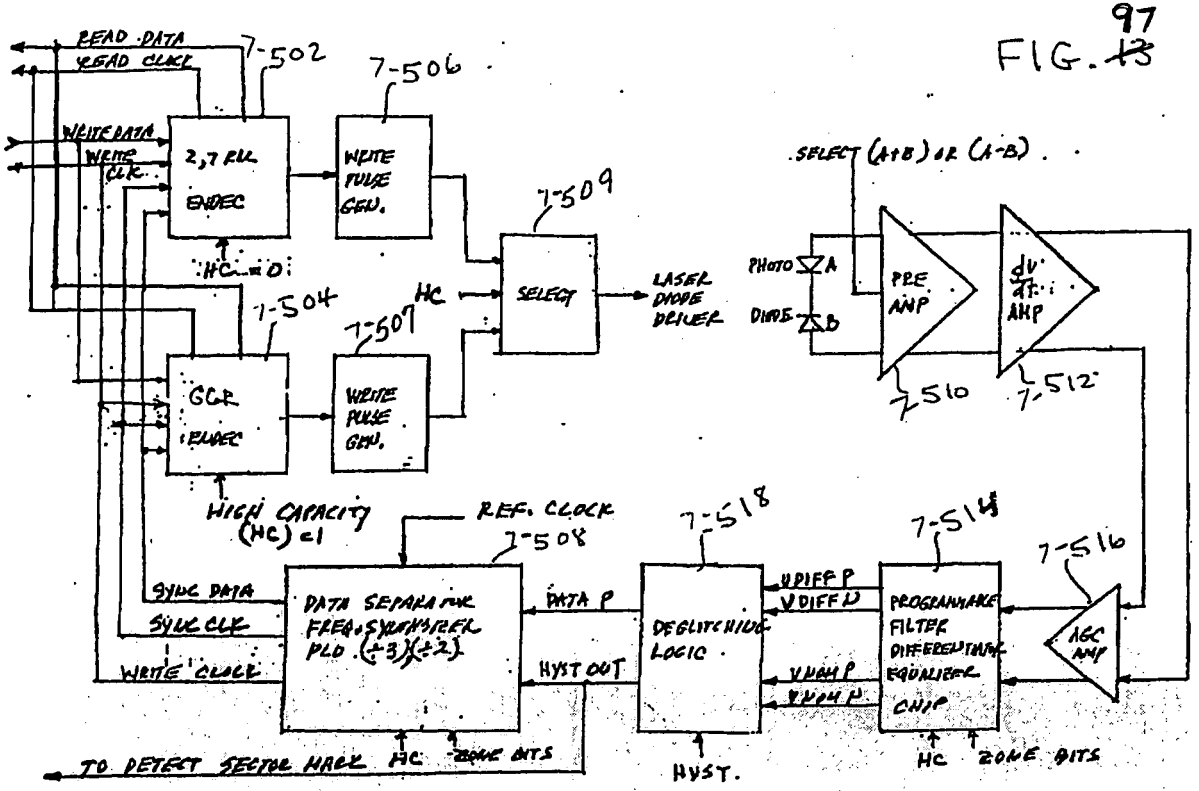
FIG. 9b



* FIXED FREQUENCY
80 CHANNEL BITS

++ 1 BYTE = 9 CHANNEL BITS

FIG. 97



HIGH CAPACITY DRIVE
4400 RPM

ZONE	(ABSOLUTE)	No. OF SECTORS PER TRACK	NO. OF SEC/ZONE	WRITE FREQ. (MHz)
1	0 — 475	40	19040	10.591
2	476 — 951	41	19516	10.852
3	952 — 1427	42	19992	11.117
4	1428 — 1903	43	20468	11.382
5	1904 — 2379	44	20944	11.636
6	2380 — 2855	45	21420	11.963
7	2856 — 3331	46	21896	12.180
8	3332 — 3807	47	22372	12.469
9	3808 — 4283	48	22848	12.705
10	4284 — 4759	49	23324	12.972
11	4760 — 5235	50	23800	13.257
12	5236 — 5711	51	24276	13.533
13	5712 — 6187	52	24752	13.831
14	6188 — 6663	53	25228	14.086
15	6664 — 7139	54	25704	14.362
16	7140 — 7615	56	26180	14.284
17	7616 — 8091	56	26656	14.914
18	8092 — 8567	57	27132	15.130
19	8568 — 9043	58	27608	15.467
20	9044 — 9519	59	28084	15.694
21	9520 — 9999	60	28500	15.950
			TOTAL SEC. 500040	
			X.512 B/S =	
			256.02 MB	

104

FIG. 98
APPENDIX A

CRC FOR ID FIELDS

The 16 check bits of the CRC of the ID field shall be computed over the first three bytes of this field. The generator polynomial shall be:

$G(x) = x^{16} + x^{12} + x^5 + 1.$

The residual polynomial is defined by:

$$R(x) = \left(\sum_{i=8}^{i=23} \overline{b_i}\, x^i + \left(\sum_{i=0}^{i=7} b_i x^i\right)\right) x^{16} \bmod G(x).$$

where b_i denotes a bit of the first three bytes and $\overline{b_i}$ an inverted bit. Bit b_{23} is the highest order bit of the first byte.

The contents of the 16 check bits c_k of the CRC are defined by:

$$R_c(x) = \sum_{k=0}^{k=15} c_k\, x^k.$$

c_{15} is recorded in the highest order bit of the fourth byte in the ID field.

FIG. ~~15~~ 99

~~APPENDIX B~~

8 Bit Byte		Encoded
Hex	Binary	9 Bit Byte
00	00000000	011001011
01	00000001	011001001
02	00000010	001001101
03	00000011	101100011
04	00000100	011001010
05	00000101	101100101
06	00000110	101100110
07	00000111	101100111
08	00001000	011001111
09	00001001	101101001
0A	00001010	101101010
0B	00001011	101101011
0C	00001100	011001110
0D	00001101	101101101
0E	00001110	101101110
0F	00001111	101101111
10	00010000	001001011
11	00010001	001001001
12	00010010	011001101
13	00010011	100100011
14	00010100	001001010
15	00010101	100100101
16	00010110	100100110
17	00010111	100100111
18	00011000	001001111
19	00011001	100101001
1A	00011010	100101010
1B	00011011	100101011
1C	00011100	001001110
1D	00011101	100101101
1E	00011110	100101110
1F	00011111	100101111
20	00100000	101001111
21	00100001	101001101
22	00100010	001010010
23	00100011	001010011
24	00100100	101001110
25	00100101	001010101
26	00100110	001010110
27	00100111	001010111
28	00101000	101001011
29	00101001	001011001
2A	00101010	001011010
2B	00101011	001011011
2C	00101100	101001010
2D	00101101	001011101
2E	00101110	001011110
2F	00101111	001011111
30	00110000	011100011
31	00110001	001100011
32	00110010	001110010
33	00110011	001110011
34	00110100	010100011
35	00110101	001110101
36	00110110	001110110
37	00110111	001110111
38	00111000	111100011
39	00111001	001111001
3A	00111010	001111010
3B	00111011	001111011
3C	00111100	110100011
3D	00111101	001111101
3E	00111110	001111110
3F	00111111	001111111
Position in byte	8 1	9 1

8 bit Byte		Encoded
Hex	Binary	9 Bit Byte
40	01000000	010001011
41	01000001	010001001
42	01000010	010010010
43	01000011	010010011
44	01000100	010001010
45	01000101	010010101
46	01000110	010010110
47	01000111	010011111
48	01001000	010001111
49	01001001	010011001
4A	01001010	010011010
4B	01001011	010011011
4C	01001100	010001110
4D	01001101	010011101
4E	01001110	010011110
4F	01001111	010011111
50	01010000	011100101
51	01010001	001100101
52	01010010	010110010
53	01010011	010110011
54	01010100	010100101
55	01010101	010110101
56	01010110	010110110
57	01010111	010110111
58	01011000	111100101
59	01011001	010111001
5A	01011010	010111010
5B	01011011	010111011
5C	01011100	110100101
5D	01011101	010111101
5E	01011110	010111110
5F	01011111	010111111
60	01100000	011100110
61	01100001	001100110
62	01100010	011010010
63	01100011	011010011
64	01100100	010100110
65	01100101	011010101
66	01100110	011010110
67	01100111	011010111
68	01101000	111100110
69	01101001	011011001
6A	01101010	011011010
6B	01101011	011011011
6C	01101100	110100110
6D	01101101	011011101
6E	01101110	011011110
6F	01101111	011011111
70	01110000	011100111
71	01110001	001100111
72	01110010	011110010
73	01110011	011110011
74	01110100	010100111
75	01110101	011110101
76	01110110	011110110
77	01110111	011110111
78	01111000	111100111
79	01111001	011111001
7A	01111010	011111010
7B	01111011	011111011
7C	01111100	110100111
7D	01111101	011111101
7E	01111110	011111110
7F	01111111	011111111
Position in byte	8 1	9 1

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FIG. ~~16a~~ 100a

APPENDIX C

8-Bit Byte		Encoded
Hex	Binary	9-Bit Byte
80	10000000	111001011
81	10000001	111001001
82	10000010	100010010
83	10000011	100010011
84	10000100	111001010
85	10000101	100010101
86	10000110	100010110
87	10000111	100010111
88	10001000	111001111
89	10001001	100011001
8A	10001010	100011010
8B	10001011	100011011
8C	10001100	111001110
8D	10001101	100011101
8E	10001110	100011110
8F	10001111	100011111
90	10010000	011101001
91	10010001	001101001
92	10010010	100110010
93	10010011	100110011
94	10010100	010101001
95	10010101	100110101
96	10010110	100110110
97	10010111	100110111
98	10011000	111101001
99	10011001	100111001
9A	10011010	100111010
9B	10011011	100111011
9C	10011100	110101001
9D	10011101	100111101
9E	10011110	100111110
9F	10011111	100111111
A0	10100000	011101010
A1	10100001	001101010
A2	10100010	101010010
A3	10100011	101010011
A4	10100100	010101010
A5	10100101	101010101
A6	10100110	101010110
A7	10100111	101010111
A8	10101000	111101010
A9	10101001	101011001
AA	10101010	101011010
AB	10101011	101011011
AC	10101100	110101010
AD	10101101	101011101
AE	10101110	101011110
AF	10101111	101011111
B0	10110000	011101011
B1	10110001	001101011
B2	10110010	101110010
B3	10110011	101110011
B4	10110100	010101011
B5	10110101	101110101
B6	10110110	101110110
B7	10110111	101110111
B8	10111000	111101011
B9	10111001	101111001
BA	10111010	101111010
BB	10111011	101111011
BC	10111100	110101011
BD	10111101	101111101
BE	10111110	101111110
BF	10111111	101111111
Position in byte	8 1	9 1

8-bit Byte		Encoded
Hex	Binary	9-Bit Byte
C0	11000000	110001011
C1	11000001	110001001
C2	11000010	110010010
C3	11000011	110010011
C4	11000100	110001010
C5	11000101	110010101
C6	11000110	110010110
C7	11000111	110010111
C8	11001000	110001111
C9	11001001	110011001
CA	11001010	110011010
CB	11001011	110011011
CC	11001100	110001110
CD	11001101	110011101
CE	11001110	110011110
CF	11001111	110011111
D0	11010000	011101101
D1	11010001	001101101
D2	11010010	110110010
D3	11010011	110110011
D4	11010100	010101101
D5	11010101	110110101
D6	11010110	110110110
D7	11010111	110110111
D8	11011000	111101101
D9	11011001	110111001
DA	11011010	110111010
DB	11011011	110111011
DC	11011100	110101101
DD	11011101	110111101
DE	11011110	110111110
DF	11011111	110111111
E0	11100000	011101110
E1	11100001	001101110
E2	11100010	111010010
E3	11100011	111010011
E4	11100100	010101110
E5	11100101	111010101
E6	11100110	111010110
E7	11100111	111010111
E8	11101000	111101110
E9	11101001	111011001
EA	11101010	111011010
EB	11101011	111011011
EC	11101100	110101110
ED	11101101	111011101
EE	11101110	111011110
EF	11101111	111011111
F0	11110000	011101111
F1	11110001	001101111
F2	11110010	111110010
F3	11110011	111110011
F4	11110100	010101111
F5	11110101	111110101
F6	11110110	111110110
F7	11110111	111110111
F8	11111000	111101111
F9	11111001	111111001
FA	11111010	111111010
FB	11111011	111111011
FC	11111100	110101111
FD	11111101	111111101
FE	11111110	111111110
FF	11111111	111111111
Position in byte	8 1	9 1

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FIG. ~~1C~~ 100b

~~APPENDIX C~~

FIG. 101

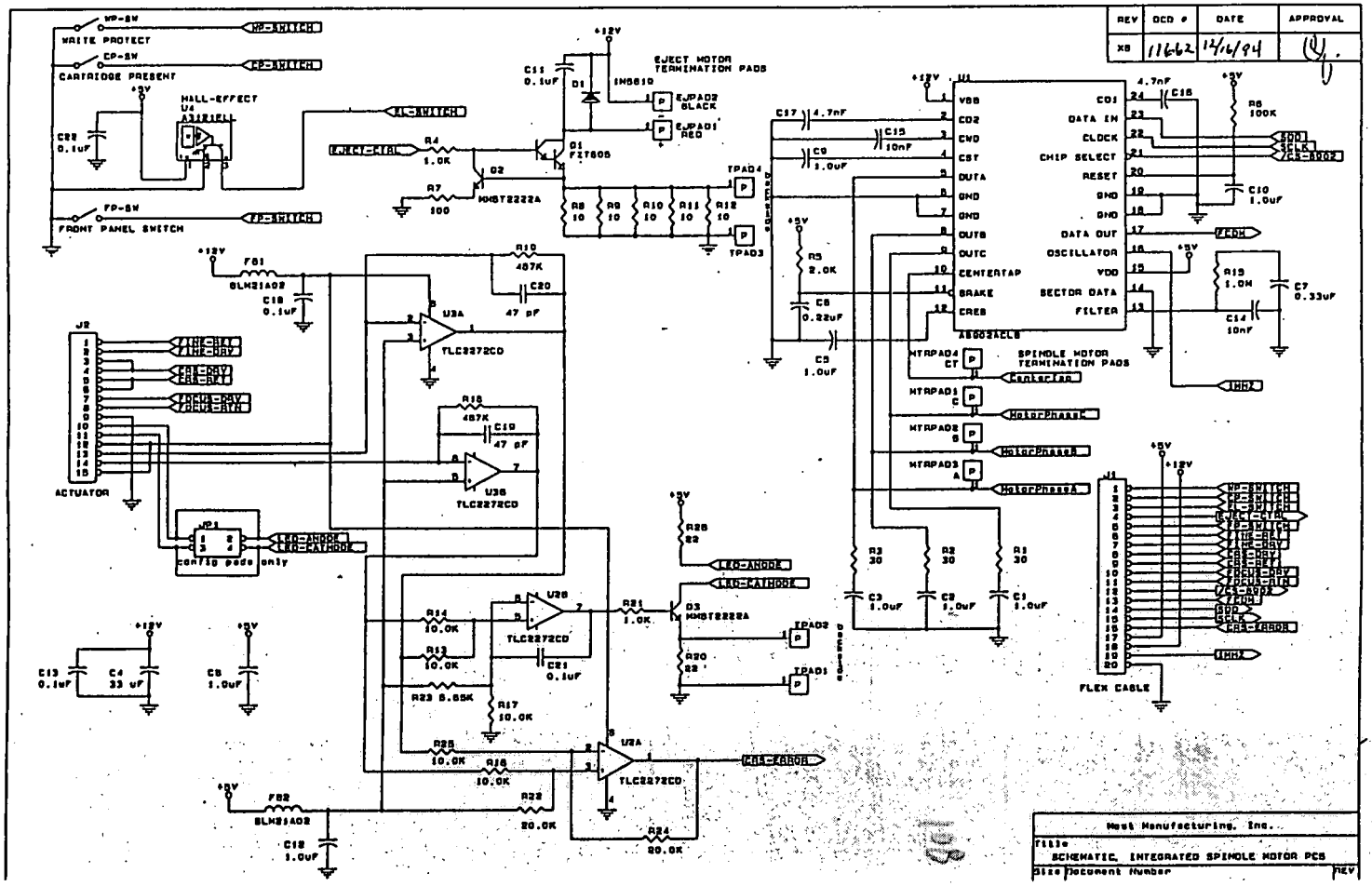

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FIG. 102

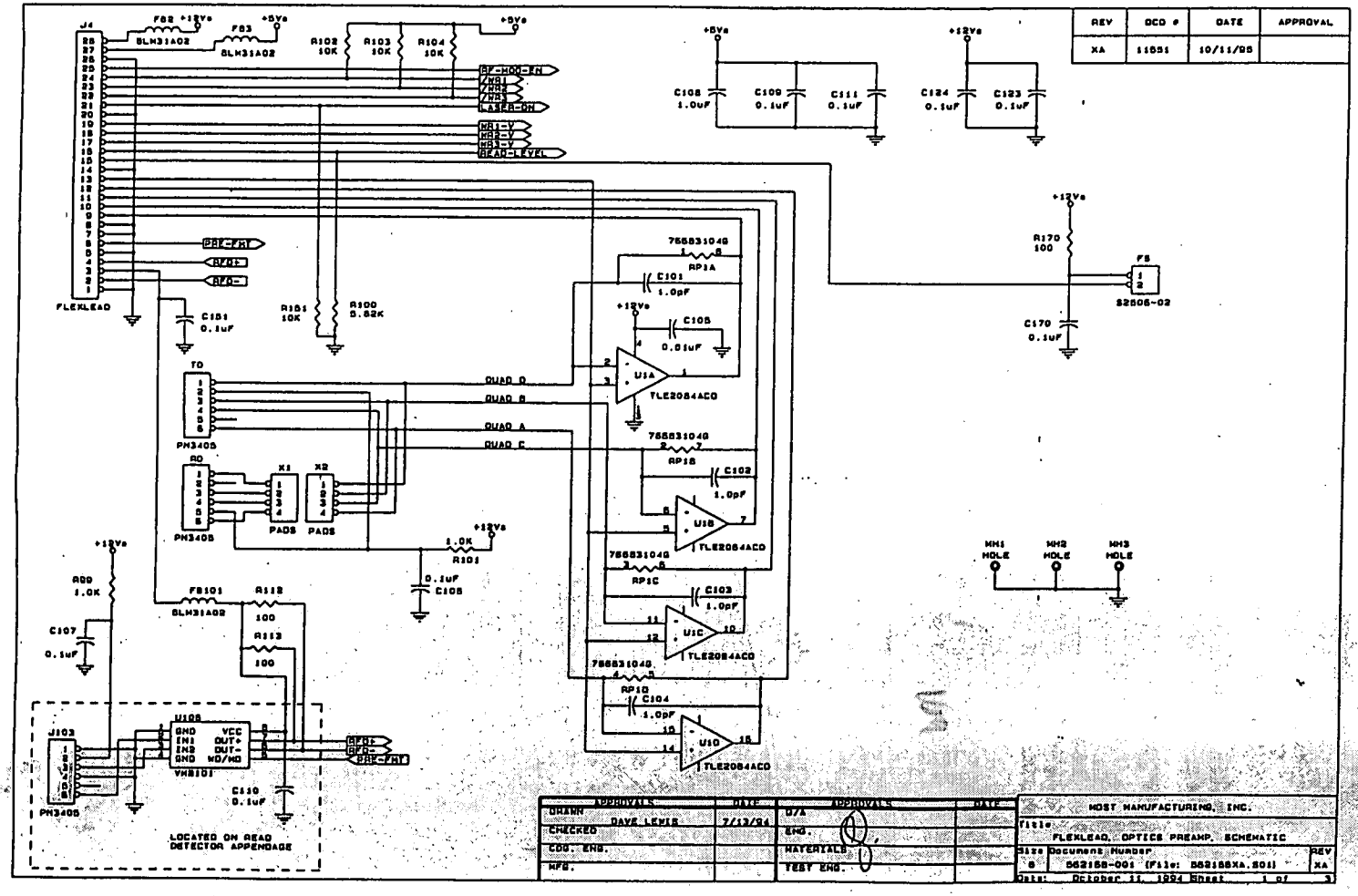

FIG. 103

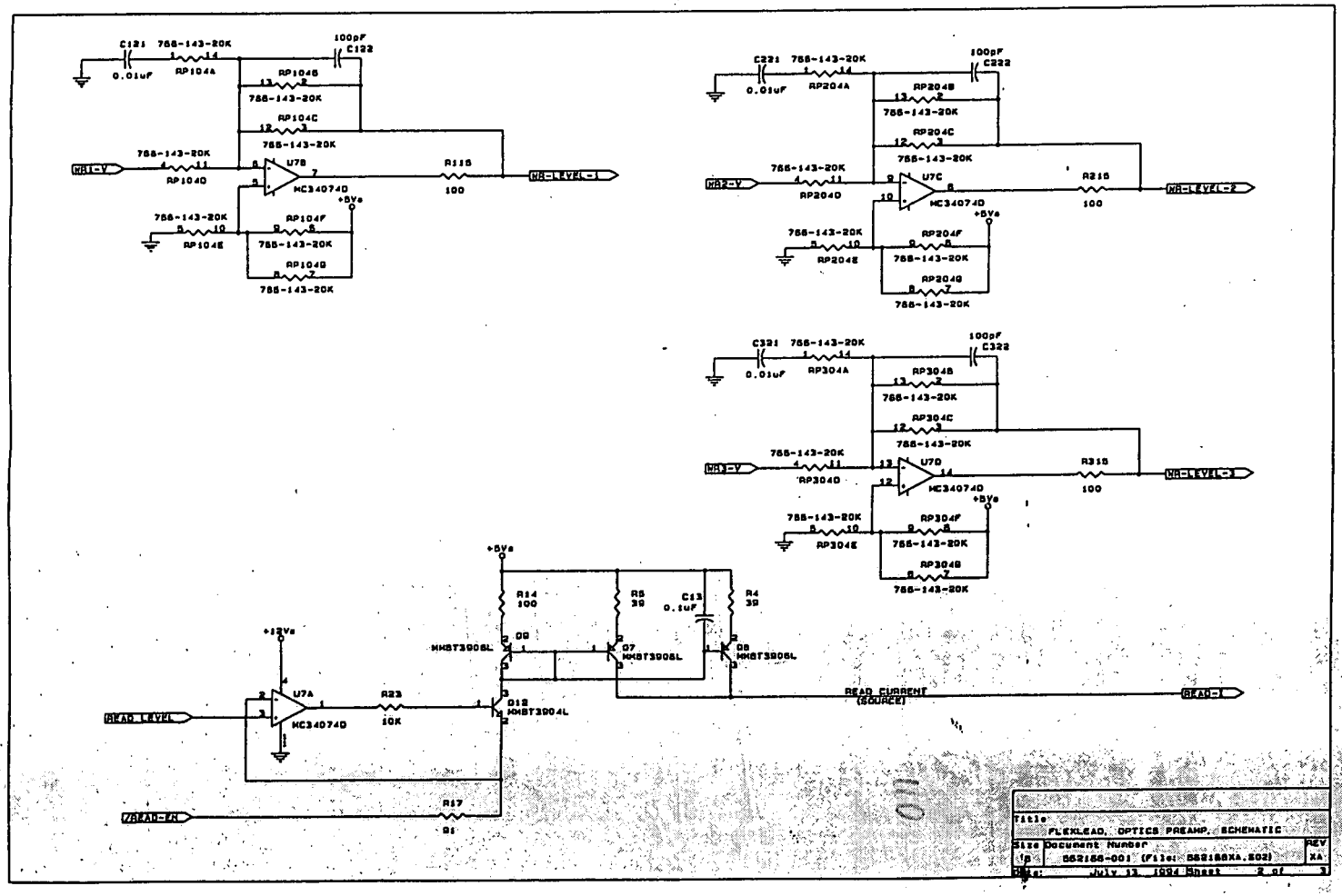

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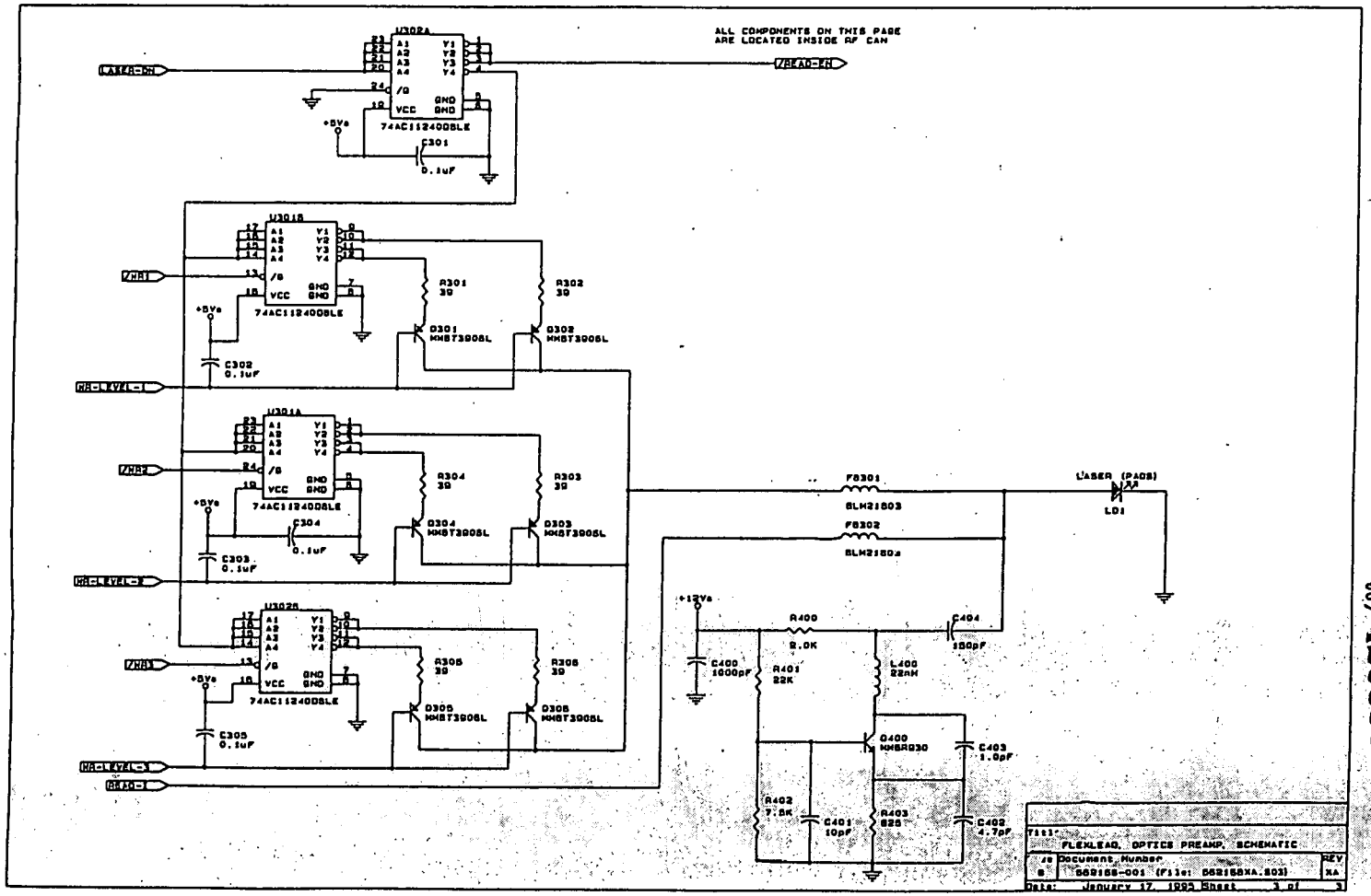
FIG. 104



08/420899

FIG. 105

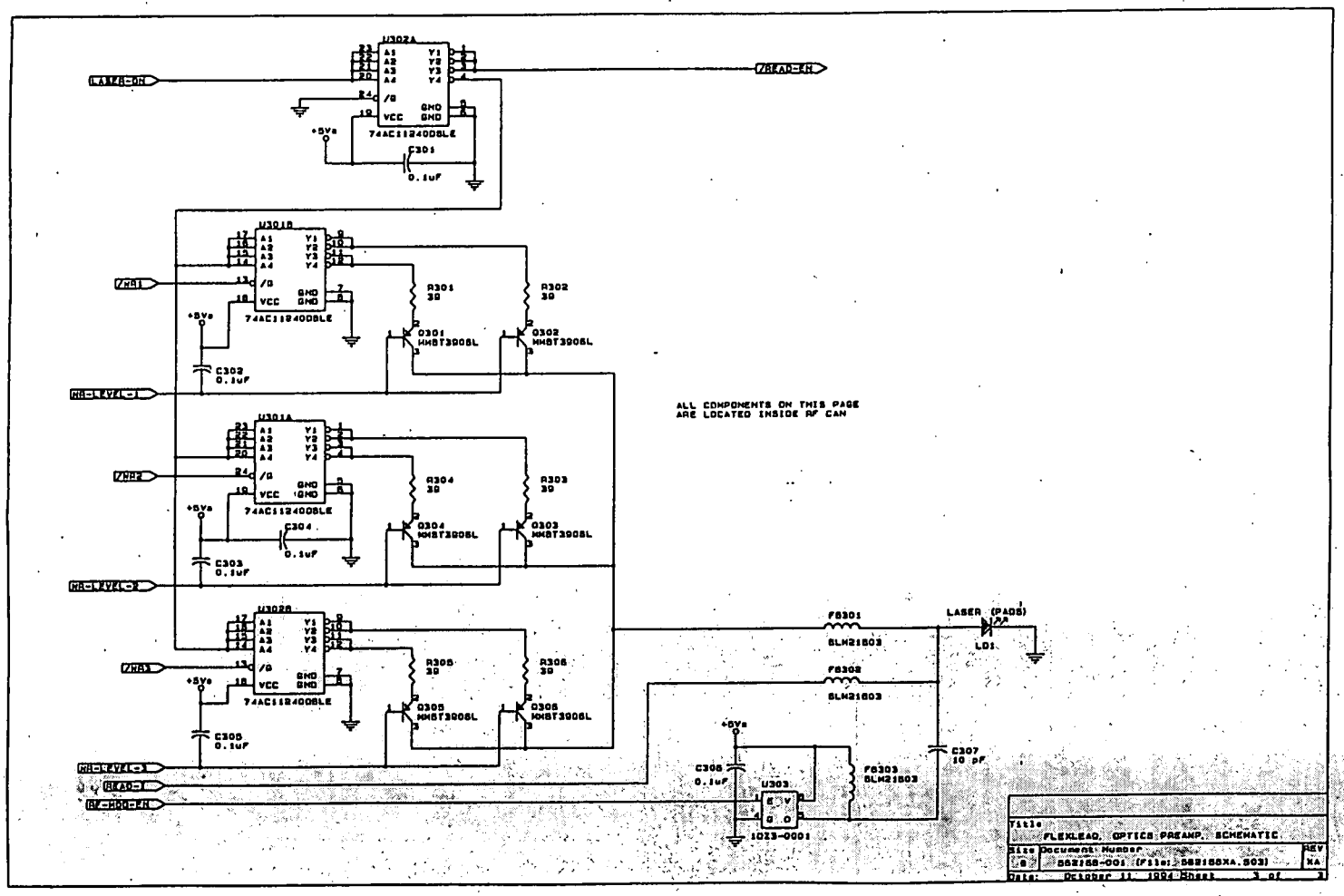


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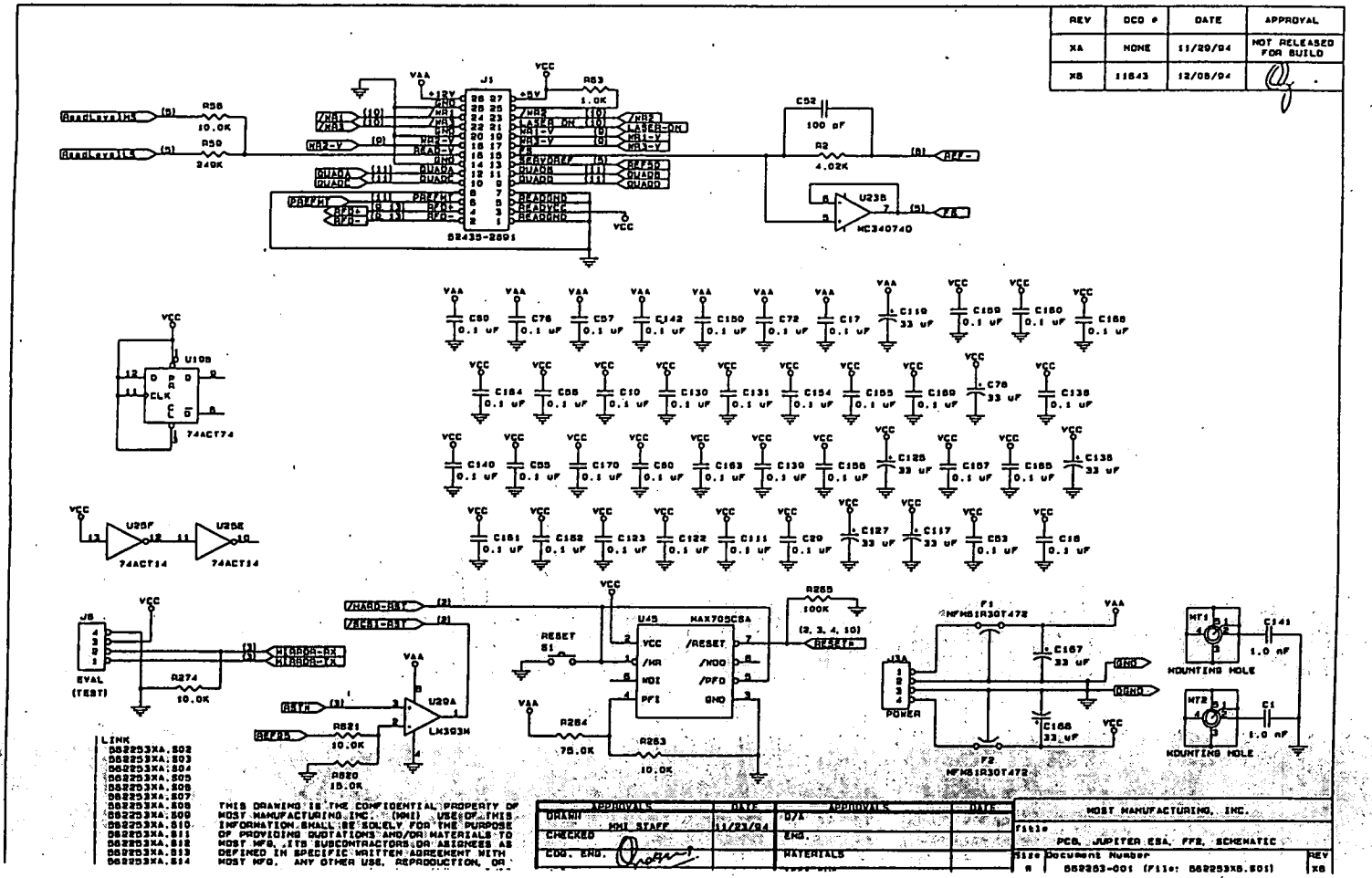
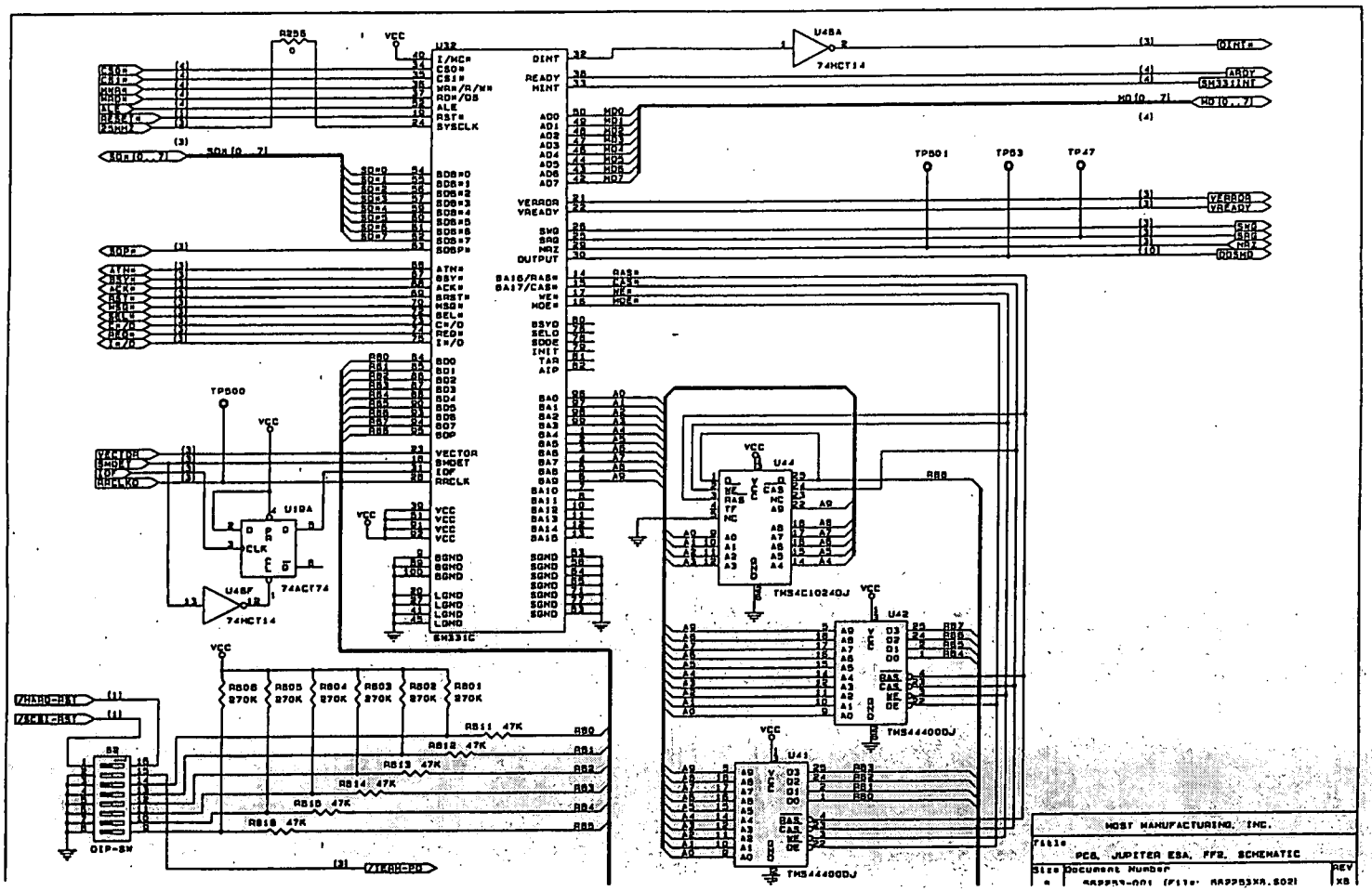
FIG. 106



08/420899

FIG. 107



08/420899

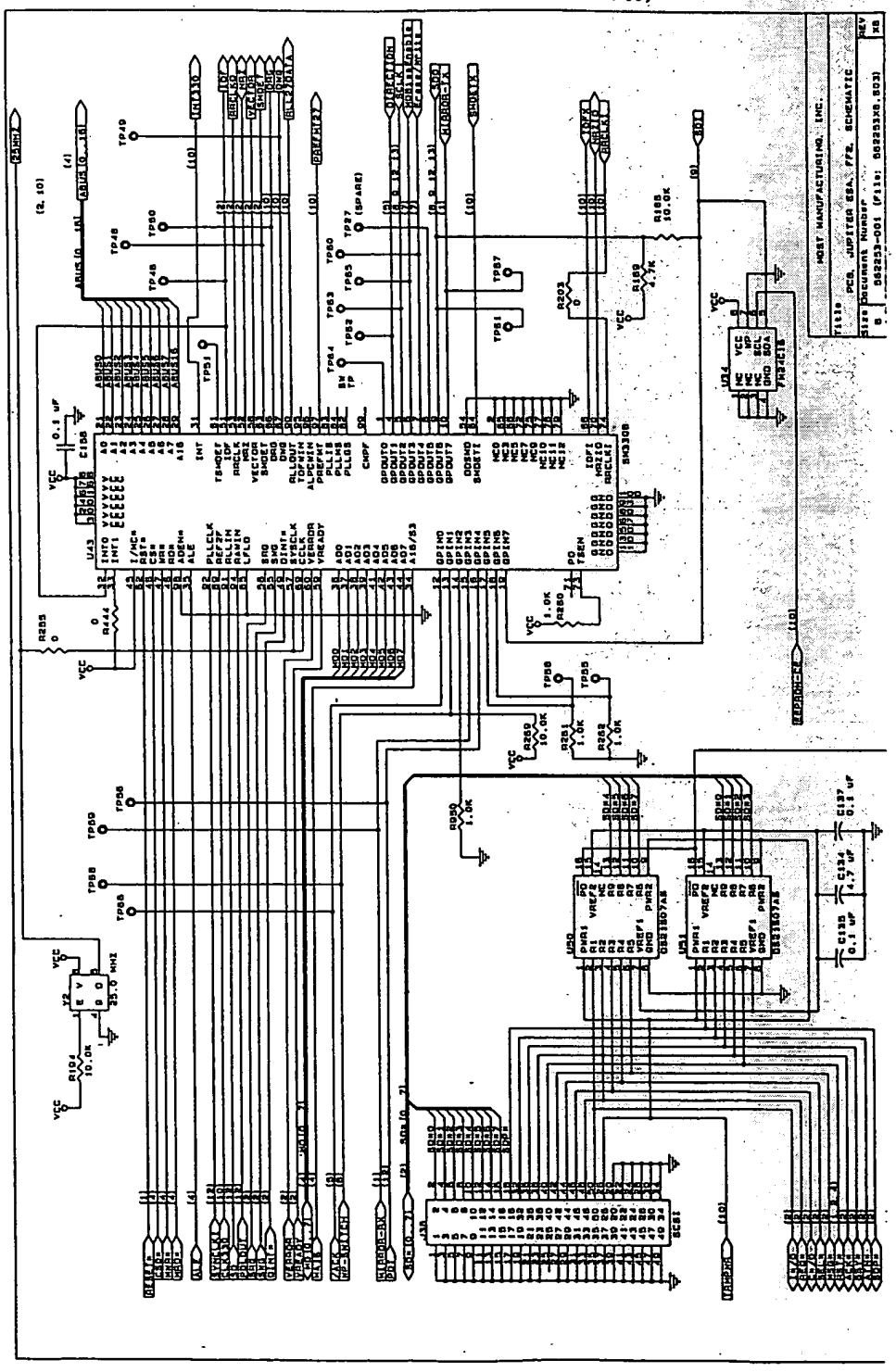


FIG 108

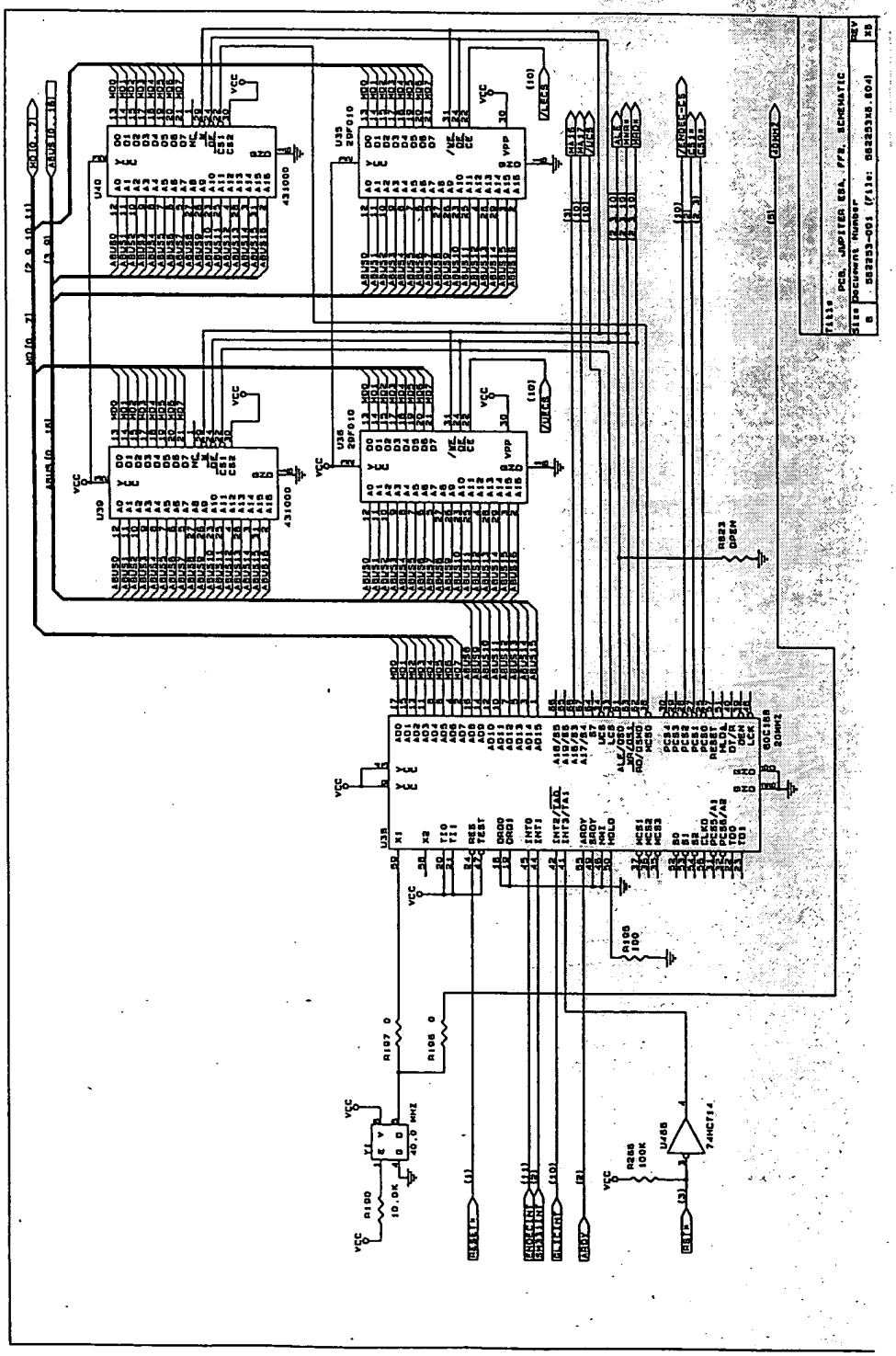


Fig. 109

FIG. 110

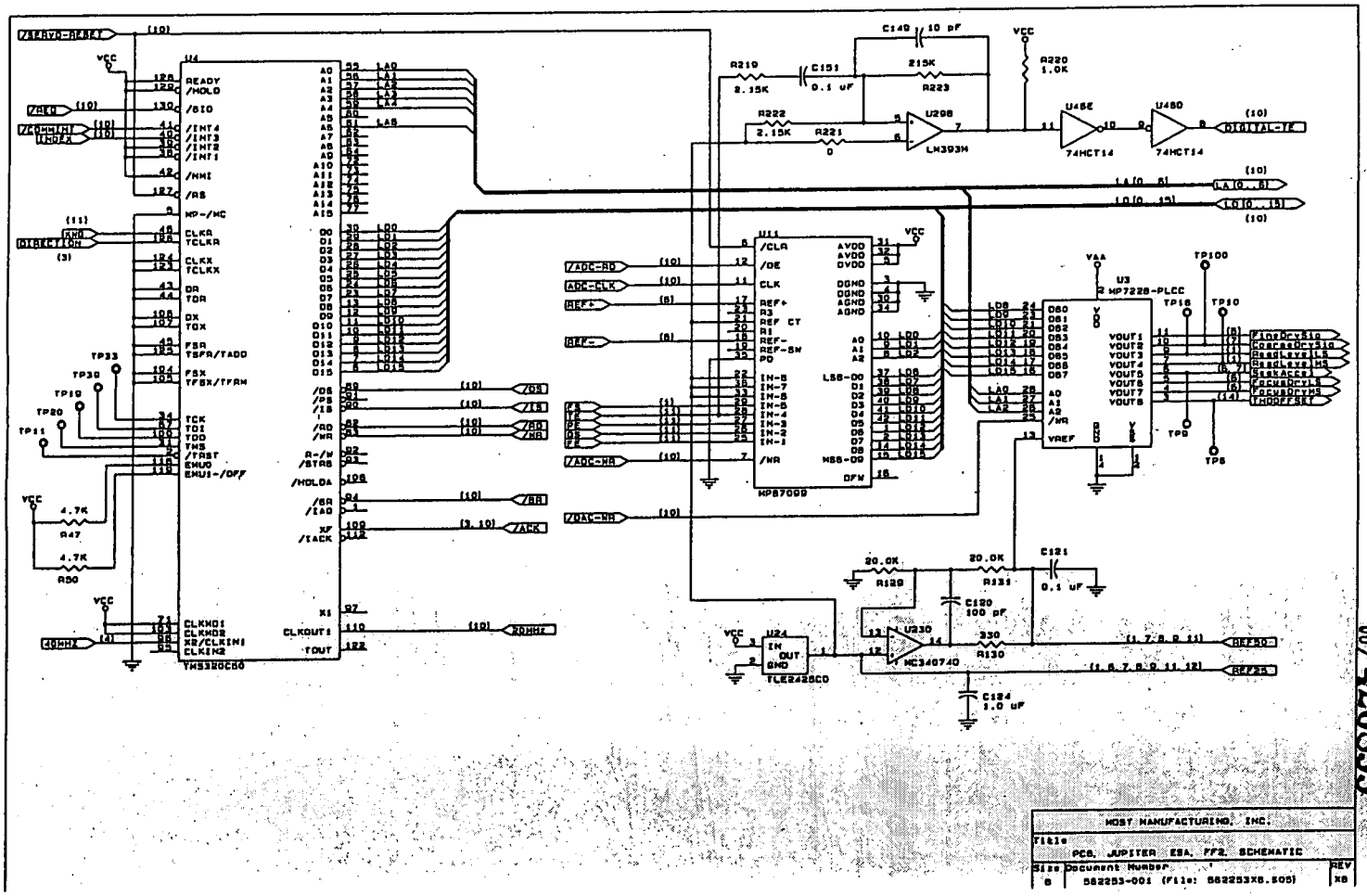


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FIG. 111

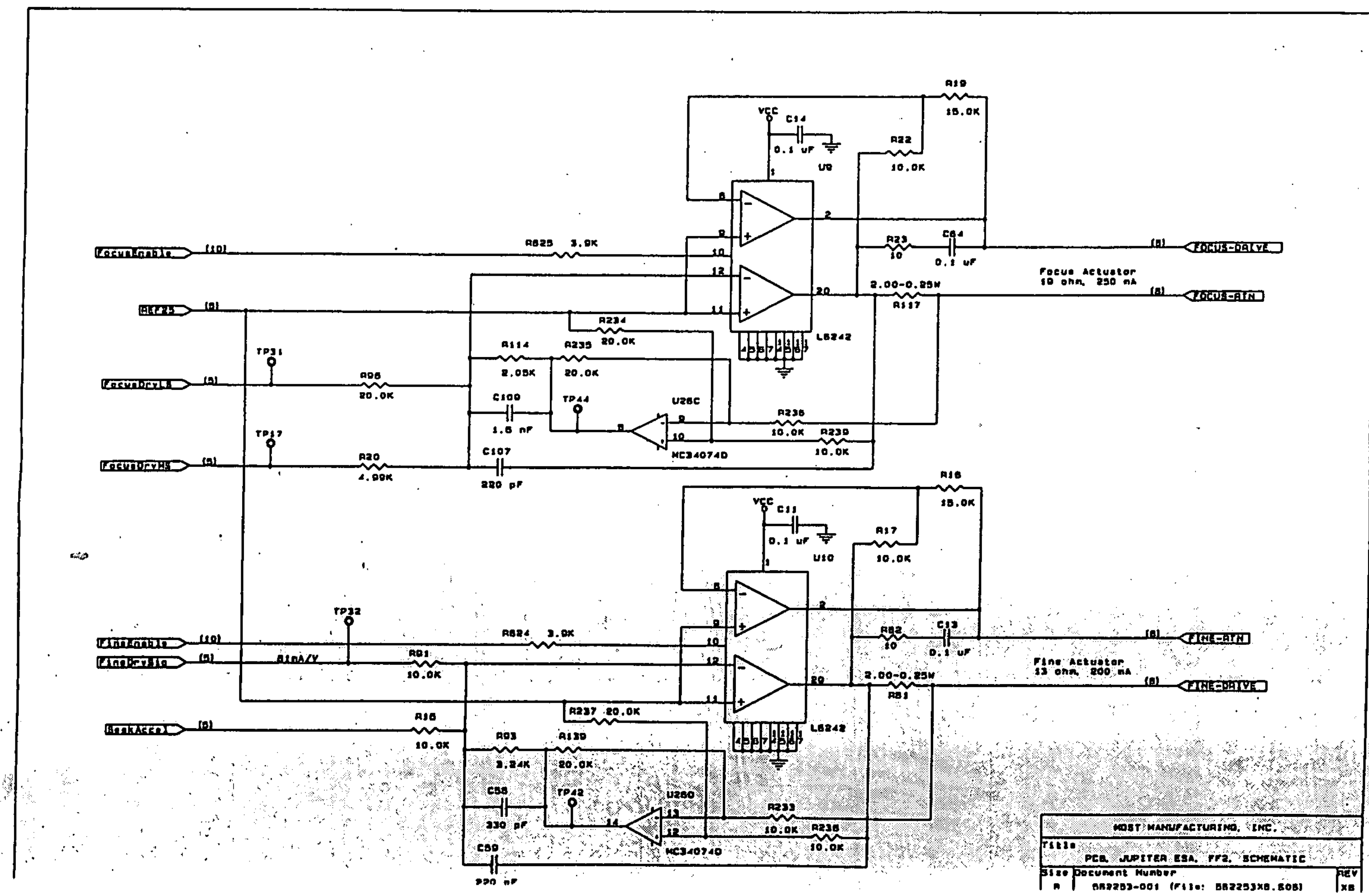

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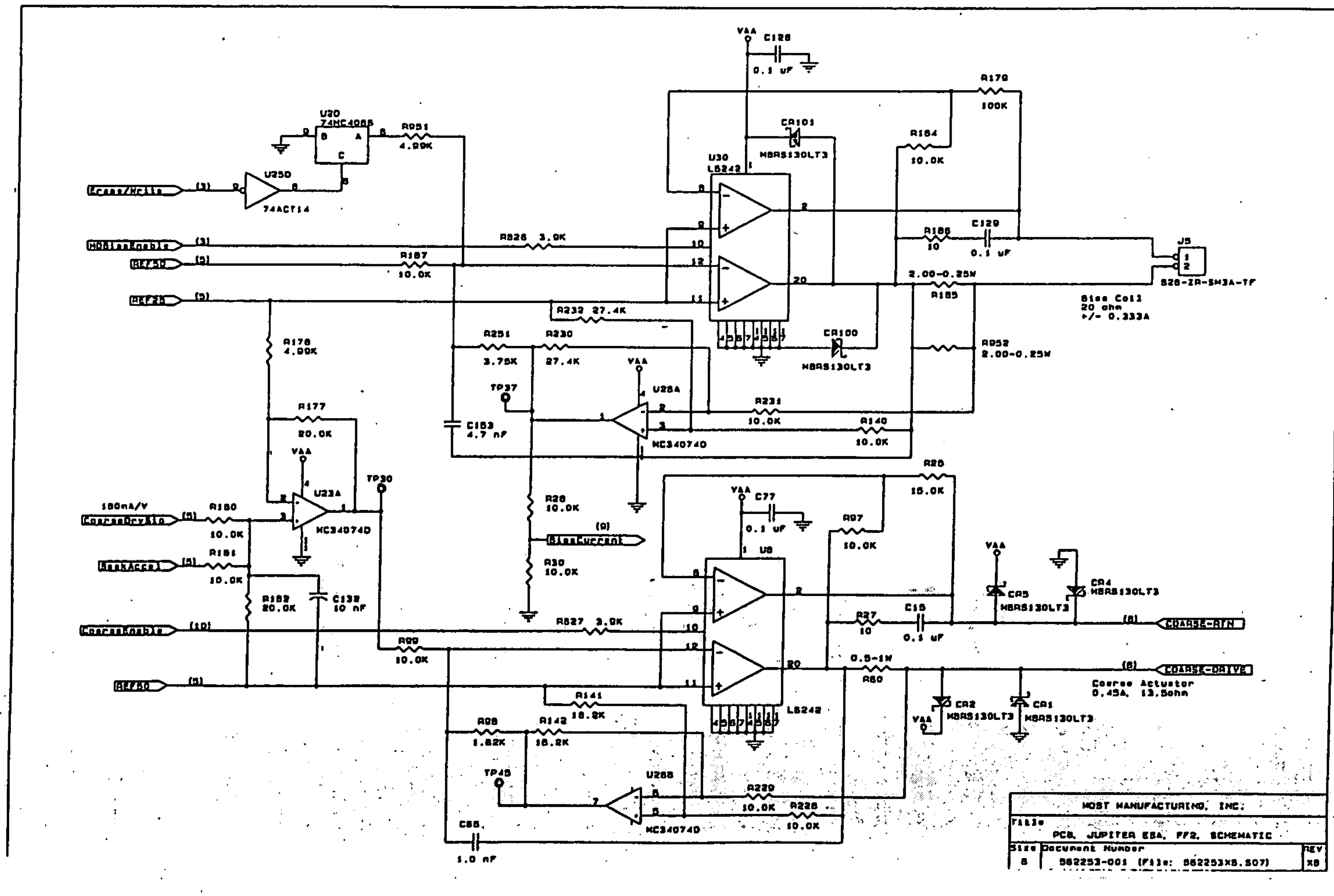
FIG 112



08/420899

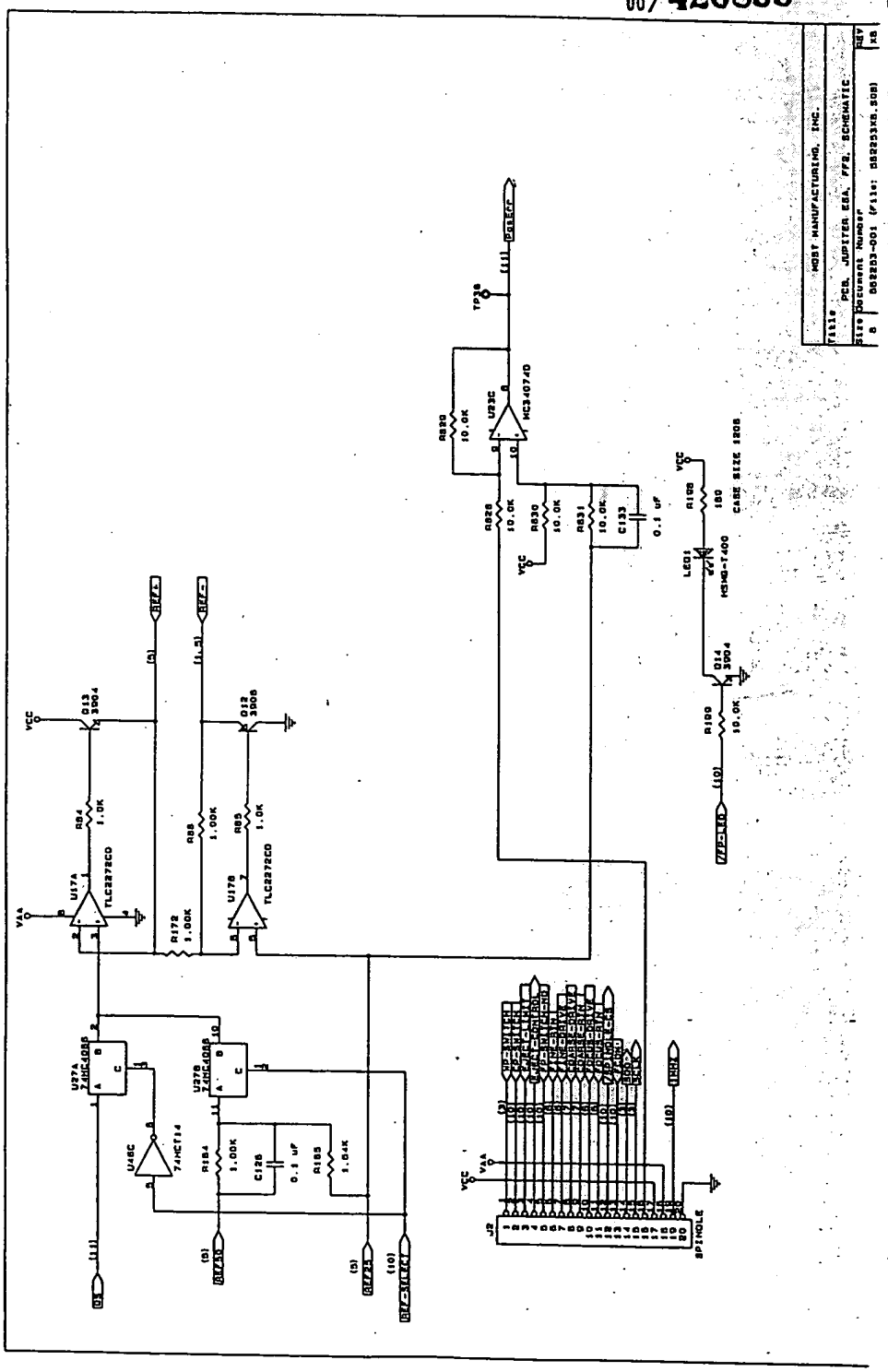

MOSY MANUFACTURING, INC.
Title PCB, JUPITER ERA, FPB, SCHEMATIC
Size Document Number 002253-001 (File: 002253X8.SCH) REV A8
VCC
VAA
U17A TLC2272CD
U17B TLC2272CD
U27A 74HC4066
U27B 74HC4066
U48C 74HCT14
U23C MC34074D
R84 1.00K
R85 1.00K
R88 1.00K
R172 1.00K
R184 1.00K
R185 1.04K
R829 10.0K
R830 10.0K
R831 10.0K
R189 10.0K
R190 180
C125 0.1 uF
C133 0.1 uF
D12 3904
D13 3904
D14 3904
LED1 MSMD-7400
CASE SIZE 1206
TP28
PSIECF
BRF-
BRF+
BRF-SYNC
J2
SPINDLE

Fig. 113

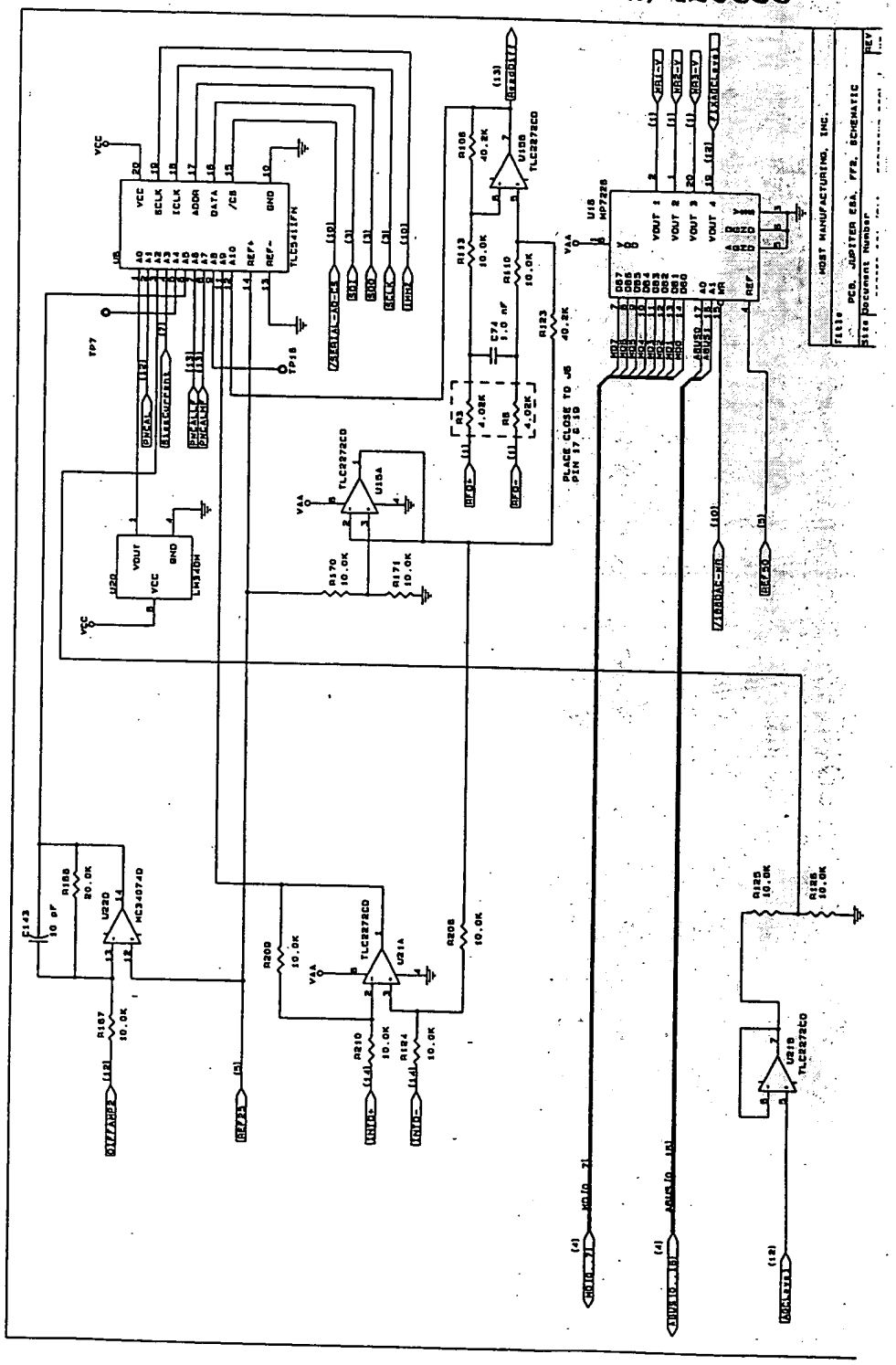
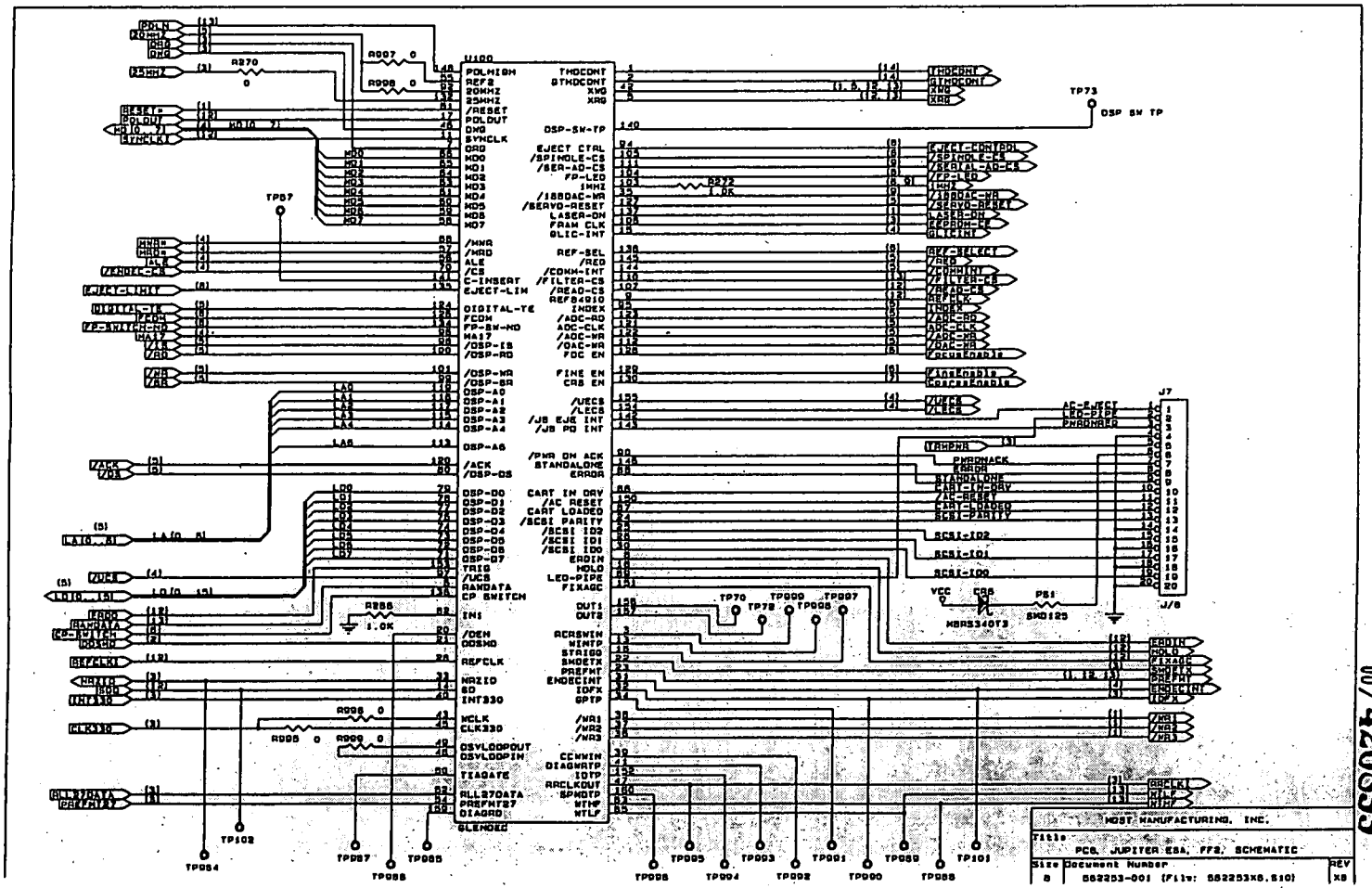

HOST MANUFACTURING, INC.
PCB, JUPITER EBA, FFB, SCHEMATIC
TLC5411FN
TLC2272CD
MC34074D
MP7226
LM340M
PLACE CLOSE TO U5 PIN 17 & 19

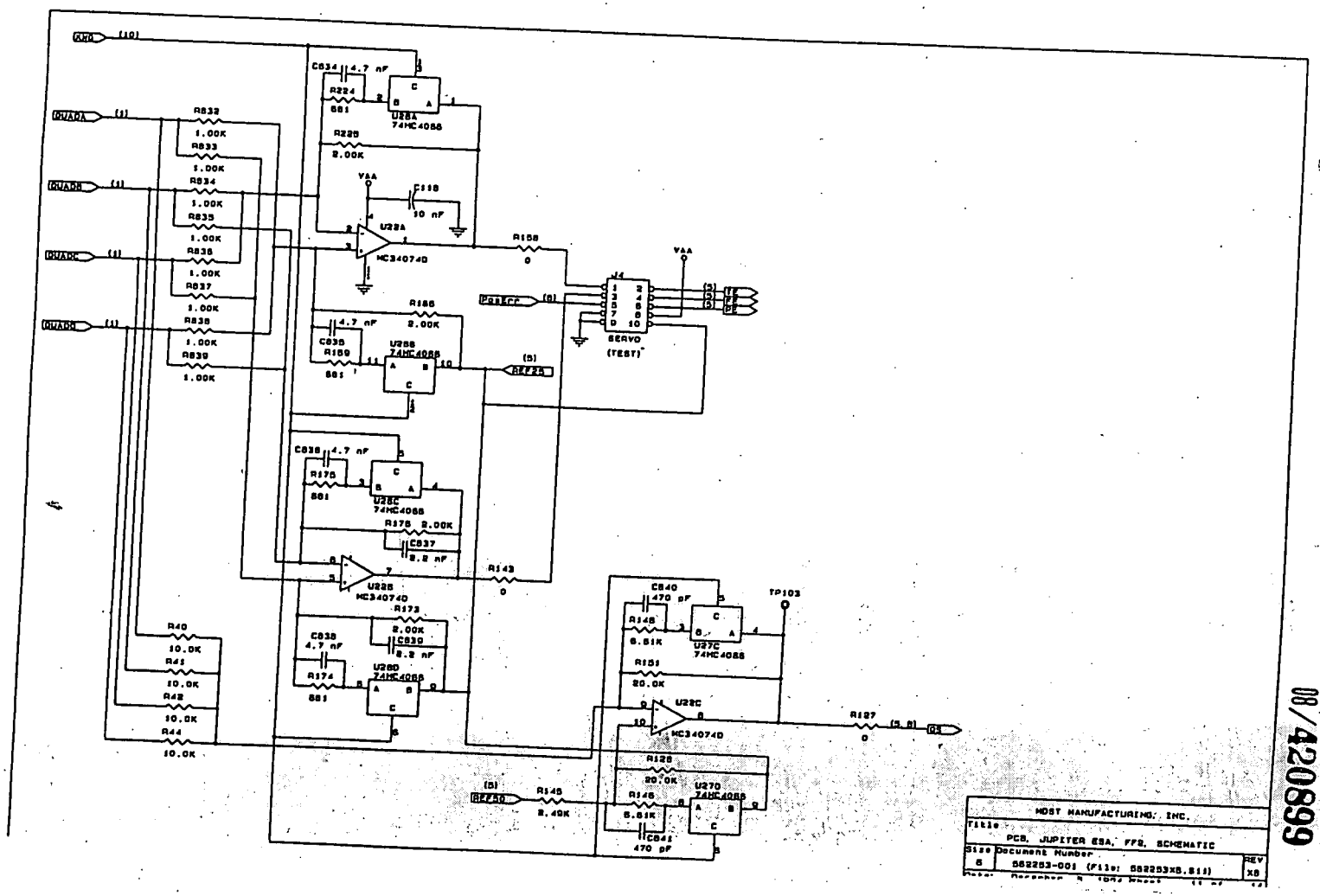
FIG. 114

FIG. 115



08/420899

FIG. 116



08/420899

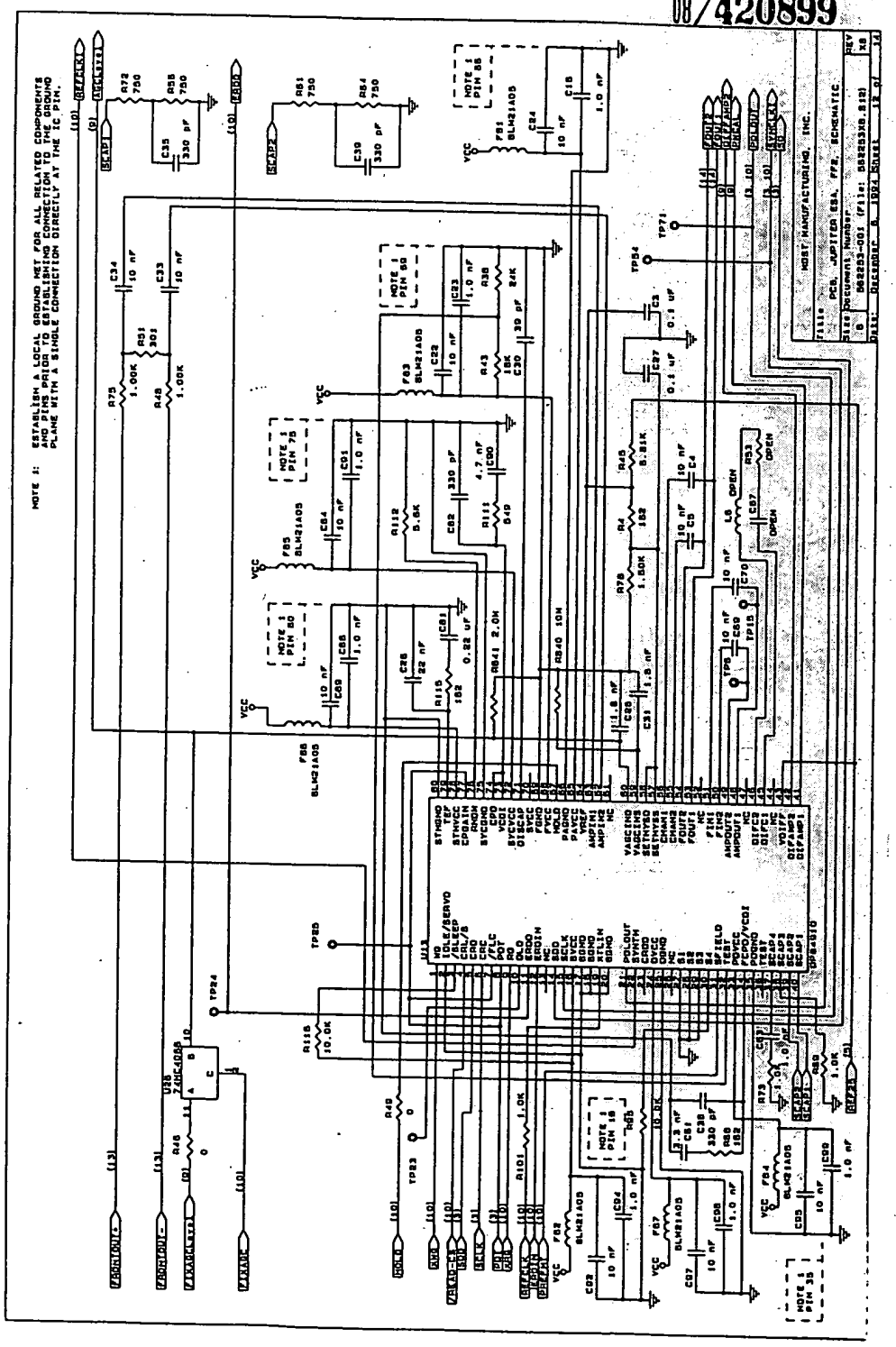
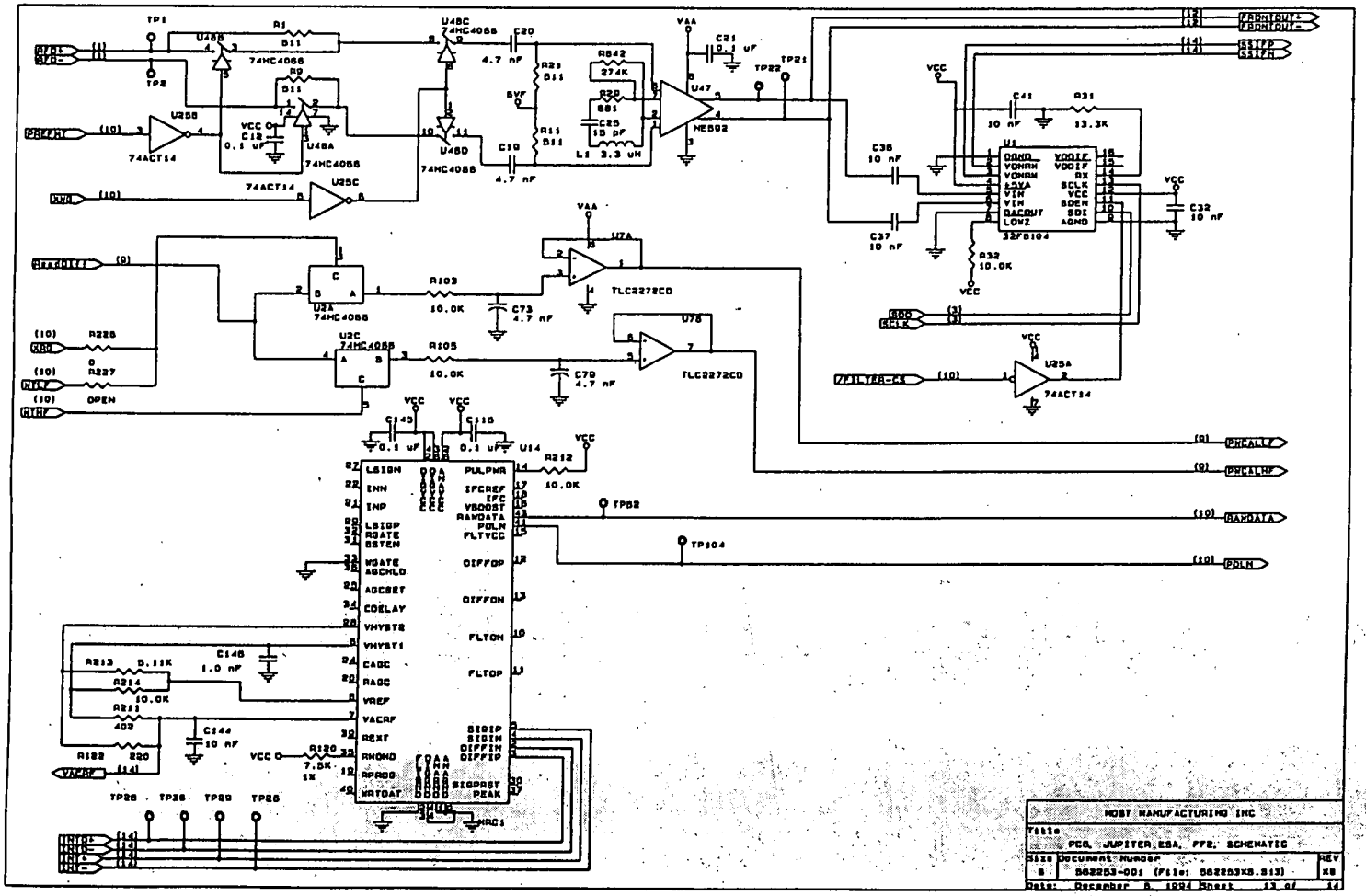


FIG. 117

FIG. 118



08/420899

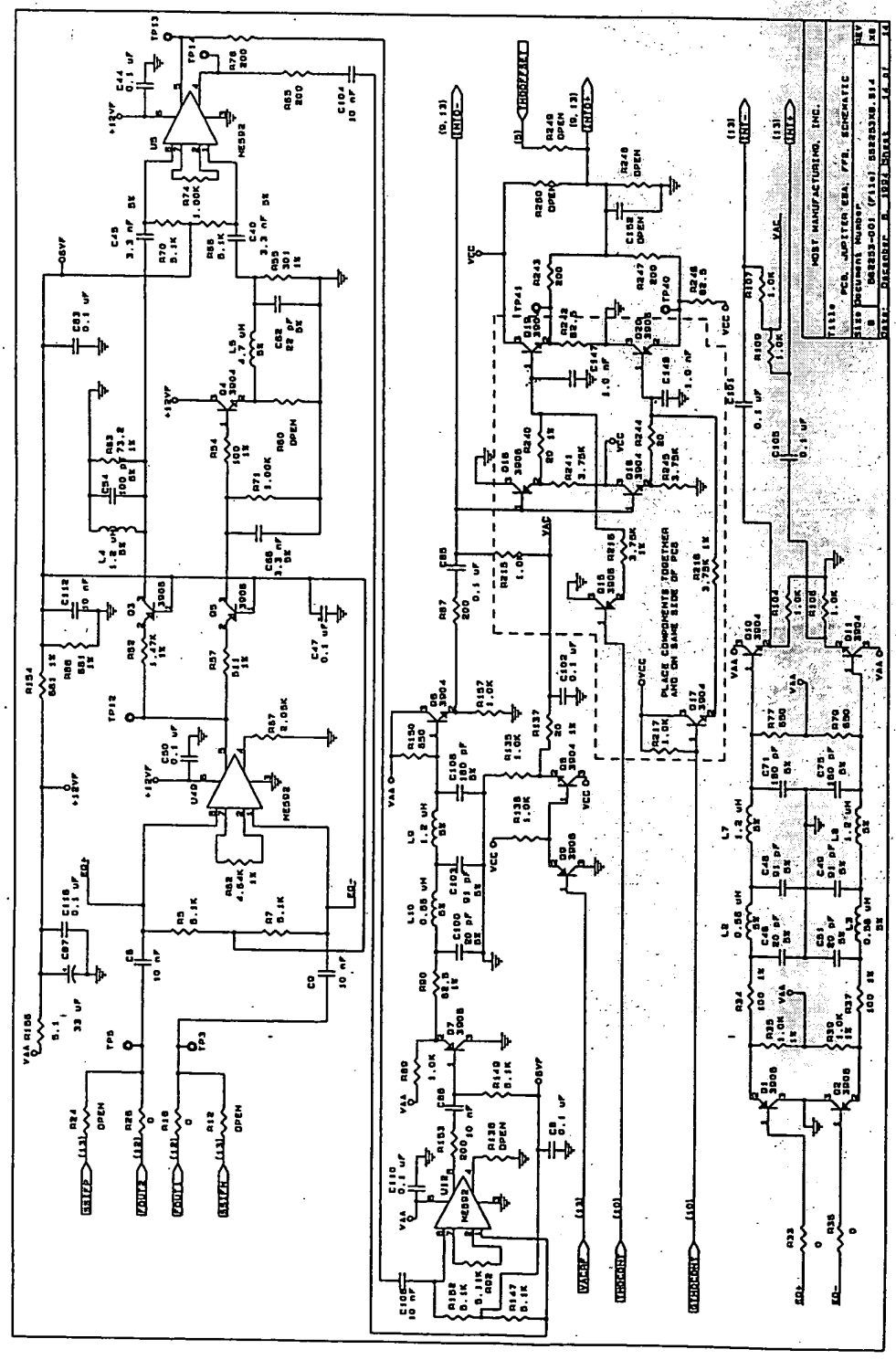


Fig. 119

CRASH STOP/POLE PIECE ISOLATOR 8/18/93 MD

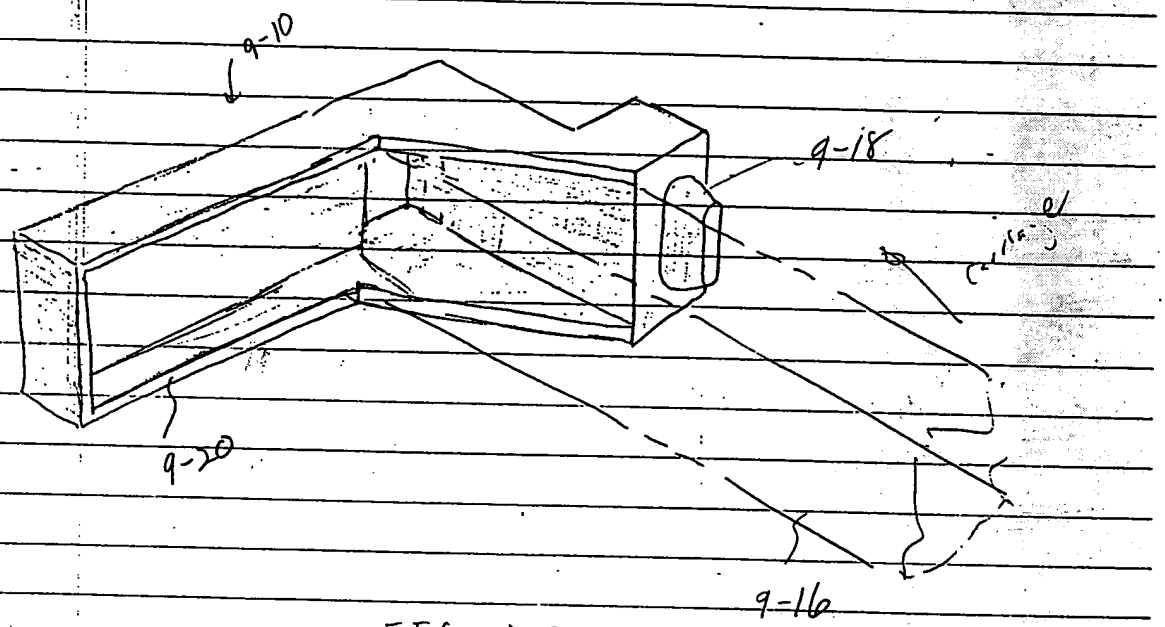
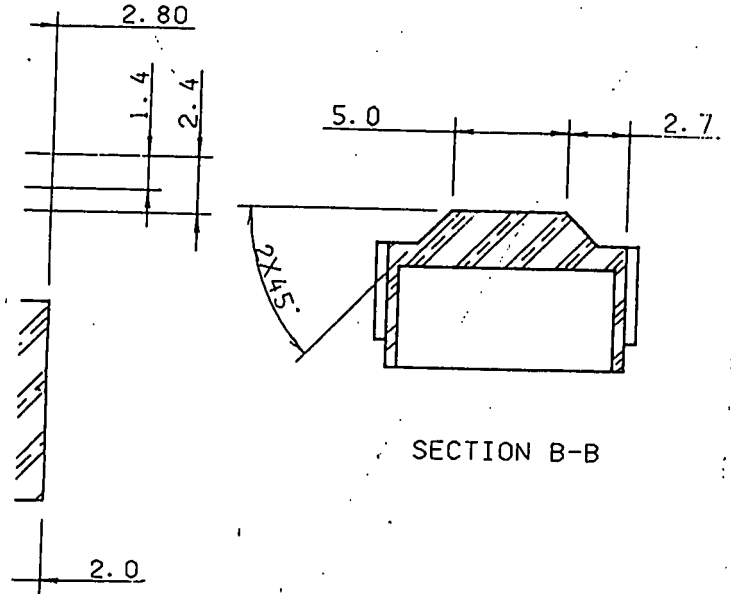


FIG. 12D

SYM	DCO NO.	DATE	APPVL
A	ECO 11671	12/21/94	



SECTION B-B

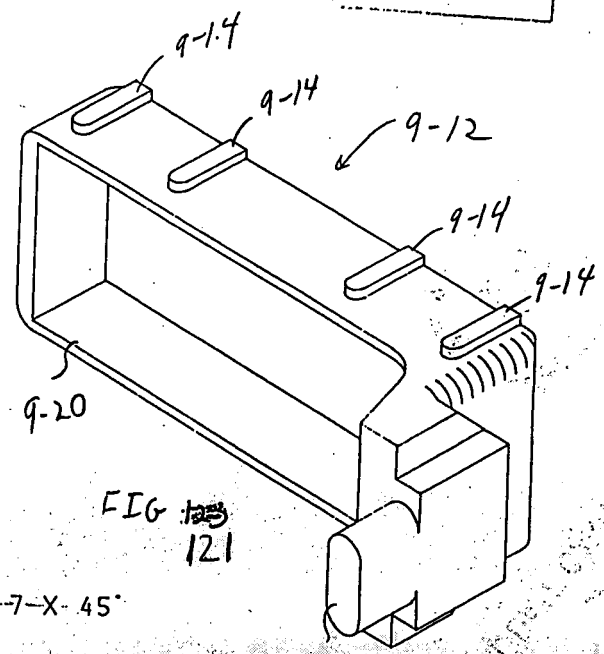


FIG ~~125~~ 121